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Inventor : **Nepote, Andrea**  
**Via Baldissero, 2**  
**I-10131 Torino (IT)**

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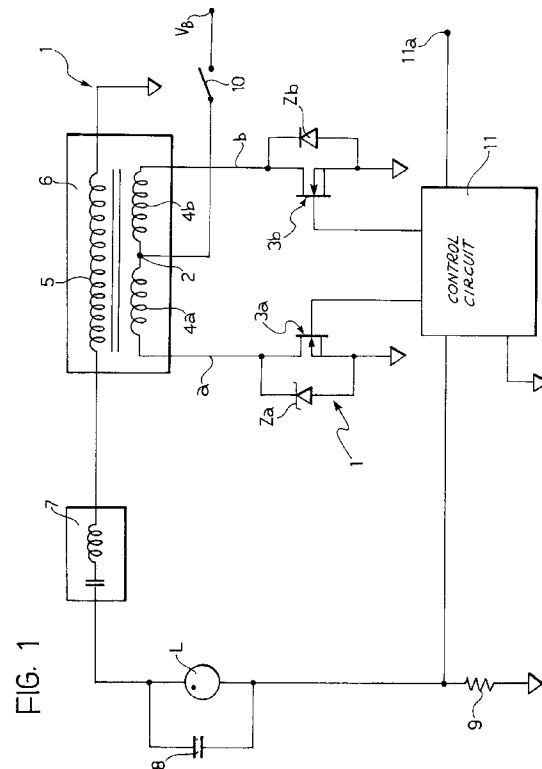
Representative : **Quinterno, Giuseppe et al**  
**c/o Jacobacci-Casetta & Perani S.p.A. Via**  
**Alfieri, 17**  
**I-10121 Torino (IT)**

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Applicant : **MARELLI AUTRONICA S.p.A.**  
**Via Griziotti 4**  
**I-20145 Milano (IT)**

**Self-oscillating circuit for driving a gas discharge lamp, particularly for use on board a motor vehicle.**

The circuit comprises a capacitor (CO) which is alternately discharged and charged in synchronism with the signal which develops across the terminals of a resistor (9) operating to detect the current in the lamp (L). A sawtooth wave form (D) is thus obtained, which is compared with a threshold level ( $S_a$ ) which is varied in dependence on the current which it is necessary to provide to the lamp (L). On the basis of this comparison logic signals (J, K) are produced which are utilised to control the power switches (3a, 3b) of the inverter (1) which controls the lamp (L).



The present invention relates to a self-oscillating circuit for driving a gas discharge lamp, particularly for use in motor vehicle headlights or spotlights.

More specifically, the invention relates to a self-oscillating circuit comprising

- an inverter having two circuit branches connectable to a DC voltage source and including respective electronic switching means in series with a respective primary winding; the primary windings of the said circuit branches being inductively coupled to the same secondary winding connected to the lamp;
- an impedante in which variable as a function of frequency, connected in series with the lamp; and
- control circuit means operable to render the said electronic switch means alternately conductive in such a way that an alternating voltage develops across the terminals of the said secondary winding, the said control circuit means having a control input intended to receive a control signal to cause a variation in the frequency of the said alternating voltage in such a way as to cause a corresponding variation of the said impedance and a consequent variation of the current flowing through the lamp;
- a current sensor resistor connected in series with the lamp and connected to the said control circuit means;
- the said control circuit means including signal treatment circuit means connected to the terminals of the said resistor and operable to provide at its output a square wave signal at the same frequency as the signal developed across the terminals of this resistor.

A self-oscillating circuit for driving a gas discharge lamp of the type specified above is described in earlier European Patent Application No. 92830358.5. In this circuit the said signal treatment circuit means are arranged to provide first and second square wave signals, respectively in phase and in phase opposition with the signal developed across the terminals of the current sensor resistor. These square wave signals are utilised to control the charging and discharging of two capacitors to generate first and second ramp signals in phase with the first and second square wave signal respectively. The ramp signals thus generated are compared with a reference level which varies in dependence on an external control signal. On the basis of the result of this comparison pilot signals are generated for the electronic switches of the two circuit branches of the inverter.

In the above described self-oscillating circuit disadvantages can arise if the two capacitors utilised to generate the said ramp signals have different capacity values.

The object of the present invention is to provide

a self-oscillating circuit with which this disadvantage can be avoided.

This object is achieved according to the invention by means of a self-oscillating circuit of the previously specified type, the main characteristic of which lies in the fact that the said control circuit means further comprise:

- second signal treatment circuit means including a single capacitor and operable to provide a triangular wave signal in phase with the said square wave signal; and
- comparison and processing circuit means operable to generate first and second logic control signals capable of rendering the electronic switching means of the said first and second circuit branches of the inverter conductive in a manner which depends upon the comparison of the said triangular wave signal with a reference level which is variable in dependence on the said external control signal.

Further characteristics and advantages of the invention will become apparent from the following detailed description, given with reference to the attached drawings, provided purely by way of non-limitative example, in which:

Figure 1 is a circuit diagram, partially in block diagram form, of a self-oscillating circuit according to the invention;

Figure 2 is a circuit diagram which shows the internal structure of a control circuit contained in the self-oscillating circuit of Figure 1;

Figure 3 is a circuit diagram which shows the detailed structure of an embodiment of a first signal-treatment circuit in the control circuit shown in Figure 2;

Figure 4 is a circuit diagram which shows an embodiment of a second signal treatment circuit and a comparison circuit included in the control circuit of Figure 2;

Figure 5 is a circuit diagram which shows an embodiment of a logic circuit contained in the control circuit of Figure 2; and

Figure 6 shows thirteen exemplary signals developed in the operation of the circuit according to the invention.

In Figure 1, the reference L indicates a gas discharge lamp usable in a spotlight or front headlight of a motor vehicle.

Associated with this lamp is a control circuit of self-oscillating type which includes an inverter generally indicated 1. This inverter has two circuit branches a and b between a terminal 2 and earth.

The said circuit branches include respective MOSFET transistors 3a, 3b with associated shunt ZENER diodes Za and Zb. These diodes are conveniently constituted by the so-called "parasitic diodes" inherent in MOSFET transistors themselves.

In series with the drain-source path of the tran-

sistors 3a, 3b in the said two circuit branches are connected two primary windings 4a and 4b. These windings are inductively coupled to the same secondary winding 5 with which they form a transformer generally indicated 6.

The secondary winding 5 is connected between earth and an impedance 7 of resonant L-C type connected in series with the lamp L.

A capacitor 8 is connected in parallel with the lamp L.

A resistor 9, functioning as a sensor for detecting the current which flows in the lamp L in operation is connected between the lamp and earth.

Reverting to the inverter 1, the terminal 2 to which the primary windings 4a and 4b are connected is connectable to a DC voltage source  $V_b$  (for example the battery on board the motor vehicle) via a control switch 10.

The gate terminals of the transistors 3a, 3b are connected to corresponding outputs of an electronic control circuit generally indicated 11 in Figure 1. This circuit has an input 11a intended to receive an external control signal indicative of the current which has to flow through the lamp L. This current can for example be of greater intensity in phases immediately after the lamp is lit. to allow a rapid heating thereof.

As will appear more clearly below, when the switch 10 is closed the inverter 1 is connected to the DC voltage source  $V_b$ .

The control circuit 11 now renders the transistors 3a and 3b conductive alternately, and consequently an alternating voltage develops across the terminals of the secondary winding 5 of the transformer 6.

This voltage causes the flow of an alternating current through the impedance 7, the lamp L and the resistor 9. The control circuit 11 determines the frequency of this alternating current in dependence on the signal applied to its control input 11a.

The impedance of the resonant circuit 7 varies with a variation in the frequency of the alternating voltage which is produced in the secondary winding 5. Consequently a corresponding variation in the alternating current flowing through the lamp and the resistor 9 is caused.

In Figures 2 and 6 the current flowing in the resistor 9 has been indicated  $I_L$ . This current is substantially equal to the current flowing through the lamp L.

The control circuit 11 includes a signal treatment circuit 12 for treatment of the signal  $I_L$ .

As will become apparent more clearly below, the circuit 12 is arranged to provide at its output two square wave signals B and C at the same frequency as the signal  $I_L$  and respectively in phase and in phase opposition with respect to it.

The signal B is applied to the input of a second signal treatment circuit generally indicated 13 in Figure 2.

This circuit provides at its output a triangular wave

or sawtooth signal of twice the frequency of the signal B, indicated D in Figures 2 and 6.

The output of the circuit 13 is connected to a comparison circuit 14 which compares the signal D with a reference signal  $s_a$  which is variable in dependence on the external control signal applied to the input 11a of the control circuit 11.

The comparison circuit 14 provides at its output a signal E (Figure 6) which is applied to an input of a logic circuit 15 to which the signal C is also supplied. On the basis of the signals E and C the logic circuit 15 provides at its output two signals J and K which are applied to the gates of the transistors 3a and 3b for the purpose of controlling its conduction.

In Figure 3 there is shown an embodiment of the treatment circuit 12 for treatment of the signal developed across the terminals of the resistor 9.

In this embodiment the circuit 12 includes an input filter 16 of R-C type followed by an amplifier circuit 17 formed by an operational amplifier 18 which amplifies the received signal and shifts its level (by introducing an offset) so as to render this signal unidirectional, for example totally positive.

The amplifier circuit 17 is followed by a squaring circuit 19 formed, in a manner known per se, by a comparator 20. In operation, at the output of the circuit 19 there is available a signal A (Figures 3 and 6) of square wave form which is at the same frequency and in phase with the signal  $I_L$ .

The output of the squaring circuit 19 is connected to the input of a logic circuit 21 which in the exemplary embodiment illustrated simply comprises two EXOR gates 22 and 23.

The EXOR gate 22 has one input connected to the output of the squaring circuit 19 and the other input connected to earth.

The EXOR gate 23 has one input connected to the output of the squaring circuit 19 and the other input connected to a DC voltage source  $V_{CC}$ , corresponding to the logic "high" level.

Consequently, these EXOR gates 22 and 23 provide at their outputs signals, indicated B and C in Figures 3 and 6. The signal B is at the same frequency and in phase with the signal A whilst the signal C is in phase opposition to the signal B.

In the embodiment illustrated in Figure 4 the circuit 13 includes two monostable circuits 24, 25 to the input of which the signal B is applied directly, and via an inverter 26 respectively. In operation each of these monostables emits from its output a narrow pulse in correspondence with each rising edge of the signal applied to its input.

The outputs of the monostable circuits 24 and 25 are connected to the inputs of an EXOR gate 27.

In operation, at the output of the EXOR gate 27 there appears a pulse signal which is shown in Figure 6. This signal has, in particular, a pulse in correspondence with each passage through zero of the current

$I_L$  in the resistor 9.

The output of the EXOR gate 27 is connected to the input 28a of a D type "toggle" bistable 28.

The Q and  $\bar{Q}$  outputs of this bistable circuit are connected to the control inputs of two electronic switches 29 and 30 disposed in series with respective current generators 31 and 32 between a node 33 and earth.

A capacitor connected between the node 33 and earth is indicated CO.

When in operation the output Q (Q) of the bistable device 28 is at level "1", the associated electronic switch 30 (29) is conductive and couples the current generator 32 (31) to the capacitor CO.

The current generator 32, which in the following will be defined as the discharge generator, generates a DC current which flows towards earth. The generator 31, hereinafter defined as the charging generator generates a DC current flowing towards the node 33.

To the node 33 there is further connected the input of a comparator 34 which in the exemplary embodiment illustrated is of the non-inverting type. This comparator compares the value of the voltage (with respect to earth) on the capacitor CO with a constant reference value produced by a generator  $V_S$ . When the voltage on the capacitor CO falls below the value  $V_S$  the output of the comparator 34 applies a reset command to the reset input R of the bistable device 28.

In operation each pulse of the signal F applied to the bistable device causes the output C of this device to rise to the "high" level. Consequently the electronic switch 30 is "closed" whilst the switch 29 is "open". In this situation the current generator 32 causes a partial discharge of the capacitor CO. This discharge continues until the voltage on the capacitor CO reaches the lower threshold  $V_S$ ; when this occurs the comparator 34 causes the bistable device 28 to reset. The output Q of this device goes to the "low" level and correspondingly the switch 30 becomes "open" whilst the switch 29 becomes "closed". The current generator 31 is now coupled to the capacitor CO, and recharges it. The recharging of the capacitor by operation of the generator 31 continues until arrival of the subsequent pulse of the signal F, which causes a new change of state of the outputs Q and  $\bar{Q}$  of the bistable device 28.

The voltage across the terminals of the capacitor CO then assumes a periodic variation with a sawtooth waveform as is shown by the waveform D in Figure 6. The sawtooth signal D is applied to the input of a comparator circuit 14, which in the embodiment shown in Figure 4 is of inverting type. This comparator compares the signal D with a threshold level  $S_a$  which varies as a function of the external control signal applied to the control input 11a.

Correspondingly, a square wave signal, indicated E in Figure 6, is made available at the output of the

comparator 14.

With reference to Figure 5, the signals E and C are applied to the input of an OR circuit 36 the output of which is connected to first inputs of two NAND gates 38 and 39. The NAND gate 38 receives the signal B on its other input. The NAND gate 39 receives the signal C on its other input. In operation, at the outputs of the gates 36, 38 and 39 appear the signals G, H and I respectively, the variations of which are shown in Figure 6.

The outputs of the NAND gates 38 and 39 are connected to the set input (S) and reset input (R) of a bistable circuit 40. This circuit is conveniently a D-type flip-flop in a set/reset configuration. This circuit provides at its output two signals the waveforms of which substantially correspond to those of the signals H and I. This circuit, however, ensures that the switching of these signals occurs in a correct sequential manner.

The outputs Q and  $\bar{Q}$  of the bistable circuit 40 are connected to the gates of transistors 3a and 3b via respective logic inverters 42 and 43.

At the output of the said inverters, therefore, the pilot signals J and K appear, the waveforms of which signals are shown in Figure 6. The signals J and K cause alternate conduction of the transistors 3a and 3b as already mentioned hereinbefore with reference to Figure 1.

If in operation it is necessary to cause an increase in the current flowing in the lamp L, the external control signal applied to the input 11a of the control circuit 11 causes the threshold level  $S_a$  associated with the threshold comparator 14 to rise. This consequently causes a variation in the conduction times of the transistors 3a and 3b.

Naturally, the principle of the invention remaining the same, the embodiments and details of construction can be widely varied with respect to what has been described and illustrated purely by way of non-limitative example, without by this departing from the ambit of the present invention.

In particular, the circuit of the invention can be formed at least in part by using integrated digital components or by a microprocessor. In place of the sawtooth signal generator circuit digital counters can be utilised, in particular counters of the incremental-decremental type, and comparison devices likewise of digital type.

## Claims

1. A self-oscillating circuit for driving a gas discharge lamp (L) particularly for use on board a motor vehicle, comprising
  - an inverter (1) with two circuit branches (a, b) connectable to a DC voltage source ( $V_B$ ) and comprising respective electronic switch

means (3a and 3b) in series with respective primary windings (4a, 4b); the primary windings (4a, 4b) of the said circuit branches (a, b) being inductively coupled to the same secondary winding (5) connected to the lamp (L);

- an impedance (7) variable as a function of the frequency, connected in series with the lamp (L);
- control circuit means (11) operable to render the said electronic switch means (3a, 3b) alternately conductive in such a way that an alternating voltage develops across the terminals of the said secondary winding (5); the said control circuit means (11) having a control input (11a) intended to receive a control signal operable to cause a variation in the frequency of the said alternating voltage in such a way as to cause a corresponding variation of the said impedance (7) and a consequent variation in the current ( $I_L$ ) flowing through the lamp (L);
- a current sensor resistor (9) connected in series with the lamp (1) and connected to the said control circuit means (11);
- the said control circuit means (11) including signal treatment means (12) connected to the said resistor (9) and operable to provide at its output a square wave signal (B) at the same frequency as the signal developed across the terminals of this resistor (9);

the circuit being characterised by the fact that the said control circuit means (11) further include

- second signal treatment circuit means (13) including a single capacitor (CO), operable to provide a triangular wave signal (D) in phase with the said square wave signal (B) and
- comparison and processing circuit means (14, 15) operable to generate first and second logic control signals (J, K) capable of rendering the electronic switch means (3a, 3b) of the said first and second circuit branches (a, b) conductive in a manner which depends on the comparison of the said triangular wave signal (D) with a reference level ( $s_a$ ) which is variable in dependence on the said control signal.

2. A self-oscillating circuit according to Claim 1, characterised by the fact that the said second signal treatment means include

- a capacitor (CO),
- means (34) for detecting the voltage across the terminals of the said capacitor (CO) and
- control circuit means (24-30) operable to couple the said current generator (31, 32)

to the capacitor (CO) to cause successive charging and discharging cycles of this capacitor (CO); the said control circuit means being arranged to cause a discharge of the capacitor (CO), each time the current ( $I_L$ ) flowing in the lamp (L) passes through zero until a predetermined minimum voltage ( $V_s$ ) is reached on the capacitor (CO), and then recharging of the capacitor (CO) until the subsequent passage of the said current ( $I_L$ ) through zero.

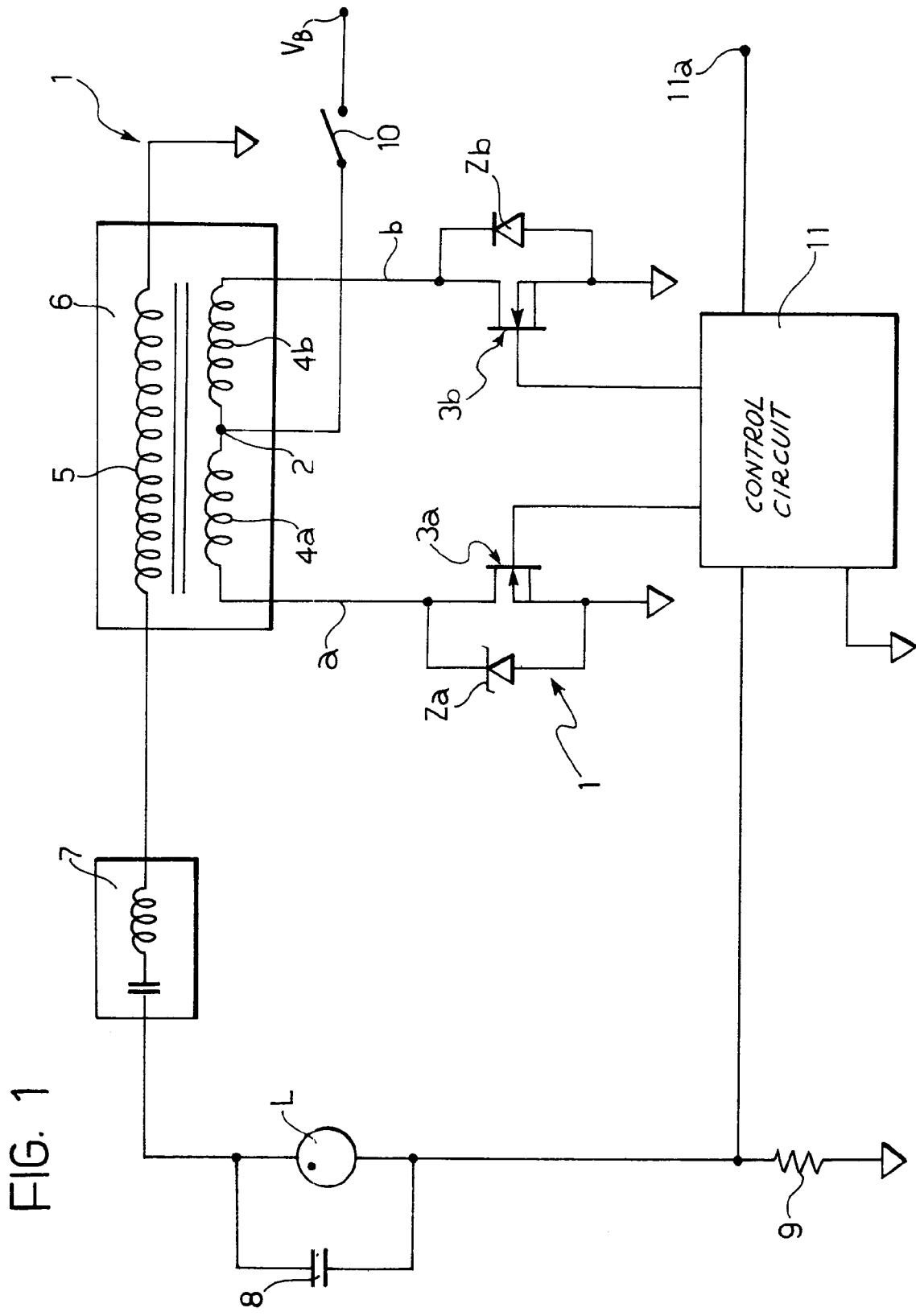


FIG. 1

FIG. 2

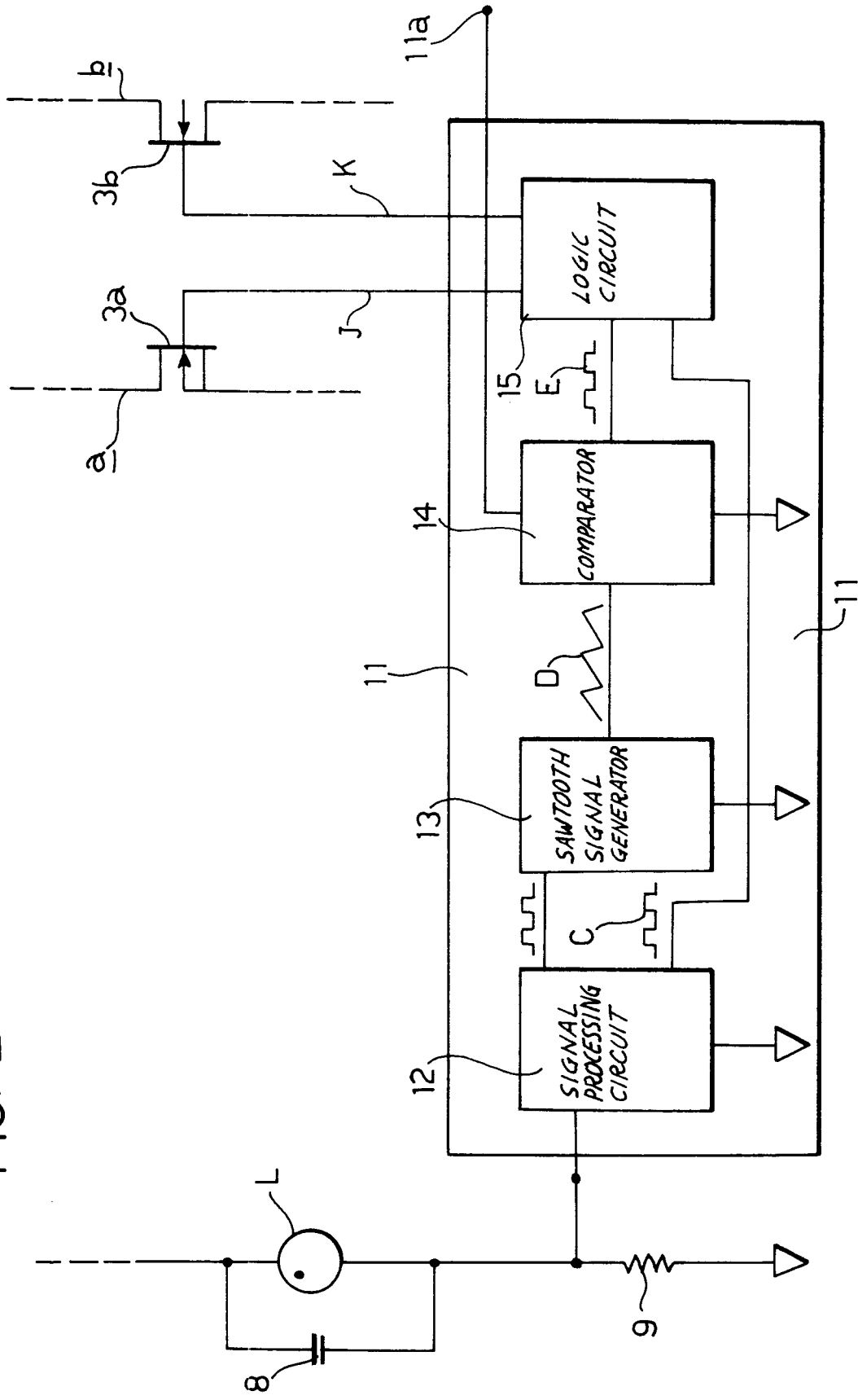


FIG. 3

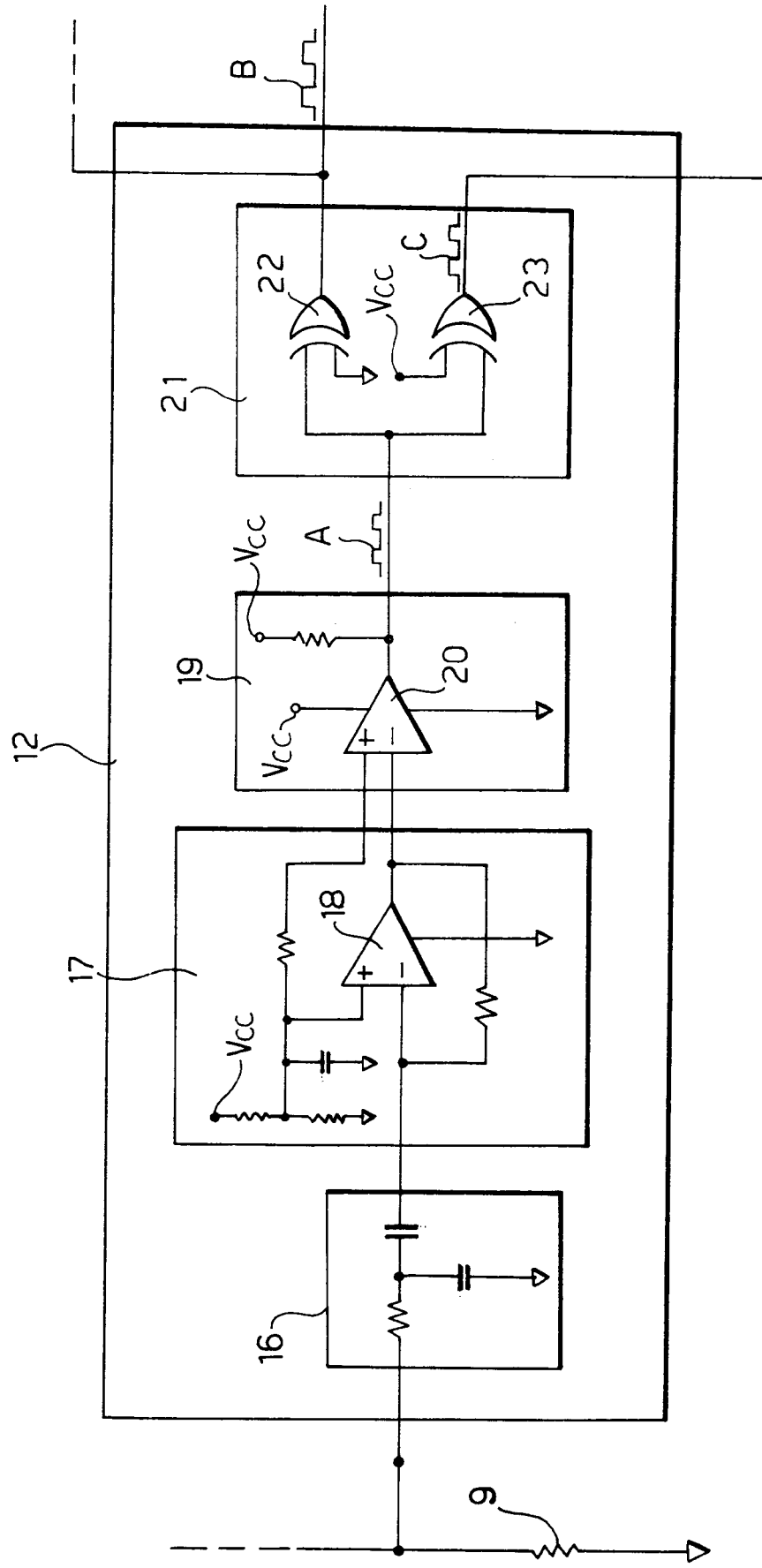
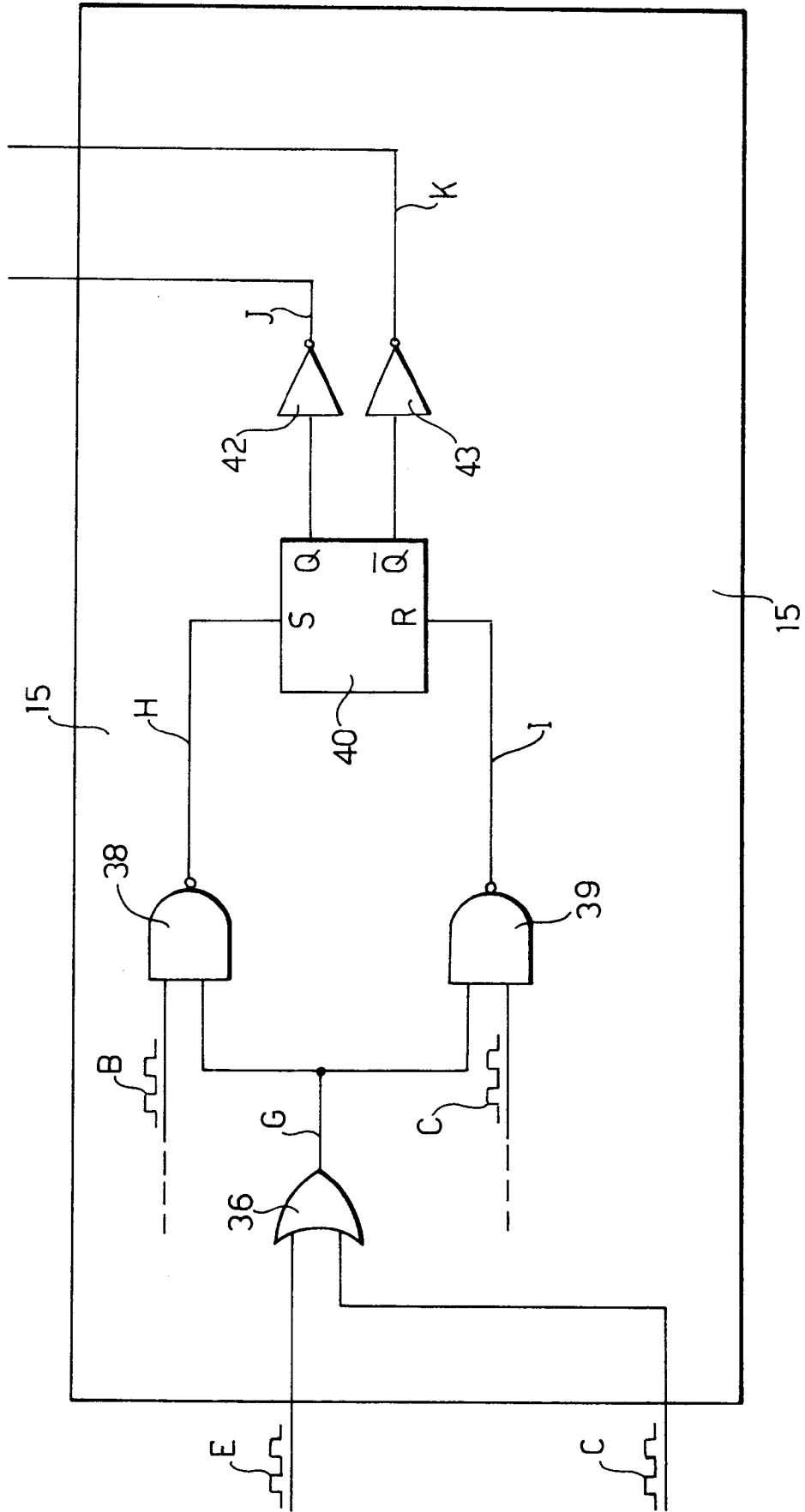
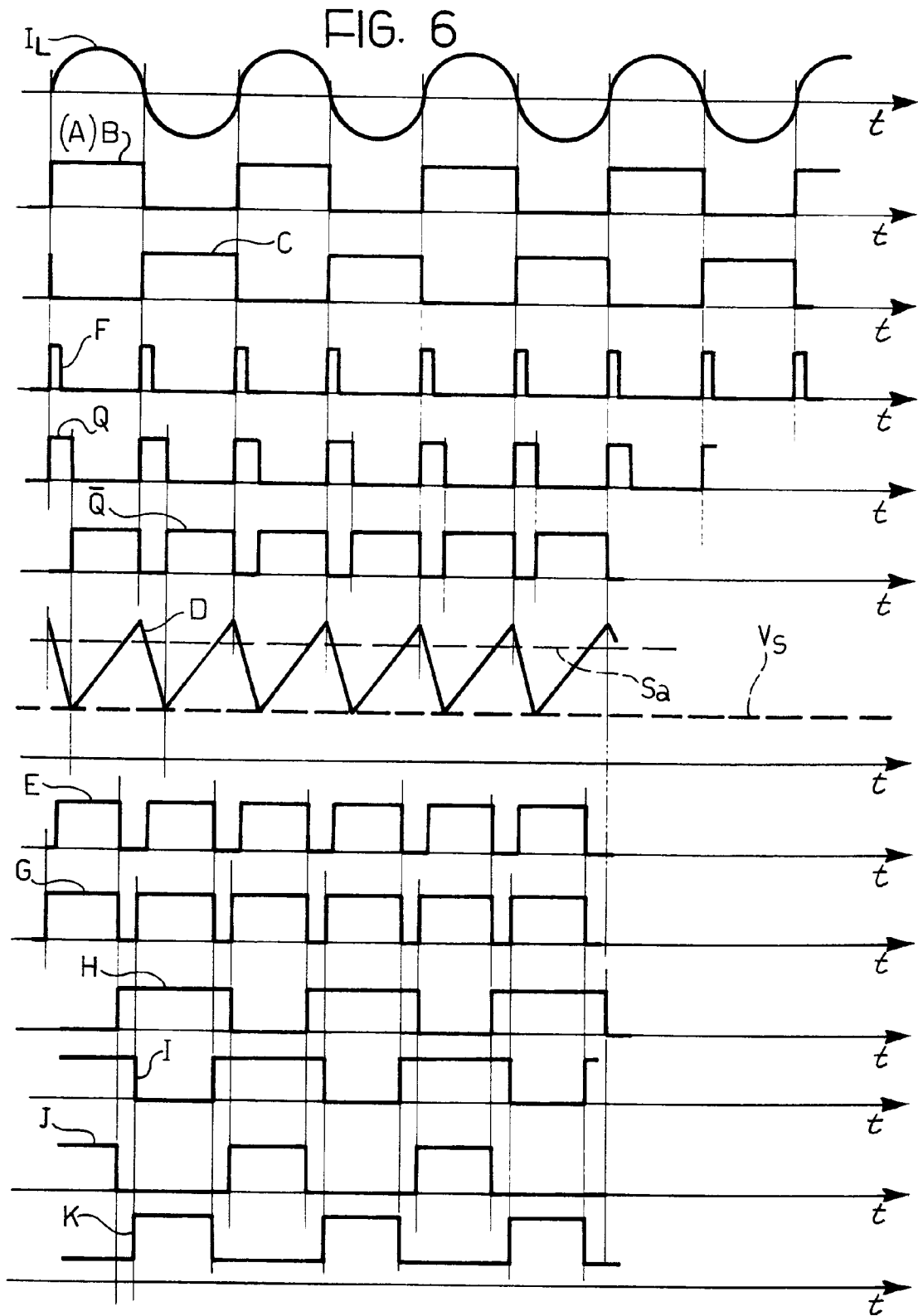






FIG. 5







European Patent  
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EUROPEAN SEARCH REPORT

Application Number

EP 92 83 0630

DOCUMENTS CONSIDERED TO BE RELEVANT			
Category	Citation of document with indication, where appropriate, of relevant passages	Relevant to claim	CLASSIFICATION OF THE APPLICATION (Int. Cl.5)
A	WO-A-8 809 108 (ROBERT BOSCH) * page 3, line 21 - page 5, line 3; figure 1 *	1	H05B41/29 H02M7/538
A	GB-A-2 137 780 (NEWTON DERBY) * page 1, line 57 - page 2, line 2; figure 1 *	1	
A	EP-A-0 311 424 (THOMAS INDUSTRIES) * column 4, line 5 - column 7, line 17; figure 1 *	1	
A	US-A-4 935 673 (VILA-MASOT) * abstract; figures 2,5 *	1	
A	US-A-4 700 113 (STUPP) * column 4, line 7 - column 6, line 32; figure 1 *	1	
-----			TECHNICAL FIELDS SEARCHED (Int. Cl.5)
			H05B H02M
The present search report has been drawn up for all claims			
Place of search THE HAGUE		Date of completion of the search 04 MARCH 1993	Examiner SPEISER P.
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