



(19) **United States**

(12) **Patent Application Publication**

**Azizi et al.**

(10) **Pub. No.: US 2014/0185662 A1**

(43) **Pub. Date: Jul. 3, 2014**

(54) **METHODS AND ARRANGEMENTS FOR PHASE TRACKING FOR MULTI-MODE OPERATION IN WIRELESS NETWORKS**

(52) **U.S. Cl.**  
CPC ..... **H04L 27/2647** (2013.01)  
USPC ..... **375/232; 375/326**

(71) Applicants: **Shahnaz Azizi**, Cupertino, CA (US);  
**Eldad Perahia**, Portland, OR (US);  
**Thomas J. Kenney**, Portland, OR (US)

(57) **ABSTRACT**  
Logic may comprise a single phase tracking implementation for all bandwidths of operation and the logic may adaptively change pre-defined and stored track parameters if the receiving packet is 1 MHz bandwidth. Logic may detect a packet and long training fields before performing a 1 MHz classification. Logic may auto-detect 1 MHz bandwidth transmissions by a property of the long training field sequences. Logic may auto-detect 1 MHz bandwidth transmissions by detecting a Binary Phase Shift Keying (BPSK) modulated first signal field symbol rather than the Quadrature Binary Phase Shift Keying (QBPSK) associated with the 2 MHz or greater bandwidth transmissions. Logic may perform an algorithm to determine an estimated phase correction value for a given orthogonal frequency division multiplexing symbol and several embodiments integrate this value with an intercept multiplier that may be 0.2 for 1 MHz transmissions and, e.g., 0.5 for 2 MHz or greater bandwidth communication.

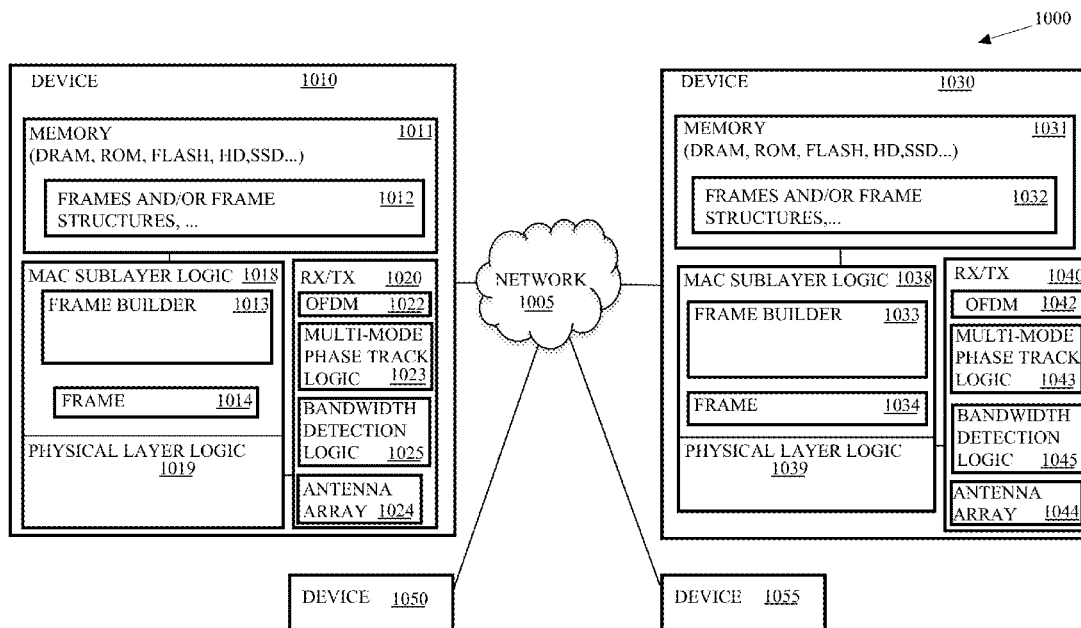
(72) Inventors: **Shahnaz Azizi**, Cupertino, CA (US);  
**Eldad Perahia**, Portland, OR (US);  
**Thomas J. Kenney**, Portland, OR (US)

(21) Appl. No.: **13/730,996**

(22) Filed: **Dec. 29, 2012**

**Publication Classification**

(51) **Int. Cl.**  
**H04L 27/26** (2006.01)



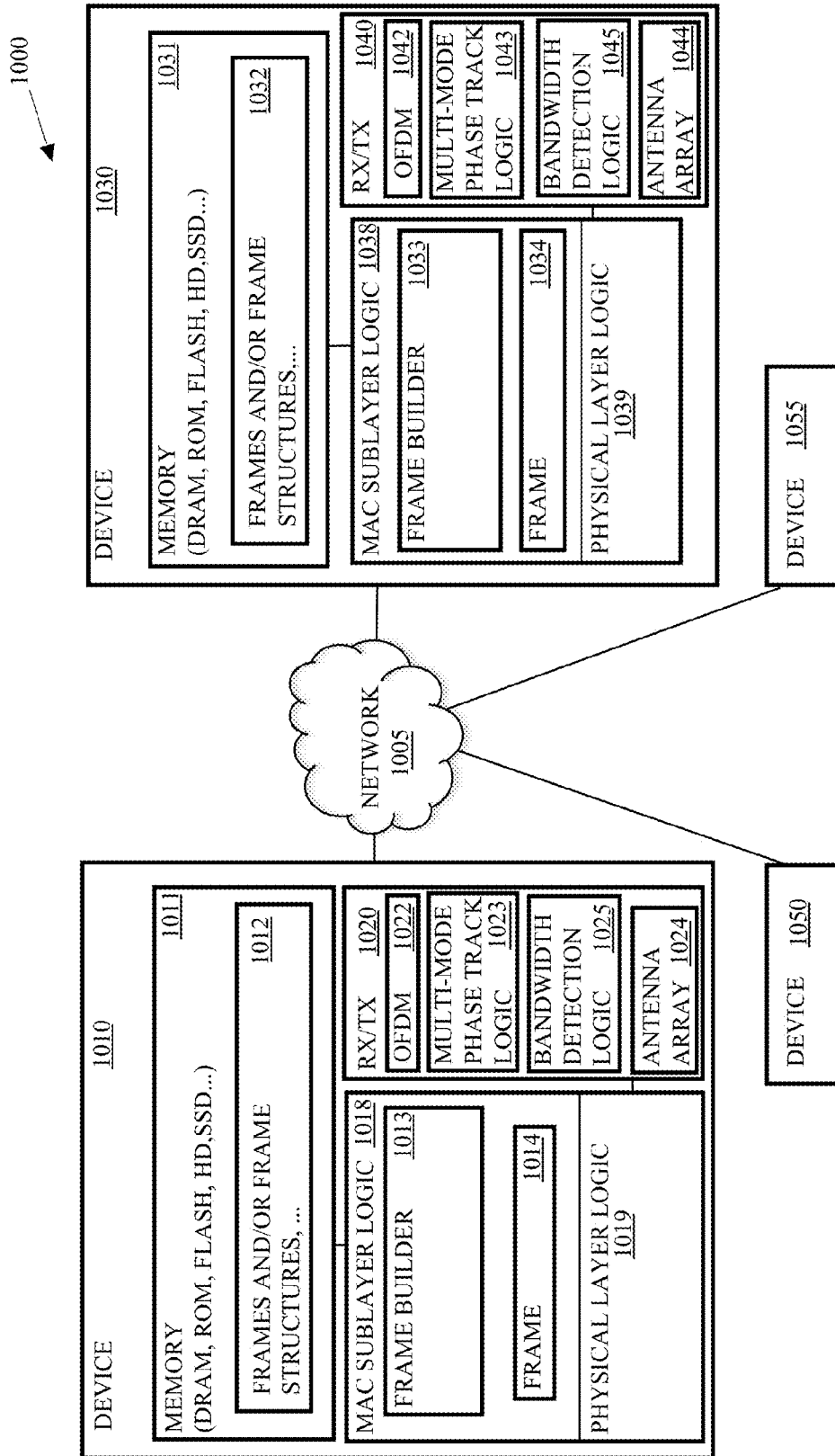


FIG. 1

1100

SYMBOL NUMBER	1	2	3	4	5	6	7	8	9	10	11	12	13
PILOT	-8	-9	-10	-11	-12	-13	-1	-2	-3	-4	-5	-6	-7
TONES	6	5	4	3	2	1	13	12	11	10	9	8	7

FIG. 1A

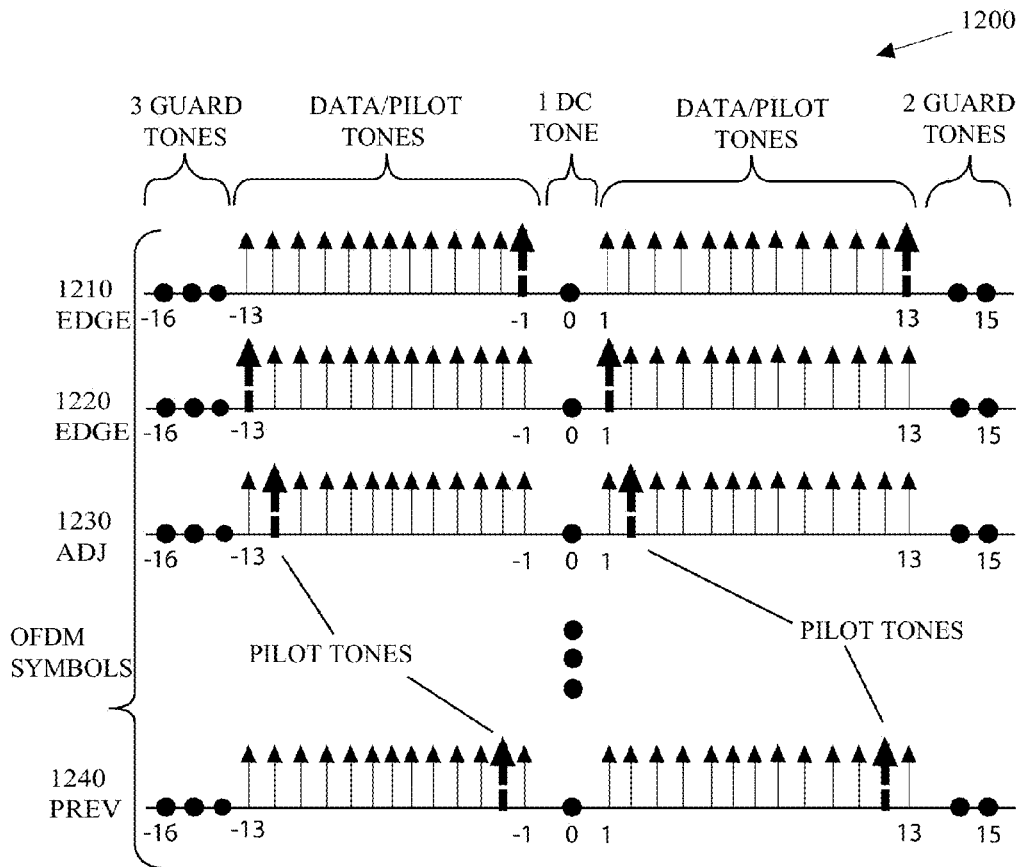


FIG. 1B

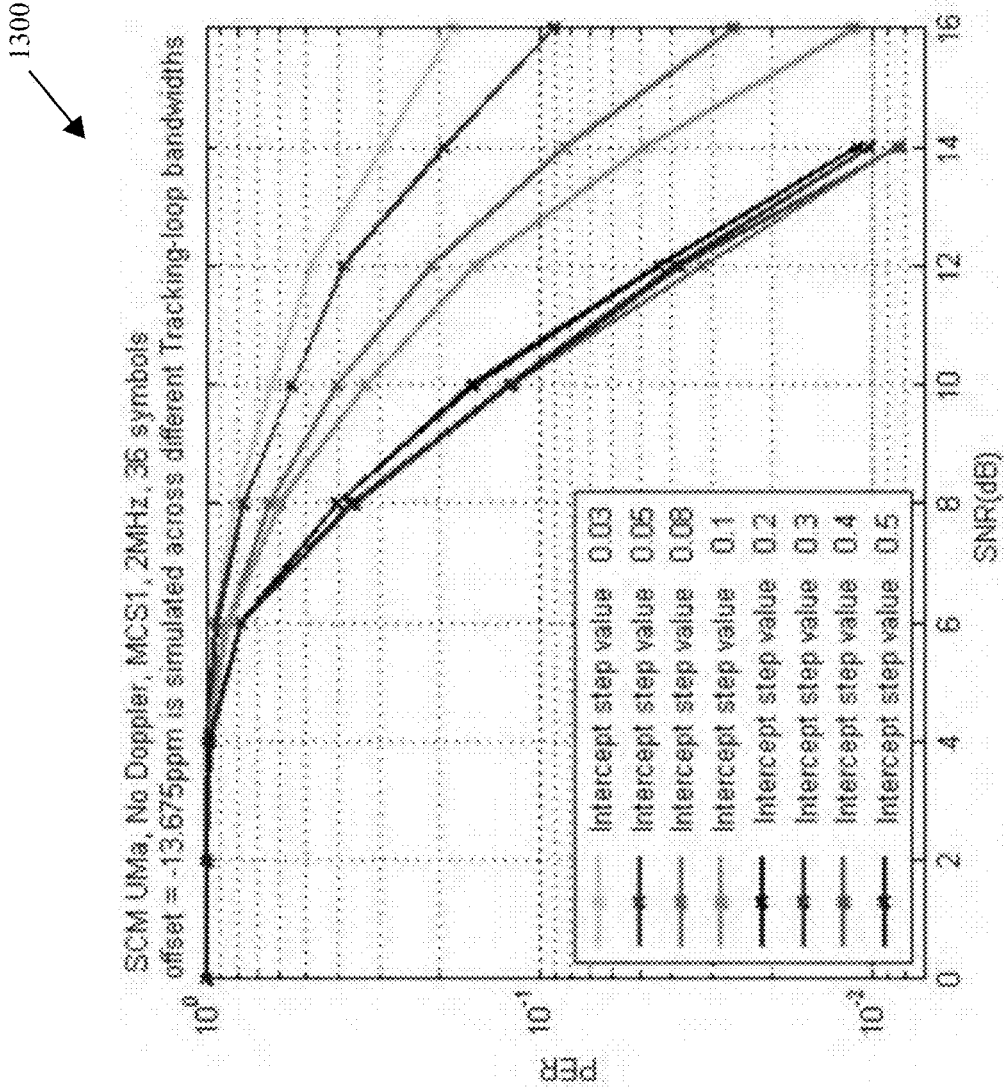


FIG. 1C

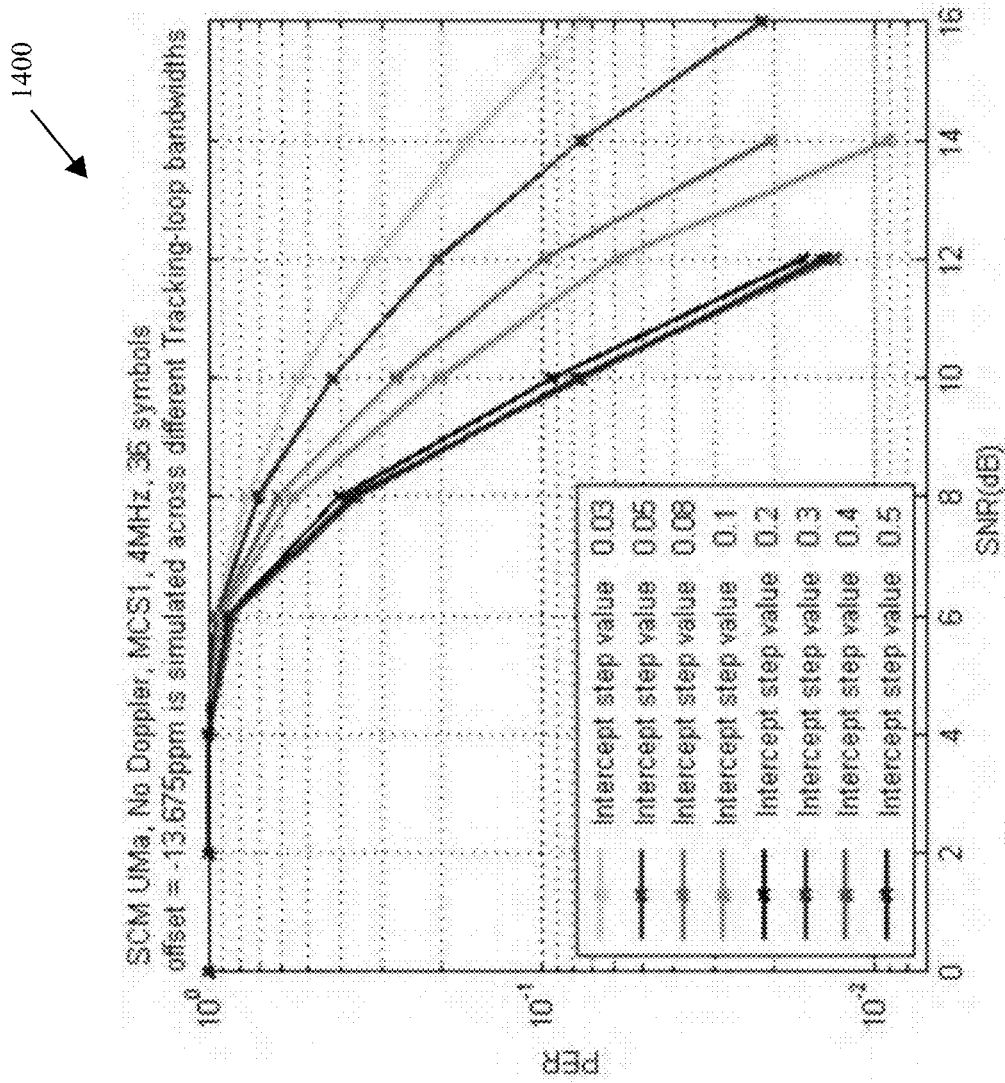


FIG. 1D

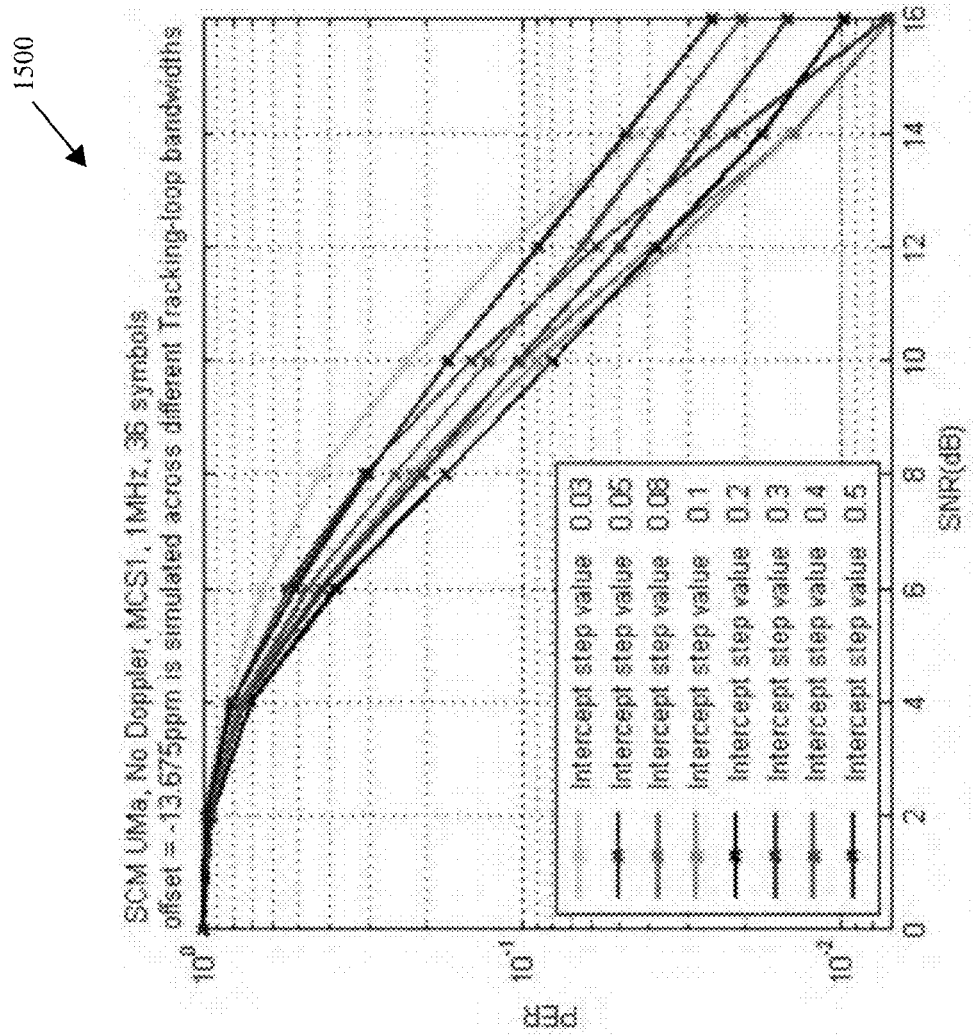


FIG. 1E

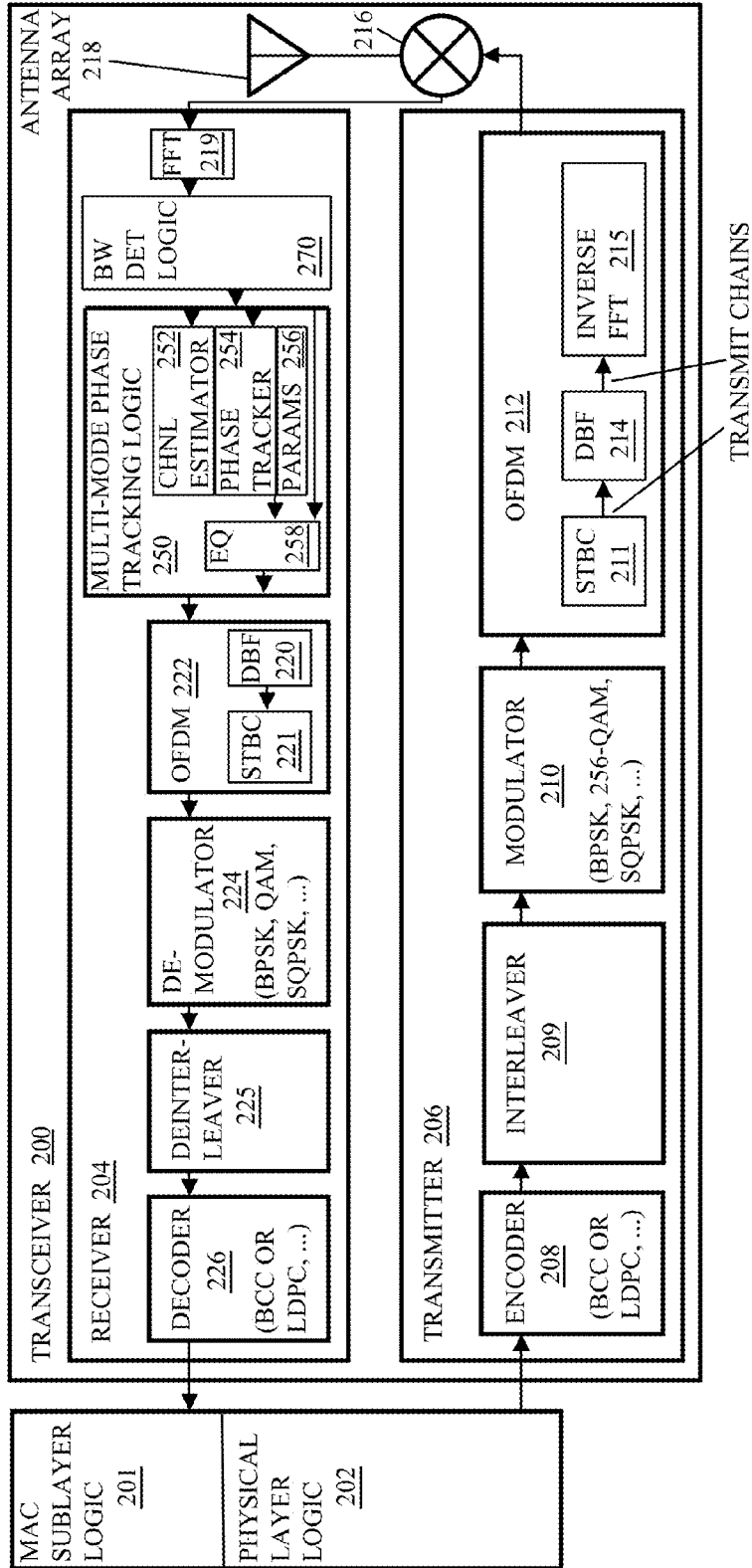
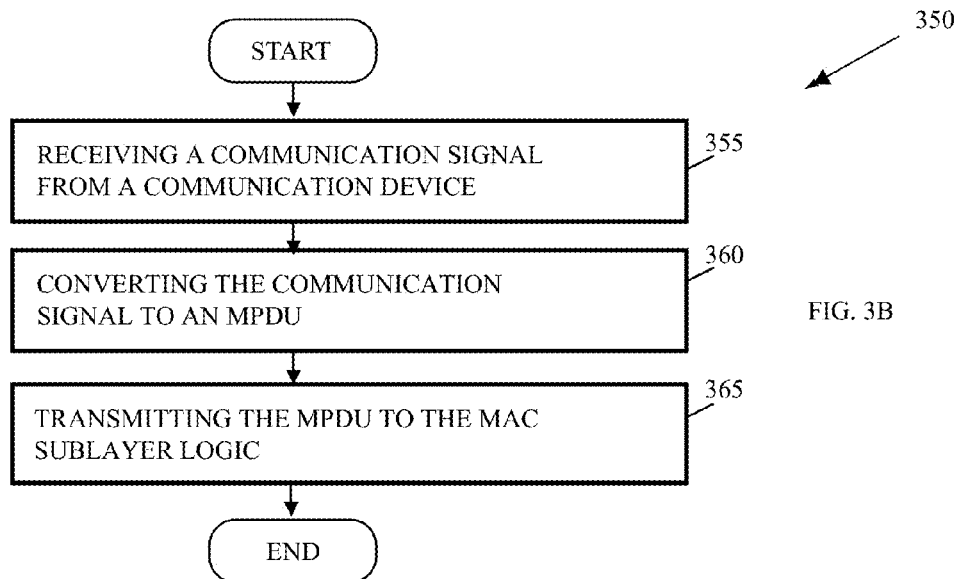
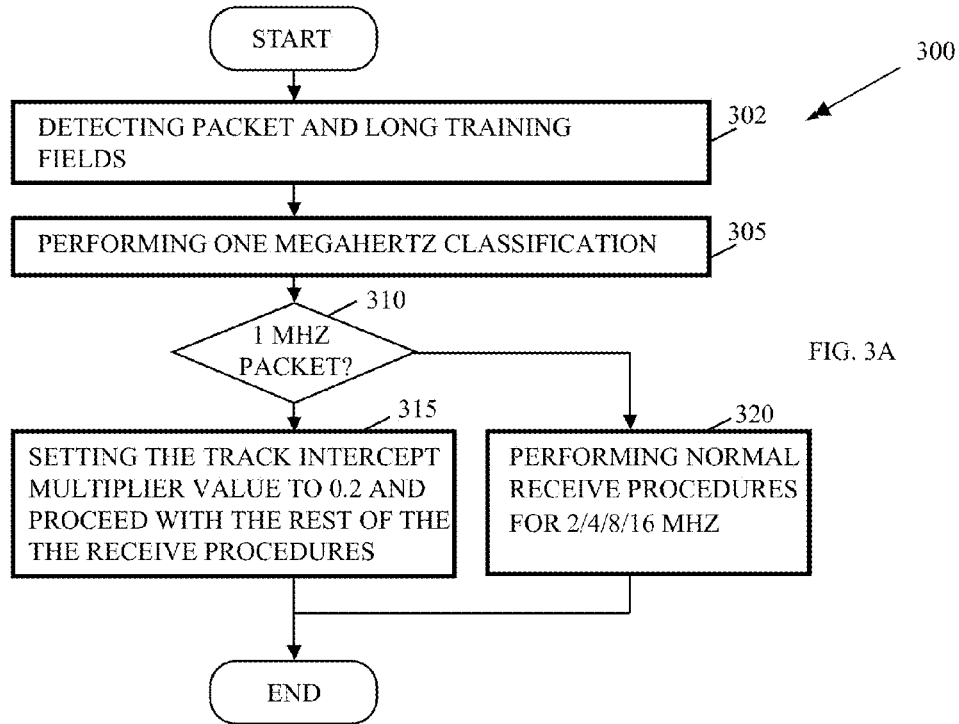


FIG. 2





## METHODS AND ARRANGEMENTS FOR PHASE TRACKING FOR MULTI-MODE OPERATION IN WIRELESS NETWORKS

### BACKGROUND

[0001] Embodiments are in the field of wireless communications. More particularly, the present disclosure relates to phase tracking for multi-mode operation.

### BRIEF DESCRIPTION OF THE DRAWINGS

[0002] FIG. 1 depicts an embodiment of a wireless network comprising a plurality of communications devices;

[0003] FIG. 1A depicts an embodiment of a table illustrating pilot tone locations and processing pilot tones except for the pilot tones at locations near the direct current (DC) and edge tones;

[0004] FIG. 1B depicts embodiments of orthogonal frequency division multiplexing (OFDM) symbols in an OFDM packet transmission with shifting pilot tones;

[0005] FIG. 1C-E depict embodiments of simulations comparing the phase tracking with different integrator coefficients at 1 MHz (megahertz), 2 MHz, and 4 MHz bandwidths;

[0006] FIG. 2 depicts an embodiment of an apparatus with multi-mode phase tracking logic to adjust phase tracking for 1 MHz and greater than 1 MHz bandwidths; and

[0007] FIGS. 3A-B depict embodiments of flowcharts for bandwidth detection and multi-mode phase tracking and to generate, transmit, receive, parse, and interpret communications.

### DETAILED DESCRIPTION OF EMBODIMENTS

[0008] The following is a detailed description of novel embodiments depicted in the accompanying drawings. However, the amount of detail offered is not intended to limit anticipated variations of the described embodiments; on the contrary, the claims and detailed description are to cover all modifications, equivalents, and alternatives falling within the spirit and scope of the present teachings as defined by the appended claims. The detailed descriptions below are designed to make such embodiments understandable to a person having ordinary skill in the art.

[0009] Institute of Electrical and Electronic Engineers (IEEE) 802.11ah systems have bandwidths currently defined are 1 MHz (MegaHertz) and a set of down-clocked IEEE 802.11ac rates, namely 2, 4, 8 and 16 MHz, where the down clocking is 10x. The 1 MHz system may use a 32-point fast Fourier transform (FFT). Of those 32 subcarriers, 24 will be used for data and 2 for pilot. Additionally, a repetition mode is being included to extend range.

[0010] One of the issues for IEEE 802.11ah wireless networks is coexistence and multi-mode operation of devices. Devices are enabled to detect 1 MHz bandwidth and other bandwidths of operation, namely 2/4/8 and 16 MHz upon receiving the preamble portion of the packet. Once the preamble is received and the bandwidth of operation is determined, the receiver is dynamically configured to the detected bandwidth. For example, for 1 MHz bandwidth, the receiver is configured to 32 point FFT with its new pilot structure design where only 2 subcarriers are used for pilots.

[0011] Many expected that the phase tracking during 1 MHz bandwidth operation is to be different from higher bandwidths because fewer tones (2 tones) are designed to be pilot tones. Hence the phase tracking module of a multi-mode

IEEE 802.11ah receiver is expected to have two sections, one section is 10x down-clocked of the previous IEEE 802.11n/11ac Wi-Fi devices, while the second section is for 1 MHz operation only.

[0012] Embodiments herein, however, may comprise a single phase tracking implementation to be used in all bandwidths of operation, and only if the incoming packet comprises a 1 MHz bandwidth, the receiver adaptively changes its pre-defined and stored track parameters. Many embodiments comprise logic to detect a packet, short and long training fields, and signal fields. Several embodiments perform a 1 MHz classification. Some embodiments auto-detect 1 MHz bandwidth transmissions by a property of the long training field sequences. Some embodiments auto-detect 1 MHz bandwidth transmissions by detecting a Binary Phase Shift Keying (BPSK) modulated first signal field symbol rather than the Quadrature Binary Phase Shift Keying (QBPSK) associated with the 2 MHz or greater bandwidth transmissions. Several embodiments perform an algorithm to determine an estimated phase correction value for a given OFDM symbol and several embodiments integrate this value with a multiplier that may be 0.2 for 1 MHz transmissions and, e.g., 0.5 for 2 MHz or greater bandwidth communication.

[0013] Embodiments may use of known pilot symbol tones shifting across the bandwidth of the orthogonal frequency division multiplexing (OFDM) packet during transmission of the packet to allow receivers to track the channel information during the transmission of the packet. Thus, the pilot information can be used to compute channel updates (also referred to as channel estimates) to the channel state information for those tones and the phase correction information (also referred to as phase rotations or phase correction estimates) to track channel phase with different tones.

[0014] In many embodiments, transmitters shift the location of the pilot tones every N symbols, where N may be a system parameter, setting, or a fixed value. Thus, the location of the pilot tones remain constant for N symbols before shifting to the next location. In several embodiments, a receiver may then use the N pilot symbols to make a channel estimate using an appropriate algorithm. Some embodiments described herein may implement the pilot shifting with N fixed to a value of one, which it means the pilots would shift every symbol and the implementation of a fixed value for N may also remove a need of signaling between transmitter and receiver to update the value of N because the single value used at the transmitter may be predetermined.

[0015] Various embodiments may be designed to address different technical problems associated with multi-mode phase tracking with shifting pilot tones. For instance, some embodiments may be designed to address one or more technical problems such as phase tracking with shifting pilot tones for a 1 MHz bandwidth in which the receiver is configured to 32 point FFT with its new pilot structure design where only 2 subcarriers are used for pilot tones and phase tracking with shifting pilot tones for a 2 MHz plus bandwidth in which the receiver is configured to 64 point FFT with its pilot structure design having 4 plus subcarriers are used for pilot tones.

[0016] Different technical problems such as those discussed above may be addressed by one or more different embodiments. For instance, some embodiments that address multi-mode phase tracking with shifting may do so by one or more different technical means such as determining whether a received packet is a 1 MHz bandwidth or a larger bandwidth, defining an integrator coefficients or static filter coefficient

for the 1 MHz bandwidth communications, changing the intercept parameter depending upon whether an incoming communications is at a 1 MHz bandwidth or a greater bandwidth, and the like.

**[0017]** Some embodiments implement Institute of Electrical and Electronic Engineers (IEEE) 802.11 systems such as IEEE 802.11ah systems and other systems that operate in accordance with standards such as the IEEE 802.11-2012, IEEE Standard for Information technology—Telecommunications and information exchange between systems—Local and metropolitan area networks—Specific requirements—Part 11: Wireless LAN Medium Access Control (MAC) and Physical Layer (PHY) Specifications (<http://standards.ieee.org/getieee802/download/802.11-2012.pdf>).

**[0018]** Several embodiments comprise access points (APs) for and/or client devices of APs or stations (STAs) such as routers, switches, servers, workstations, netbooks, mobile devices (Laptop, Smart Phone, Tablet, and the like), as well as sensors, meters, controls, instruments, monitors, appliances, and the like. Some embodiments may provide, e.g., indoor and/or outdoor “smart” grid and sensor services. For example, some embodiments may provide a metering station to collect data from sensors that meter the usage of electricity, water, gas, and/or other utilities for a home or homes within a particular area and wirelessly transmit the usage of these services to a meter substation. Further embodiments may collect data from sensors for home healthcare, clinics, or hospitals for monitoring healthcare related events and vital signs for patients such as fall detection, pill bottle monitoring, weight monitoring, sleep apnea, blood sugar levels, heart rhythms, and the like. Embodiments designed for such services may generally require much lower data rates and much lower (ultra low) power consumption than devices provided in IEEE 802.11n/ac systems.

**[0019]** Logic, modules, devices, and interfaces herein described may perform functions that may be implemented in hardware and/or code. Hardware and/or code may comprise software, firmware, microcode, processors, state machines, chipsets, or combinations thereof designed to accomplish the functionality.

**[0020]** Embodiments may facilitate wireless communications. Some embodiments may comprise low power wireless communications like Bluetooth®, wireless local area networks (WLANs), wireless metropolitan area networks (WMANs), wireless personal area networks (WPAN), cellular networks, communications in networks, messaging systems, and smart-devices to facilitate interaction between such devices. Furthermore, some wireless embodiments may incorporate a single antenna while other embodiments may employ multiple antennas. The one or more antennas may couple with a processor and a radio to transmit and/or receive radio waves. For instance, multiple-input and multiple-output (MIMO) is the use of radio channels carrying signals via multiple antennas at both the transmitter and receiver to improve communication performance.

**[0021]** While some of the specific embodiments described below will reference the embodiments with specific configurations, those of skill in the art will realize that embodiments of the present disclosure may advantageously be implemented with other configurations with similar issues or problems.

**[0022]** Turning now to FIG. 1, there is shown an embodiment of a wireless communication system **1000**. The wireless communication system **1000** comprises a communications

device **1010** that may be wire line and wirelessly connected to a network **1005**. The communications device **1010** may communicate wirelessly with a plurality of communication devices **1030**, **1050**, and **1055** via the network **1005**. The communications device **1010** may comprise an access point. The communications device **1030** may comprise a low power communications device such as a sensor, a consumer electronics device, a personal mobile device, or the like. And communications devices **1050** and **1055** may comprise sensors, stations, access points, hubs, switches, routers, computers, laptops, netbooks, cellular phones, smart phones, PDAs (Personal Digital Assistants), or other wireless-capable devices. Thus, communications devices may be mobile or fixed. For example, the communications device **1010** may comprise a metering substation for water consumption within a neighborhood of homes. Each of the homes within the neighborhood may comprise a sensor such as the communications device **1030** and the communications device **1030** may be integrated with or coupled to a water usage meter.

**[0023]** When the communications device **1010** transmits a packet to the communications device **1030** to notify the communications device **1030** that, e.g., the communications device **1010** is buffering data for the communications device **1030**, the communications device **1010** may transmit an orthogonal frequency division multiplexing (OFDM) packet encapsulating a frame **1014**. The OFDM **1022** of the transceiver (RX/TX) **1020** may generate the transmission with pilot tones shifting locations within the symbol indices of the transmission every N symbols. In some embodiments, the communications device **1010** may have previously transmitted an indication of the value of N to the communications device **1030**. In such embodiments, the indication may be transmitted within frame **1014**, e.g., a field in the header may numerate N. In further embodiments, the value of N may be a fixed value. In some embodiments, the value of N is fixed at a value of one.

**[0024]** In some of such embodiments, the communications device **1030** may communicate a capability such as a capability to process pilot tones only to track a phase of the channel by a bit in a capability information field to the communications device **1010**. In several embodiments, the capability bit may refer to a single bit or a bit that is part of a pair of bits to indicate capabilities of the communications device. For instance, the pair of bits may indicate an: (a) ability to process shifted pilot tones (for channel estimate and/or phase rotation updates), (b) ability to process shifted pilot tones only for phase rotation, (c) inability to process shifted pilot tones. In other embodiments, one bit may indicate (a) ability to process shifted pilot tones (for both channel estimate and phase rotation updates) or (b) inability to process shifted pilot tones where the bit, if set to 1, (a) is met, if set to 0, (b) is met, or vice versa.

**[0025]** The communications device **1010** may transmit the OFDM packet one symbol after the other sequentially and every N symbols, the location of the pilot tones within the OFDM packet may change either sequentially or randomly. In some embodiments, the location of the pilot tones may shift by one symbol index, or, in other words, from one sub-carrier to the adjacent sub-carrier.

**[0026]** Pilot tone shifting is a process where the pilot tones are sequentially assigned to different subcarriers as a function of time. In many embodiments, only a subset of subcarriers may be used for pilot tones or data purposes (usable subcarriers). For example, the pilot tones may be used only on

data sub-carriers (e.g., sweep through with the pilot tone on a symbol by symbol basis), may avoid nulled sub-carriers (e.g., DC subcarriers and guard subcarriers), and, in some embodiments, may even avoid data tones that are adjacent to guard or DC subcarriers, and further in some embodiments may avoid a subset of data tones, for example all even numbered tones.

**[0027]** The pilot tones and their positioning can be based on channel conditions such as coding scheme, packet length, and the like. FIG. 1A depicts a table **1100** of pilot tone positions demarked by symbol numbers. The following discussion is for the 1 MHz systems and is an example. There are other allocations for the 2, 4, 8 and 16 MHz bandwidths.

**[0028]** The table **1100** shows a progression of symbols transmitted from the communications device **1010** to the communications device **1030** from 1 to 13. The number of symbols is chosen in the present embodiment based upon the number of data and pilot tones, or useable sub-carriers, and the pattern of pilot locations in relation to the symbol indices repeat after 13. In particular, the table **1100** illustrates two pilot tones for each symbol number. One pilot tone travels between the  $-13$  subcarrier index and a  $-1$  subcarrier index and the second pilot tone travels between the  $1$  subcarrier index and the  $13$  subcarrier index. For example, the first symbol transmitted may be symbol 1, which has two pilot tones, one located at the subcarrier index  $-8$  and one located at the subcarrier index  $6$ . The second symbol transmitted may then be symbol 2 with pilot tones at the subcarrier indices  $-9$  and  $5$ . The third symbol transmitted may then be symbol 3 with pilot tones at the subcarrier indices  $-10$  and  $4$  and these pilot tone shifts continue through symbol 13 at which the pilot tones are at the subcarrier indices  $-7$  and  $7$ .

**[0029]** As shown in the table **1100**, the pilot tones are shifted or assigned to different tones or frequency bins as a function of time, which is referred to herein as the locations of the pilot tone. The time between shifts in the location of the pilot tones is  $N=1$  in table **1100** so the pilot tones shift between locations between every symbol. The table **1100** also illustrates the pilot tones changing by one subcarrier index at a time and sequentially. However, not all embodiments may implement a location for pilot tones as a function of time that results in the pilot tones shifting through sub-carriers or frequencies sequentially. In other words, the location of the pilot tones may shift every  $N$  symbols  $\{N=1, 2, 3, 4, \dots, 8, \dots\}$  but the shift in frequency/location may be random within the subset of sub-carriers rather than sequential. The shifting of the pilot tones can also be based on the modulation and coding scheme (MCS) used for transmission or on the packet length of the transmission (i.e., channel conditions). Further, the amount of time the one or more pilot tones occupy a particular location may be based on a modulation and coding scheme (MCS) and the MCS may be selected based on a data rate and a level of robustness required by traffic type. After a set of pilot tones are assigned, the process illustrated by the table **1100** is cyclic and may be repeated over any number of symbols greater than 13.

**[0030]** The communications device **1030** may detect the start of a packet and begin to receive the transmission from the communications device **1010** and the communications device **1030** may implement multi-mode phase tracking logic **1043** to determine how to implement phase tracking for the incoming signal. The communications device **1010** may also implement multi-mode phase tracking logic **1023** to determine how to implement phase tracking for incoming signals.

**[0031]** The bandwidth detection logic **1045** of the RX/TX module **1040** may first begin to receive the preamble of the OFDM transmission including a short training field, a long training field, and a signal field. In 1 MHz transmissions, the fields may be repeated to increase the range of the transmission so the communications device **1030** may determine that the OFDM transmission is a 1 MHz bandwidth transmission by the presence of another Binary Phase Key Shift (BPSK) modulated long training field whereas the 2 MHz bandwidth or greater transmission may comprise a Quadrature Binary Phase Key Shift (QBPSK) modulated signal field at the corresponding time-wise symbol in the transmission.

**[0032]** In other embodiments, the bandwidth detection logic **1045** may determine whether the transmission is a 1 MHz bandwidth transmission or a 2 MHz bandwidth or greater transmission based upon the property of orthogonal long training sequences in the long training field.

**[0033]** For situations in which the transmission is a 2 MHz bandwidth or greater, the multi-mode phase tracking logic **1043** may proceed with normal processing. For instance, some embodiments may determine channel estimates from the pilot tones or by another method to update the weight coefficients for equalization. And several embodiments may determine phase correction information based upon the rotations of the pilot tones for phase tracking.

**[0034]** For situations in which the transmission is a 1 MHz bandwidth transmission, the bandwidth detection logic **1045** may instruct the multi-mode phase tracking logic **1043** to modify the parameters of the phase tracking algorithm or otherwise provide an indication to the multi-mode phase detection logic **1043** that the incoming OFDM transmission is a 1 MHz bandwidth transmission. In particular, the multi-mode phase tracking logic **1043** may implement a phase correction estimate with a least squares or phase averaging calculation such as WLS (weighted least squares), Simple phase averaging, Weighted phase averaging, Minimum Mean-Square Error (MMSE) Weighted phase averaging, or the like.

**[0035]** The multi-mode phase tracking logic **1043** may determine a phase correction estimate by estimating the residual carrier frequency offset and the residual timing error through calculation of the intercept and the slope from the plot of received pilot tones, in doing so, the multi-mode phase tracking logic **1043** uses pilot information that are obtained from one OFDM symbol or combination of the information obtained from two or more received OFDM symbols. A weight may be assigned to information obtained from each pilot subcarrier during since the subcarriers suffer from different levels of fading and thus may have different signal-to-noise ratio (SNR).

**[0036]** In many embodiments, the multi-mode phase tracking logic **1043** may determine a phase correction estimate by jointly estimating the residual carrier frequency offset and the residual timing error using weighted least-squares algorithm where weights are derived optimally based on the estimated channel information and the receiver thermal noise. In some embodiments, the multi-mode phase tracking logic **1043** may determine the phase correction estimate by determining the average phase estimates of pilot tones in the same OFDM symbol. In several embodiments, the multi-mode phase tracking logic **1043** may determine the phase correction estimate by determining a weighted phase averaging, which uses the magnitude square channel estimates for pilot tones in an OFDM symbol to determine a weighted average. And, in

some embodiments, the multi-mode phase tracking logic **1043** may provide an MMSE weighted phase averaging that includes the magnitude square channel estimates added to the square noise variance. Note that, in many embodiments, the channel estimates may be the initial channel estimates determined from the long training field. In other embodiments, the channel estimates may be updated based upon the shifting pilot tones.

[**0037**] After determining the phase correction estimate, the multi-mode phase tracking logic **1043** may multiply the calculated intercept and/or slope values by a multiplier, the multiplier for the intercept has value of 0.2 for a 1 MHz bandwidth transmission, whereas it may be 0.5 for a 2 MHz bandwidth or greater transmission. The multi-mode phase tracking logic **1043** may add the final product to the prior integrated phase correction and integrate to determine the current integrated phase correction that can be applied to the data subcarriers in the OFDM symbol.

[**0038**] In the present embodiment, the multi-mode phase tracking logic **1043** may receive the OFDM packet with pilot tones distributed across the bandwidth of the OFDM packet in accordance with the table **1100**. The table **1100** provides an example of a pattern for 1 MHz system (32 tones FFT, 24 data and 2 pilot tones in each symbol) based on which pilots travel one symbol to another covering the entire bandwidth after 13 symbols. The pattern repeats periodically to cover all symbols in a packet.

[**0039**] FIG. 1B illustrates an embodiment of the OFDM packet **1200** transmitted from the communications device **1010** to the communications device **1030**. The OFDM module **1022** may generate different OFDM symbols for different bandwidths such as 2 MHz, 4 MHz, 8 MHz, and 16 MHz and may generate the OFDM packet **1200** for a 1 MHz bandwidth channel, for transceivers such as the transceivers of FIG. 1, corresponding to a 32-point, inverse Fourier transform. The OFDM packet **1200** comprises 32 tones, also referred to as sub-carriers, indexed from -16 to 15. The 32 tones, in this embodiment, include 24 data tones, five guard tones, two pilot tones, and one direct current (DC) tone. The four lowest frequency tones are guard tones provided for filter ramp up and filter ramp down. The index zero frequency tone is the DC tone and is nulled, at least in part, to better enable the receivers to employ direct-conversion receivers to reduce complexity. As per a commonplace practice, the DC is selected to be one of the two subcarriers closest to the middle of the frequency band. And the data and pilot frequency tones are provided between indices -13 through -1 and indices 1 through 13.

[**0040**] The RF receiver comprises an OFDM module **1042**, which receives electromagnetic energy at an RF frequency and extracts the digital data therefrom. For 1 MHz operation, OFDM **1042** may extract OFDM symbols comprising 24 data tones, five guard tones, and one DC tone such as the OFDM symbol **1210** illustrated in FIG. 1B. In other embodiments, the OFDM symbols may be encoded in other manners with different numbers of data tones, pilot tones, and guard tones.

[**0041**] Note that the OFDM packet **1200** comprises OFDM symbols **1210**, **1220**, **1230**, through **1240** and the OFDM symbols correspond to the pilot tone pattern illustrated in table **1100**. In particular, the OFDM symbols **1210-1240** illustrate a dot for each of the guard tones, which are also referred to as edge tones. There is one dot in the center of the symbols **1210-1240** illustrating the position of the DC tone as subcarrier index 0, and the DATA/PILOT TONES are demarked with numbers that start at the subcarrier index -13

on the left side through the -1 index next to the DC tone at the 0 index, and continue with index 1 adjacent to the DC index 0 through the index 13 adjacent to the guard tones on the right side.

[**0042**] The OFDM symbol **1220** illustrates the OFDM symbol index 6 in table **1100** and the pilot tones are the emboldened arrows at subcarrier indices {-13, 1}. Note that the OFDM symbol **1210** has pilot tones {-1, 13} adjacent to the DC tone and the guard tones. The OFDM symbol **1220** has pilot tones {-13, 1} adjacent to the DC tone and the guard tones. The OFDM symbol **1230** has pilot tones {-12, 2} adjacent to the location of the symbol index that is adjacent to the DC tone and the guard tones. And, the OFDM symbol **1230** has pilot tones {-2, 12} adjacent to the location of the symbol index that is adjacent to the DC tone and the guard tones.

[**0043**] In further embodiments, the communications device **1010** may facilitate data offloading. For example, communications devices that are low power sensors may include a data offloading scheme to, e.g., communicate via Wi-Fi, another communications device, a cellular network, or the like for the purposes of reducing power consumption consumed in waiting for access to, e.g., a metering station and/or increasing availability of bandwidth. Communications devices that receive data from sensors such as metering stations may include a data offloading scheme to, e.g., communicate via Wi-Fi, another communications device, a cellular network, or the like for the purposes of reducing congestion of the network **1005**.

[**0044**] The network **1005** may represent an interconnection of a number of networks. For instance, the network **1005** may couple with a wide area network such as the Internet or an intranet and may interconnect local devices wired or wirelessly interconnected via one or more hubs, routers, or switches. In the present embodiment, network **1005** communicatively couples communications devices **1010**, **1030**, **1050**, and **1055**.

[**0045**] The communication devices **1010** and **1030** comprise memory **1011** and **1031**, medium access control (MAC) sublayer logic **1018** and **1038**, and physical layer (PHY) logic **1019** and **1039**, respectively. The memory **1011** and **1031** may comprise a storage medium such as dynamic random access memory (DRAM), read only memory (ROM), buffers, registers, cache, flash memory, hard disk drives, solid-state drives, or the like. The memory **1011** and **1031** may store frames and/or frame structures, or portions thereof such as structures for an association request frame, an association response frame, a probe frame, and the like.

[**0046**] The MAC sublayer logic **1018**, **1038** may comprise logic to implement functionality of the MAC sublayer of the data link layer of the communications device **1010**, **1030**. The MAC sublayer logic **1018**, **1038** may generate the frames and the physical layer logic **1019**, **1039** may generate physical layer protocol data units (PPDUs) based upon the frames. For example, the frame builder may generate frames **1014**, **1034**. The physical layer logic **1019**, **1039** may encapsulate the frames with preambles to generate PPDUs for transmission via a physical layer device such as the transceivers represented by receive/transmit chains (RX/TX) **1020** and **1040**.

[**0047**] The communications devices **1010**, **1030**, **1050**, and **1055** may each comprise a transceiver (RX/TX) such as transceivers (RX/TX) **1020** and **1040**. In many embodiments, transceivers **1020** and **1040** implement orthogonal frequency-division multiplexing (OFDM). OFDM is a method

of encoding digital data on multiple carrier frequencies. OFDM is a frequency-division multiplexing scheme used as a digital multi-carrier modulation method. A large number of closely spaced orthogonal sub-carrier signals are used to carry data as OFDM symbols. The OFDM symbols are divided into several parallel data streams or channels, one for each sub-carrier and encoded with the sub-carriers by which the OFDM symbols will be transmitted to a receiving device such as twenty-four data sub-carriers, five guard sub-carriers, two pilot sub-carriers, and one DC subcarrier. Each sub-carrier is modulated with a modulation scheme at a low symbol rate, maintaining total data rates similar to conventional single-carrier modulation schemes in the same bandwidth.

**[0048]** An OFDM system uses several carriers, or “tones,” for functions including data, pilot, guard, and nulling. Data tones are used to transfer information between the transmitter and receiver via one of the channels. Pilot tones are used to maintain the channels, and may provide information about time/frequency and channel tracking. And guard tones may help the signal conform to a spectral mask. The nulling of the direct component (DC) may be used to simplify direct conversion receiver designs. And guard intervals may be inserted between symbols such as between every OFDM symbol as well as between the short training field (STF) and long training field (LTF) symbols by the front-end of the transmitter during transmission to avoid inter-symbol interference (ISI), which might result from multi-path distortion.

**[0049]** Each transceiver **1020**, **1040** comprises an RF transmitter and an RF receiver. The RF transmitter comprises an OFDM module **1022**, which impresses digital data, OFDM symbols encoded with tones, onto RF frequencies, also referred to as sub-carriers, for transmission of the data by electromagnetic radiation. In the present embodiment, the OFDM module **1022** may impress the digital data as OFDM symbols encoded with tones onto the sub-carriers to for transmission.

**[0050]** FIG. 1 may depict a number of different embodiments including a Multiple-Input, Multiple-Output (MIMO) system with, e.g., four spatial streams, and may depict degenerate systems in which one or more of the communications devices **1010**, **1030**, **1050**, and **1055** comprise a receiver and/or a transmitter with a single antenna including a Single-Input, Single Output (SISO) system, a Single-Input, Multiple Output (SIMO) system, and a Multiple-Input, Single Output (MISO) system. In the alternative, FIG. 1 may depict transceivers that include multiple antennas and that may be capable of multiple-user MIMO (MU-MIMO) operation.

**[0051]** The antenna array **1024** is an array of individual, separately excitable antenna elements. The signals applied to the elements of the antenna array **1024** cause the antenna array **1024** to radiate one to four spatial channels. Each spatial channel so formed may carry information to one or more of the communications devices **1030**, **1050**, and **1055**. Similarly, the communications device **1030** comprises a transceiver **1040** to receive and transmit signals from and to the communications device **1010**. The transceiver **1040** may comprise an antenna array **1044**.

**[0052]** FIGS. 1C-E depict embodiments of simulations **1300**, **1400**, and **1500** comparing the phase tracking with different intercept multiplier values at 1 MHz (megahertz), 2 MHz, and 4 MHz bandwidths. To study phase tracking in 1 MHz, we implemented an advanced algorithm where pilot tones across frequency (in one OFDM symbol) and time (two or more OFDM symbols) are weighted and averaged to pro-

vide optimum weighted least-square tracking. A simulation study was conducted to determine the optimum setting for the bandwidth of tracking loop, and to obtain the operating intercept multiplier value to be configured for tracking algorithm. FIG. 1C and FIG. 1D show Packet Error Rate (PER) vs. Signal-to-Noise Ratio (SNR) simulations results for 2 MHz and 4 MHz bandwidth operations, respectively, and are compared to FIG. 1E for 1 MHz operation. These simulation cases were for modulation and coding scheme one (MCS1) for IEEE 802.11ah devices, which is QPSK with a coding rate of  $\frac{1}{2}$ , using a 36 symbol (228-byte in 2 MHz bandwidth, and 482-byte in 4 MHz bandwidth, and 104-byte in 1 MHz bandwidth) packet using all simulation impairments and a carrier offset of  $-13.675$  parts-per-million (ppm).

**[0053]** By studying the performance curves in figures below, it is observed that in 2 MHz and 4 MHz bandwidths where 4 or more pilot tones are dedicated for tracking, setting the larger value for the intercept multiplier (and therefore the coefficient of the tracking loop bandwidth), improved performance is obtained. This is consistent with IEEE 802.11n/11ac implementation where an intercept multiplier value is set to 0.5 (blue curve) for all bandwidths. However, it is observed that for 1 MHz operation, because only 2 pilot tones exist, the behavior of tracking algorithm is different, and setting a value of 0.5 will cause almost 2 dB loss of SNR at 1% PER, and even more than 4 decibel (dB) loss at 0.1% PER SNR.

**[0054]** Note that without adaptive change of track parameter implemented by the multi-mode phase tracking logic such as the multi-mode phase tracking logic **1043** and **1023**, the 1 MHz IEEE 802.11ah receiver will suffer at least 2 dB degradation in SNR at 1% PER; defeating one of the main purposes of 1 MHz bandwidth operation that is extended range. Implementation of alternative track algorithms for 1 MHz, e.g., interpolation across time and frequency, may require more silicon area in multi-mode devices as well as additional implementation and validation time.

**[0055]** FIG. 2 depicts an embodiment of an apparatus to generate, transmit, receive, and interpret or decode frames. The apparatus comprises a transceiver **200** coupled with Medium Access Control (MAC) sublayer logic **201** and a physical layer (PHY) logic **250**. The MAC sublayer logic **201** may determine a frame and the physical layer (PHY) logic **250** may determine the PPDU by encapsulating the frame or multiple frames, MAC protocol data units (MPDUs), with a preamble to transmit via transceiver **200**. For example, a frame builder may generate a frame including a type field that specifies whether the frame is a management, control or data frame and a subtype field to specify the function of the frame. A control frame may include a Ready-To-Send or Clear-To-Send frame. A management frame may comprise a Beacon, Probe Response, Association Response, and Reassociation Response frame type. And the data type frame is designed to transmit data.

**[0056]** In many embodiments, the MAC sublayer logic **201** may comprise a frame builder **202** to generate frames. The PHY logic **250** may comprise a data unit builder **203**. The data unit builder **203** may determine a preamble to encapsulate the MPDU or more than one MPDUs to generate a PPDU. In many embodiments, the data unit builder **203** may create the preamble based upon communications parameters chosen through interaction with a destination communications device.

[0057] The transceiver 200 comprises a receiver 204 and a transmitter 206. The transmitter 206 may comprise one or more of an encoder 208, a modulator 210, an OFDM 212, and a DBF 214. The encoder 208 of transmitter 206 receives and encodes data destined for transmission from the MAC sub-layer logic 202 with, e.g., a binary convolutional coding (BCC), a low density parity check coding (LDPC), and/or the like. The modulator 210 may receive data from encoder 208 and may impress the received data blocks onto a sinusoid of a selected frequency via, e.g., mapping the data blocks into a corresponding set of discrete amplitudes of the sinusoid, or a set of discrete phases of the sinusoid, or a set of discrete frequency shifts relative to the frequency of the sinusoid.

[0058] The output of modulator 209 is fed to an orthogonal frequency division multiplexing (OFDM) module 212. The OFDM module 212 may comprise a space-time block coding (STBC) module 211, a digital beamforming (DBF) module 214, and an inverse, fast Fourier transform (IFFT) module 215. The STBC module 211 may receive constellation points from the modulator 209 corresponding to one or more spatial streams and may spread the spatial streams to a greater number of space-time streams (also generally referred to as data streams). In some embodiments, the STBC 211 may be controlled to pass through the spatial streams for situations in which, e.g., the number of spatial streams is the maximum number of space-time streams. Further embodiments may omit the STBC.

[0059] The OFDM module 212 impresses or maps the modulated data formed as OFDM symbols onto a plurality of orthogonal sub-carriers so the OFDM symbols are encoded with the sub-carriers or tones. The OFDM module 212 may generate symbols in which the pilot tones change location within the data/pilot sub-carriers every N symbols. In many embodiments, the OFDM module 212 may generate symbols in which the pilot tones shift locations along the symbol indices sequentially. In several embodiments, the pilot tones may shift locations every symbol. For instance, when the communications device 1030 in FIG. 1 responds to a transmission from the communications device 1010, the RX/TX 1040 may respond with OFDM packets in which the pilot tones shift every N symbols. In some embodiments, the value of N may match the value of N provided by the communications device 1010. In other embodiments, the value of N may be a fixed value for the communications device 1030 and/or for the communications device 1010. And, in some embodiments, the communications device 1010, which may be the access point, may use a value of N provided to the communications device 1010 by the communications device 1030.

[0060] In some embodiments, the OFDM symbols are fed to the Digital Beam Forming (DBF) module 214. Digital beam forming techniques may be employed to increase the efficiency and capacity of a wireless system. Generally, digital beam forming uses digital signal processing algorithms that operate on the signals received by, and transmitted from, an array of antenna elements. For example, a plurality of spatial channels may be formed and each spatial channel may be steered independently to maximize the signal power transmitted to and received from each of a plurality of user terminals. Further, digital beam forming may be applied to minimize multi-path fading and to reject co-channel interference.

[0061] The OFDM module 212 may also comprise an inverse Fourier transform module that performs an inverse discrete Fourier transform (IDFT) on the OFDM symbols. In the present embodiment, the IDFT may comprise the IFFT

module 215, to perform an IFFT on the data. For 1 MHz bandwidth operation, the IFFT module 215 performs a 32-point, inverse FFT on the data streams.

[0062] The output of the OFDM module 212 may be up-converted to a higher carrying frequency or may be performed integrally with up-conversion. Shifting the signal to a much higher frequency before transmission enables use of an antenna array of practical dimensions. That is, the higher the transmission frequency, the smaller the antenna can be. Thus, an up-converter multiplies the modulated waveform by a sinusoid to obtain a signal with a carrier frequency that is the sum of the central frequency of the waveform and the frequency of the sinusoid.

[0063] The transceiver 200 may also comprise duplexers 216 connected to antenna array 218. Thus, in this embodiment, a single antenna array is used for both transmission and reception. When transmitting, the signal passes through duplexers 216 and drives the antenna with the up-converted information-bearing signal. During transmission, the duplexers 216 prevent the signals to be transmitted from entering receiver 204. When receiving, information bearing signals received by the antenna array pass through duplexers 216 to deliver the signal from the antenna array to receiver 204. The duplexers 216 then prevent the received signals from entering transmitter 206. Thus, duplexers 216 operate as switches to alternately connect the antenna array elements to the receiver 204 and the transmitter 206.

[0064] The antenna array 218 radiates the information bearing signals into a time-varying, spatial distribution of electromagnetic energy that can be received by an antenna of a receiver. The receiver can then extract the information of the received signal. In other embodiments, the transceiver 200 may comprise one or more antennas rather than antenna arrays and, in several embodiments, the receiver 204 and the transmitter 206 may comprise their own antennas or antenna arrays.

[0065] The transceiver 200 may comprise a receiver 204 for receiving, demodulating, and decoding information bearing communication signals. The communication signals may comprise, e.g., 32 tones on a 1 MHz carrier frequency with pilot tones that shift every N symbols. For example, a data collection station compliant with IEEE 802.11ah for a farm may periodically receive data from low power sensors that have integrated wireless communications devices compliant with IEEE 802.11ah. The sensors may enter a low power mode for a period of time, wake to collect data periodically, and communicate with the data collection station periodically to transmit the data collected by the sensor. In some embodiments, the sensor may proactively initiate communications with the data collection station, transmit data indicative of a communications capability, and begin communicating the data to the data collection station in response to a clear-to-send (CTS) or the like. In other embodiments, the sensor may transmit data to the data collection station in response to initiation of communications by the data collection station.

[0066] The receiver 204 may comprise a fast Fourier transform (FFT) module 219. The FFT module 219 may transform the communication signals from the time domain to the frequency domain. In other embodiments, a multi-mode phase tracking logic 250 may operate at least in part on time domain signals prior to performing the fast Fourier transform.

[0067] In many embodiments, the receiver 204 may comprise a bandwidth detection logic 270. The bandwidth detection logic 270 may detect and receive the OFDM signal and

may determine whether the incoming OFDM transmission is a 1 MHz bandwidth transmission or a 2 MHz or greater bandwidth transmission. In some embodiments, the bandwidth detection logic 270 may perform at least some processing of the preamble to determine the bandwidth of the OFDM transmission. During the processing of the preamble, the bandwidth detection logic 270 may detect a property of the orthogonal long training sequences or detect the BSPK modulated long training sequence in the 1 MHz bandwidth preamble at a point that the 2 MHz or greater bandwidths may have a QPSK modulated signal field. In response to determining whether the incoming signal is a 1 MHz bandwidth or a 2 MHz or greater bandwidth signal, the bandwidth detection logic 270 may inform the multi-mode phase tracking logic 250 so the multi-mode phase tracking logic 250 may set the track intercept multiplier value to 0.2 for the 1 MHz bandwidth signal or 0.5 for the 2 MHz or greater bandwidth signal.

[0068] In several embodiments, the bandwidth detection logic 270 may provide an indication to the multi-mode phase tracking logic 250 only when the incoming OFDM transmission is a 1 MHz bandwidth transmission because the multi-mode phase tracking logic 250 may, by default, be set up to process 2 MHz or greater bandwidth transmissions. In other embodiments, the bandwidth detection logic 270 may provide an indication to the multi-mode phase tracking logic 250 of the bandwidth for each incoming signal and the multi-mode phase tracking logic 250 may select the appropriate track intercept multiplier. In still other embodiments, the bandwidth detection logic 270 may provide an indication to the multi-mode phase tracking logic 250 only when the incoming OFDM transmission is a 2 MHz or greater bandwidth transmission because the multi-mode phase tracking logic 250 may, by default, be set up to process 1 MHz bandwidth transmissions. In further embodiments, the default bandwidth mode of the multi-mode phase tracking logic 250 may be set by a user as a software switch or a hardware switch or may be set by the manufacturer at the time of deployment or distribution.

[0069] The receiver 204 may comprise the multi-mode phase tracking logic 250 comprising a channel estimator 252, a phase tracker 254, parameters 256, and an equalizer 258. The multi-mode phase tracking logic 250 may be configured for processing shifting pilot tones as well as data tones. The receiver 204 may comprise an equalizer 258 with hard-coded logic or running an equalizer application or instructions, a channel estimator 252, and a phase tracker 254.

[0070] The multi-mode phase tracking logic 250 may comprise filters, delay elements, and taps or other logic to apply weighting functions to the input signal based upon weight values determined and updated from processing the pilot tones in the incoming signal. The weight coefficients for the weighting functions are weight values which may be adjusted based on the pilot tones to achieve a specific level of performance, and to, e.g., optimize signal quality at the receiver. In some embodiments, the multi-mode phase tracking logic 250 is able to track channel changes over time (i.e., using the pilot tones to update the equalizer weight coefficients) because of the rotation of the pilot tones through each of the OFDM subcarriers over the OFDM packet through time. As noted above, the pilot tones are separated by some number of data subcarriers so that estimation of slope and intercept for sub-carrier tracking could be maintained. As the pilot tones are shifted through symbol index locations across the band of the

OFDM packet, the weight coefficients for the equalizer for the subcarriers that the pilot tones currently populate may be updated as well.

[0071] The receiver 204 may receive and convert the pilot tones to a baseband representation. The received pilot tones may then be input into the channel estimator 252 that uses the received sequences to determine updated channel estimates for the wireless channel (using, for example, a least squares approach). The channel estimator 252 may have a priori knowledge of the transmitted pilot tones, which it compares to the received signals to determine the channel estimates. The channel estimates may then be delivered to the equalizer 258.

[0072] The baseband representation of the received data signals may be delivered to the input of the equalizer 258, which filters the signals in a manner dictated by the weighting function in accordance with the weight coefficients currently being applied to the equalizer 258. The equalizer 258 may include any type of equalizer structure (including, for example, a transversal filter, a maximum likelihood sequence estimator (MLSE), and others). When properly configured, the equalizer 258 may reduce or eliminate undesirable channel effects within the received signals (e.g., inter-symbol interference).

[0073] The received data signals with pilot tones are also delivered to the input of the phase tracker 254, which uses the received signals to track the phase of the subcarriers. The phase tracking is dependent upon the bandwidth of the signal and the multi-mode phase tracking logic 250 may select the parameters such as the track intercept multiplier value from the parameters 256 to implement in the phase tracker 254 based upon the bandwidth of the incoming OFDM signal. The multi-mode phase tracking logic 250 may begin processing the preamble of the signal including the long training sequences to generate initial channel estimates for the equalizer 258.

[0074] After the bandwidth detection logic 270 determines the bandwidth of the incoming signal and the multi-mode phase tracking logic 250 sets the track intercept multiplier value accordingly, the multi-mode phase tracking logic 250 may process the pilot tones to determine phase correction information from the subcarriers to apply to the data signals and may, in some embodiments, determine updated channel estimates for the equalizer 256.

[0075] The receiver 204 may also comprise an OFDM module 222, a demodulator 224, a deinterleaver 225, and a decoder 226, and the equalizer 258 may output the weighted data signals for the OFDM packet to the OFDM module 222. The OFDM 222 extracts signal information as OFDM symbols from the plurality of subcarriers onto which information-bearing communication signals are modulated. For instance, the OFDM symbols may comprise data associated with 24 data sub-carriers, two pilot sub-carriers, five guard sub-carriers, and one DC sub-carrier.

[0076] The OFDM module 222 may comprise a DBF module 220, and an STBC module 221. The received signals are fed from the equalizer to the DBF module 220 transforms N antenna signals into L information signals. And the STBC module 221 may transform the data streams from the space-time streams to spatial streams. In one embodiment, the demodulation is performed in parallel on the output data of the FFT. In another embodiment, a separate demodulator 224 performs demodulation separately.

[0077] The demodulator 224 demodulates the spatial streams. Demodulation is the process of extracting data from the spatial streams to produce demodulated spatial streams. The method of demodulation depends on the method by which the information is modulated onto the received carrier signal and such information is included in the transmission vector (TXVECTOR) included in the communication signal. Thus, for example, if the modulation is BPSK, demodulation involves phase detection to convert phase information to a binary sequence. Demodulation provides to the deinterleaver 225 a sequence of bits of information.

[0078] The deinterleaver 225 may deinterleave the sequence of bits of information. For instance, the deinterleaver 225 may store the sequence of bits in columns in memory and remove or output the bits from the memory in rows to deinterleave the bits of information. The decoder 226 decodes the deinterleaved data from the demodulator 224 and transmits the decoded information, the MPDU, to the MAC sublayer logic 202.

[0079] Persons of skill in the art will recognize that a transceiver may comprise numerous additional functions not shown in FIG. 2 and that the receiver 204 and transmitter 206 can be distinct devices rather than being packaged as one transceiver. For instance, embodiments of a transceiver may comprise a Dynamic Random Access Memory (DRAM), a reference oscillator, filtering circuitry, synchronization circuitry, an interleaver and a deinterleaver, possibly multiple frequency conversion stages and multiple amplification stages, etc. Further, some of the functions shown in FIG. 2 may be integrated. For example, digital beam forming may be integrated with orthogonal frequency division multiplexing.

[0080] The MAC sublayer logic 201 may decode or parse the MPDU or MPDUs to determine the particular type of frame or frames included in the MPDU(s).

[0081] FIGS. 3A-B depict embodiments of flowcharts for bandwidth detection and multi-mode phase tracking to process pilot tones and to generate, transmit, receive, parse, and interpret communications. Referring to FIG. 3A, the flowchart 300 may begin with receiving an OFDM packet with pilot tones that shift locations of across the bandwidth of the packet periodically. In many embodiments, the OFDM packet may be received one symbol at a time and the pilot tones may shift to a new location every N symbols, where N may be a settable, calculated or fixed value. Thus, the pilot tones' locations may remain constant for N symbols before shifting to the next location. In some embodiments, the value of N is fixed at one and, in other embodiments, the value of N is fixed at two.

[0082] After the receiver begins to receive the OFDM packet, the receiver may detect and begin to process the preamble including the long training sequences in the long training fields (element 302). The receiver may detect the energy of the signal and may process a long training field in the preamble to determine initial weight coefficients for equalization.

[0083] While processing the preamble, the receiver may perform a 1 MHz classification (element 305). For instance, the receiver may detect the property of orthogonal long training field sequences in the OFDM transmission indication that the transmission is a 1 MHz bandwidth transmission. In other embodiments, the receiver may detect that after two long training sequences the subsequent symbol is modulated with Binary Phase Shift Keying (BPSK) rather than Quadrature

Binary Phase Shift Keying (QBPSK) indicating that the transmission is a 1 MHz bandwidth transmission.

[0084] If the transmission is a 1 MHz bandwidth transmission (element 310), the set the track intercept field multiplier value to 0.2 and proceed with the rest of the receive procedures. On the other hand, if the transmission is a 2 MHz bandwidth transmission or greater, the receiver may follow the normal receive procedures. For instance, if the transmission is a 2 MHz bandwidth transmission or greater, the receiver may set the track intercept multiplier value to, e.g., 0.5, and proceed with determining a phase correction estimate, integrating the phase correction estimate to determine the integrated phase correction or phase rotations, and apply the integrated phase correction to the phase.

[0085] Referring to FIG. 3B, the flowchart 350 begins with a receiver of a station such as the receiver 204 in FIG. 2 receiving a communication signal via one or more antenna(s) such as an antenna element of antenna array 218 (element 355). The communication signal may comprise the pilot tones that shift to new locations every N symbols. Thus, the pilot tone location remains constant for N symbols, then shifts to the next location. The receiver may then use the N pilot symbols to make a channel estimate using an appropriate algorithm. In many embodiments, the receiver may not process or may not use information determined from processing pilot tones adjacent to DC or edge tones.

[0086] The receiver may convert the communication signal into one or more MPDUs in accordance with the process described in the preamble (element 360). More specifically, the received signal may be fed from the one or more antennas to a bandwidth detection logic such as the bandwidth detection logic 270 in FIG. 1 to detect at least the difference between the 1 MHz bandwidth and the 2 MHz or greater bandwidth transmissions. Then the signal is fed to a multi-mode phase tracking logic such as multi-mode phase tracking logic 250 for equalization and phase correction and then to a DBF such as the DBF 220. The DBF transforms the signals into information signals. The output of the DBF is fed to OFDM such as the OFDM 222. The OFDM extracts signal information from the plurality of subcarriers onto which information-bearing signals are modulated. Then, the demodulator such as the demodulator 224 demodulates the signal information via, e.g., BPSK, 16-QAM, 64-QAM, 256-QAM, QPSK, or SQPSK. And the decoder such as the decoder 226 decodes the signal information from the demodulator via, e.g., BCC or LDPC, to extract the one or more MPDUs (element 360) and transmits the one or more MPDUs to MAC sublayer logic such as MAC sublayer logic 202 (element 365).

[0087] The MAC sublayer logic may parse and interpret the frame in each of the MPDUs. For instance, the MAC sublayer logic may parse and interpret the frame to determine the frame type and subtype.

[0088] The following examples pertain to further embodiments. One example comprises a method. The method may involve receiving an orthogonal frequency division multiplexing (OFDM) transmission of a packet with pilot tones shifting locations between of OFDM symbols; processing a preamble of the packet to determine whether the OFDM transmission comprises a one megahertz bandwidth or comprises a bandwidth of two megahertz or greater; setting a track intercept multiplier value to 0.2 if the OFDM transmission comprises a one megahertz bandwidth; determining phase



rotations of the pilot tones based upon the track intercept multiplier value; and performing phase tracking based upon the phase rotations.

**[0089]** In some embodiments, the method may further comprise processing a long training field in the preamble to determine initial weight coefficients for equalization. In some embodiments, the method may further comprise determining channel estimates for the pilot tones based upon the pilot tones; and updating weight coefficients for equalization based upon the channel estimates. In many embodiments, receiving the OFDM transmission comprises receiving the OFDM transmission with pilot tones shifting every N OFDM symbols. In several embodiments, receiving the OFDM transmission comprises receiving the OFDM transmission with pilot tones shifting every OFDM symbol. In several embodiments, determining phase rotations of the pilot tones based upon the track intercept multiplier value comprises determining the phase rotations of the pilot tones based upon an track intercept multiplier value 0.5 if the OFDM transmission comprises the bandwidth of two megahertz or greater. In several embodiments, determining phase rotations of the pilot tones based upon the track intercept multiplier value comprises integrating previous phase rotations plus a current phase estimate multiplied by the track intercept multiplier value. And, in some embodiments, integrating previous phase rotations plus a current phase estimate multiplied by the track intercept multiplier value comprises determining the current phase estimate by performing a least squares or phase averaging algorithm on the pilot tones.

**[0090]** Another example comprises an apparatus. The apparatus may comprise a memory to store an intercept multiplier value; and logic coupled with the memory to receive an orthogonal frequency division multiplexing (OFDM) transmission of a packet with pilot tones shifting locations between of OFDM symbols; process a preamble of the packet to determine whether the OFDM transmission comprises a one megahertz bandwidth or comprises a bandwidth of two megahertz or greater; set a track intercept multiplier value to 0.2 if the OFDM transmission comprises a one megahertz bandwidth; determine phase rotations of the pilot tones based upon the track intercept multiplier value; and perform phase tracking based upon the phase rotations.

**[0091]** In some embodiments, the logic comprises logic to process a long training field in the preamble to determine initial weight coefficients for equalization. In some embodiments, the logic comprises logic to determine channel estimates for the pilot tones based upon the pilot tones; and update weight coefficients for equalization based upon the channel estimates. In some embodiments, the logic comprises logic to receive the OFDM transmission with pilot tones shifting every N OFDM symbols. In some embodiments, the logic comprises logic to receive the OFDM transmission with pilot tones shifting every OFDM symbol. In some embodiments, the logic comprises logic to determine the phase rotations of the pilot tones based upon an track intercept multiplier value 0.5 if the OFDM transmission comprises the bandwidth of two megahertz or greater. In some embodiments, the logic comprises logic to integrate previous phase rotations plus a current phase estimate multiplied by the track intercept multiplier value. And, in some embodiments, the logic comprises logic to determine the current phase estimate by performing a least squares or phase averaging algorithm on the pilot tones.

**[0092]** Another example comprises a system. The system may comprise memory to store an intercept multiplier value; and a receiver coupled with the memory to receive an orthogonal frequency division multiplexing (OFDM) transmission of a packet with pilot tones shifting locations between of OFDM symbols; process a preamble of the packet to determine whether the OFDM transmission comprises a one megahertz bandwidth or comprises a bandwidth of two megahertz or greater; set a track intercept multiplier value to 0.2 if the OFDM transmission comprises a one megahertz bandwidth; determine phase rotations of the pilot tones based upon the track intercept multiplier value; and perform phase tracking based upon the phase rotations; and a transmitter to transmit a second OFDM transmission with pilot tones shifting locations.

**[0093]** In some embodiments, the receiver comprises logic to process a long training field in the preamble to determine initial weight coefficients for equalization. In some embodiments, the receiver comprises logic to determine channel estimates for the pilot tones based upon the pilot tones; and update weight coefficients for equalization based upon the channel estimates. In some embodiments, the receiver comprises logic to receive the OFDM transmission with pilot tones shifting every N OFDM symbols. In some embodiments, the receiver comprises logic to receive the OFDM transmission with pilot tones shifting every OFDM symbol. In some embodiments, the receiver comprises logic to determine the phase rotations of the pilot tones based upon an track intercept multiplier value 0.5 if the OFDM transmission comprises the bandwidth of two megahertz or greater. In some embodiments, the receiver comprises logic to integrate previous phase rotations plus a current phase estimate multiplied by the track intercept multiplier value. And, in some embodiments, the receiver comprises logic to determine the current phase estimate by performing a least squares or phase averaging algorithm on the pilot tones.

**[0094]** The following examples pertain to further embodiments. One example comprises a machine-accessible product. The machine-accessible product may comprise a medium containing instructions for multi-mode phase tracking with shifting pilot tones, wherein the instructions, when executed by the access point, causes the access point to perform operations, the operations comprising receiving an orthogonal frequency division multiplexing (OFDM) transmission of a packet with pilot tones shifting locations between of OFDM symbols; processing a preamble of the packet to determine whether the OFDM transmission comprises a one megahertz bandwidth or comprises a bandwidth of two megahertz or greater; setting a track intercept multiplier value to 0.2 if the OFDM transmission comprises a one megahertz bandwidth; determining phase rotations of the pilot tones based upon the track intercept multiplier value; and performing phase tracking based upon the phase rotations.

**[0095]** In some embodiments, the operations may further comprise processing a long training field in the preamble to determine initial weight coefficients for equalization. In some embodiments, the operations may further comprise determining channel estimates for the pilot tones based upon the pilot tones; and updating weight coefficients for equalization based upon the channel estimates. In many embodiments, determining the phase rotations of the pilot tones based upon an track intercept multiplier value 0.5 if the OFDM transmission comprises the bandwidth of two megahertz or greater. In several embodiments, integrating previous phase rotations

plus a current phase estimate multiplied by the track intercept multiplier value. And, in some embodiments, determining the current phase estimate by performing a least squares or phase averaging algorithm on the pilot tones.

**[0096]** In some embodiments, some or all of the features described above and in the claims may be implemented in one embodiment. For instance, alternative features may be implemented as alternatives in an embodiment along with logic or selectable preference to determine which alternative to implement. Some embodiments with features that are not mutually exclusive may also include logic or a selectable preference to activate or deactivate one or more of the features. For instance, some features may be selected at the time of manufacture by including or removing a circuit pathway or transistor. Further features may be selected at the time of deployment or after deployment via logic or a selectable preference such as a dipswitch or the like. A user after via a selectable preference such as a software preference, an e-fuse, or the like may select still further features.

**[0097]** A number of embodiments may have one or more advantageous effects. For instance, some embodiments may offer reduced MAC header sizes with respect to standard MAC header sizes. Further embodiments may include one or more advantageous effects such as smaller packet sizes for more efficient transmission, lower power consumption due to less data traffic on both the transmitter and receiver sides of communications, less traffic conflicts, less latency awaiting transmission or receipt of packets, and the like.

**[0098]** Another embodiment is implemented as a program product for implementing systems, apparatuses, and methods described with reference to FIGS. 1-4. Embodiments can take the form of an entirely hardware embodiment, a software embodiment implemented via general purpose hardware such as one or more processors and memory, or an embodiment containing both specific-purpose hardware and software elements. One embodiment is implemented in software or code, which includes but is not limited to firmware, resident software, microcode, or other types of executable instructions.

**[0099]** Furthermore, embodiments can take the form of a computer program product accessible from a machine-accessible, computer-usable, or computer-readable medium providing program code for use by or in connection with a computer, mobile device, or any other instruction execution system. For the purposes of this description, a machine-accessible, computer-usable, or computer-readable medium is any apparatus or article of manufacture that can contain, store, communicate, propagate, or transport the program for use by or in connection with the instruction execution system or apparatus.

**[0100]** The medium may comprise an electronic, magnetic, optical, electromagnetic, or semiconductor system medium. Examples of a machine-accessible, computer-usable, or computer-readable medium include memory such as volatile memory and non-volatile memory. Memory may comprise, e.g., a semiconductor or solid-state memory like flash memory, magnetic tape, a removable computer diskette, a random access memory (RAM), a read-only memory (ROM), a rigid magnetic disk, and/or an optical disk. Current examples of optical disks include compact disk-read only memory (CD-ROM), compact disk-read/write memory (CD-R/W), digital video disk (DVD)-read only memory (DVD-ROM), DVD-random access memory (DVD-RAM), DVD-Recordable memory (DVD-R), and DVD-read/write memory (DVD-R/W).

**[0101]** An instruction execution system suitable for storing and/or executing program code may comprise at least one processor coupled directly or indirectly to memory through a system bus. The memory may comprise local memory employed during actual execution of the code, bulk storage such as dynamic random access memory (DRAM), and cache memories which provide temporary storage of at least some code in order to reduce the number of times code must be retrieved from bulk storage during execution.

**[0102]** Input/output or I/O devices (including but not limited to keyboards, displays, pointing devices, etc.) can be coupled to the instruction execution system either directly or through intervening I/O controllers. Network adapters may also be coupled to the instruction execution system to enable the instruction execution system to become coupled to other instruction execution systems or remote printers or storage devices through intervening private or public networks. Modem, Bluetooth™, Ethernet, Wi-Fi, and WiDi adapter cards are just a few of the currently available types of network adapters.

What is claimed is:

1. A method to multi-mode phase track with shifting pilot tones, the method comprising:
  - receiving an orthogonal frequency division multiplexing (OFDM) transmission of a packet with pilot tones shifting locations between OFDM symbols;
  - processing a preamble of the packet to determine whether the OFDM transmission comprises a one megahertz bandwidth or comprises a bandwidth of two megahertz or greater;
  - setting a track intercept multiplier value to 0.2 if the OFDM transmission comprises a one megahertz bandwidth;
  - determining phase rotations of the pilot tones based upon the track intercept multiplier value; and
  - performing phase tracking based upon the phase rotations.
2. The method of claim 1, further comprising processing a long training field in the preamble to determine initial weight coefficients for equalization.
3. The method of claim 1, further comprising:
  - determining channel estimates for the pilot tones based upon the pilot tones; and
  - updating weight coefficients for equalization based upon the channel estimates.
4. The method of claim 1, wherein receiving the OFDM transmission comprises receiving the OFDM transmission with pilot tones shifting every N OFDM symbols.
5. The method of claim 1, wherein receiving the OFDM transmission comprises receiving the OFDM transmission with pilot tones shifting every OFDM symbol.
6. The method of claim 1, wherein determining phase rotations of the pilot tones based upon the track intercept multiplier value comprises determining the phase rotations of the pilot tones based upon an track intercept multiplier value 0.5 if the OFDM transmission comprises the bandwidth of two megahertz or greater.
7. The method of claim 1, wherein determining phase rotations of the pilot tones based upon the track intercept multiplier value comprises integrating previous phase rotations plus a current phase estimate multiplied by the track intercept multiplier value.
8. The method of claim 7, wherein integrating previous phase rotations plus a current phase estimate multiplied by the track intercept multiplier value comprises determining the

current phase estimate by performing a least squares or phase averaging algorithm on the pilot tones.

**9.** A device to multi-mode phase track with shifting pilot tones, the device comprising:

memory to store an intercept multiplier value; and

logic coupled with the memory to receive an orthogonal frequency division multiplexing (OFDM) transmission of a packet with pilot tones shifting locations between of OFDM symbols; process a preamble of the packet to determine whether the OFDM transmission comprises a one megahertz bandwidth or comprises a bandwidth of two megahertz or greater; set a track intercept multiplier value to 0.2 if the OFDM transmission comprises a one megahertz bandwidth; determine phase rotations of the pilot tones based upon the track intercept multiplier value; and perform phase tracking based upon the phase rotations.

**10.** The device of claim **9**, wherein the logic comprises logic to process a long training field in the preamble to determine initial weight coefficients for equalization.

**11.** The device of claim **9**, wherein the logic comprises logic to determine channel estimates for the pilot tones based upon the pilot tones; and update weight coefficients for equalization based upon the channel estimates.

**12.** The device of claim **9**, wherein the logic comprises logic to receive the OFDM transmission with pilot tones shifting every N OFDM symbols.

**13.** The device of claim **9**, wherein the logic comprises logic to receive the OFDM transmission with pilot tones shifting every OFDM symbol.

**14.** The device of claim **9**, wherein the logic comprises logic to determine the phase rotations of the pilot tones based upon an track intercept multiplier value 0.5 if the OFDM transmission comprises the bandwidth of two megahertz or greater.

**15.** The device of claim **9**, wherein the logic comprises logic to integrate previous phase rotations plus a current phase estimate multiplied by the track intercept multiplier value.

**16.** The device of claim **9**, wherein the logic comprises logic to determine the current phase estimate by performing a least squares or phase averaging algorithm on the pilot tones.

**17.** A system to multi-mode phase track with shifting pilot tones, the system comprising:

a processor, a radio, and one or more antennas coupled with the radio;

memory coupled with the processor to store an intercept multiplier value; and

a receiver coupled with the memory to receive an orthogonal frequency division multiplexing (OFDM) transmission of a packet with pilot tones shifting locations between of OFDM symbols; process a preamble of the packet to determine whether the OFDM transmission comprises a one megahertz bandwidth or comprises a bandwidth of two megahertz or greater; set a track intercept multiplier value to 0.2 if the OFDM transmission comprises a one megahertz bandwidth; determine phase rotations of the pilot tones based upon the track intercept multiplier value; and perform phase tracking based upon the phase rotations; and

a transmitter coupled with the radio to transmit a second OFDM transmission with pilot tones shifting locations.

**18.** The system of claim **17**, wherein the receiver comprises logic to process a long training field in the preamble to determine initial weight coefficients for equalization.

**19.** The system of claim **17**, wherein the receiver comprises logic to determine channel estimates for the pilot tones based upon the pilot tones; and update weight coefficients for equalization based upon the channel estimates.

**20.** The system of claim **17**, wherein the receiver comprises logic to receive the OFDM transmission with pilot tones shifting every N OFDM symbols.

**21.** The system of claim **17**, wherein the receiver comprises logic to receive the OFDM transmission with pilot tones shifting every OFDM symbol.

**22.** The system of claim **17**, wherein the receiver comprises logic to determine the phase rotations of the pilot tones based upon an track intercept multiplier value 0.5 if the OFDM transmission comprises the bandwidth of two megahertz or greater.

**23.** The system of claim **17**, wherein the receiver comprises logic to integrate previous phase rotations plus a current phase estimate multiplied by the track intercept multiplier value.

**24.** The system of claim **17**, wherein the receiver comprises logic to determine the current phase estimate by performing a least squares or phase averaging algorithm on the pilot tones.

**25.** A machine-accessible product comprising:

a medium containing instructions to multi-mode phase track with shifting pilot tones, wherein the instructions, when executed by a station, causes the station to perform operations, the operations comprising:

receiving an orthogonal frequency division multiplexing (OFDM) transmission of a packet with pilot tones shifting locations between of OFDM symbols;

processing a preamble of the packet to determine whether the OFDM transmission comprises a one megahertz bandwidth or comprises a bandwidth of two megahertz or greater;

setting a track intercept multiplier value to 0.2 if the OFDM transmission comprises a one megahertz bandwidth;

determining phase rotations of the pilot tones based upon the track intercept multiplier value; and

performing phase tracking based upon the phase rotations.

**26.** The machine accessible product of claim **25**, wherein the operations further comprise processing a long training field in the preamble to determine initial weight coefficients for equalization.

**27.** The machine accessible product of claim **25**, wherein the operations further comprise:

determining channel estimates for the pilot tones based upon the pilot tones; and

updating weight coefficients for equalization based upon the channel estimates.

**28.** The machine accessible product of claim **27**, wherein determining phase rotations of the pilot tones based upon the track intercept multiplier value comprises determining the phase rotations of the pilot tones based upon an track intercept multiplier value 0.5 if the OFDM transmission comprises the bandwidth of two megahertz or greater.

**29.** The machine accessible product of claim **25**, wherein determining phase rotations of the pilot tones based upon the track intercept multiplier value comprises integrating previous phase rotations plus a current phase estimate multiplied by the track intercept multiplier value.

30. The machine accessible product of claim 25, wherein integrating previous phase rotations plus a current phase estimate multiplied by the track intercept multiplier value comprises determining the current phase estimate by performing a least squares or phase averaging algorithm on the pilot tones.

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