

(19) World Intellectual Property Organization  
International Bureau



(43) International Publication Date  
18 October 2007 (18.10.2007)

PCT

(10) International Publication Number  
**WO 2007/118121 A2**

- (51) International Patent Classification:  
*H01L 31/0264* (2006.01)
- (21) International Application Number:  
PCT/US2007/065964
- (22) International Filing Date: 4 April 2007 (04.04.2007)
- (25) Filing Language: English
- (26) Publication Language: English
- (30) Priority Data:  
60/789,730 5 April 2006 (05.04.2006) US
- (71) Applicant (for all designated States except US): **SILICON GENESIS CORPORATION** [US/US]; 61 Daggett Drive, San Jose, California 95134 (US).

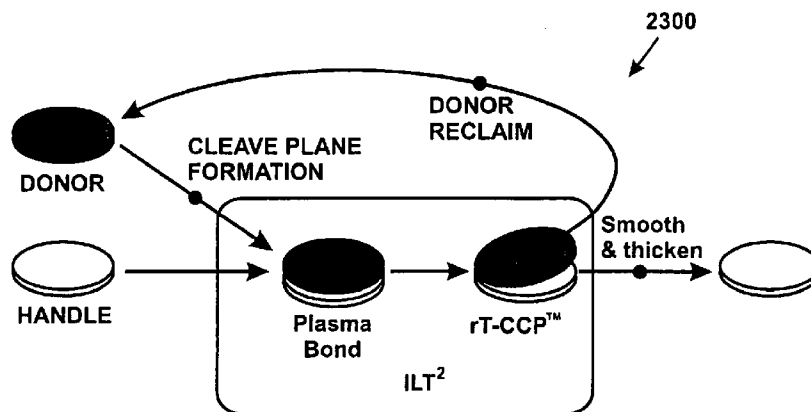
- (81) Designated States (unless otherwise indicated, for every kind of national protection available): AE, AG, AL, AM, AT, AU, AZ, BA, BB, BG, BH, BR, BW, BY, BZ, CA, CH, CN, CO, CR, CU, CZ, DE, DK, DM, DZ, EC, EE, EG, ES, FI, GB, GD, GE, GH, GM, GT, HN, HR, HU, ID, IL, IN, IS, JP, KE, KG, KM, KN, KP, KR, KZ, LA, LC, LK, LR, LS, LT, LU, LY, MA, MD, MG, MK, MN, MW, MX, MY, MZ, NA, NG, NI, NO, NZ, OM, PG, PH, PL, PT, RO, RS, RU, SC, SD, SE, SG, SK, SL, SM, SV, SY, TJ, TM, TN, TR, TT, TZ, UA, UG, US, UZ, VC, VN, ZA, ZM, ZW.
- (84) Designated States (unless otherwise indicated, for every kind of regional protection available): ARIPO (BW, GH, GM, KE, LS, MW, MZ, NA, SD, SL, SZ, TZ, UG, ZM, ZW), Eurasian (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European (AT, BE, BG, CH, CY, CZ, DE, DK, EE, ES, FI, FR, GB, GR, HU, IE, IS, IT, LT, LU, LV, MC, MT, NL, PL, PT, RO, SE, SI, SK, TR), OAPI (BF, BJ, CF, CG, CI, CM, GA, GN, GQ, GW, ML, MR, NE, SN, TD, TG).

- (72) Inventor; and
- (75) Inventor/Applicant (for US only): **HENLEY, Francois, J.** [US/US]; 525 Shadowmere Way, Aptos, California 95003 (US).
- (74) Agents: **TOBIN, Kent, J.** et al.; Two Embarcadero Center, 8th Floor, San Francisco, California 94111-3834 (US).

Published:  
— without international search report and to be republished upon receipt of that report

For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.

(54) Title: METHOD AND STRUCTURE FOR FABRICATING SOLAR CELLS USING A LAYER TRANSFER PROCESS



(57) Abstract: A reusable silicon substrate device for use with layer transfer process. The device has a reusable substrate having a surface region, a cleave region, and a total thickness of material. The total thickness of material is at least N times greater than a first thickness of material to be removed. In a specific embodiment, the first thickness of material to be removed is between the surface region and the cleave region, whereupon N is an integer greater than about ten. The device also has a chuck member adapted to hold a handle substrate member in place. The chuck member is configured to hold the handle substrate in a manner to facilitate bonding the handle substrate to the first thickness of material to be removed. In a preferred embodiment, the device has a mechanical pressure device operably coupled to the chuck member. The mechanical pressure device is adapted to provide a force to cause bonding of the handle substrate to the first thickness of material to be removed.

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## METHOD AND STRUCTURE FOR FABRICATING SOLAR CELLS USING A LAYER TRANSFER PROCESS

### CROSS-REFERENCES TO RELATED APPLICATIONS

[0001] The instant nonprovisional patent application claims priority to U.S. Provisional Patent Application No. 60/789,730, filed April 5, 2006 and incorporated by reference in its entirety herein for all purposes.

### BACKGROUND OF THE INVENTION

[0002] Embodiments in accordance with the present invention generally relate to techniques including a method and a structure for forming a solar cell structure using layer transfer techniques for photovoltaic applications. But it will be recognized that the invention has a wider range of applicability; it can also be applied to other types of applications such as for three-dimensional packaging of integrated semiconductor devices, photonic devices, piezoelectronic devices, flat panel displays, microelectromechanical systems ("MEMS"), nano-technology structures, sensors, actuators, integrated circuits, biological and biomedical devices, and the like.

[0003] From the beginning of time, human beings have relied upon the "sun" to derive almost all useful forms of energy. Such energy comes from petroleum, radiant, wood, and various forms of thermal energy. As merely an example, human being have relied heavily upon petroleum sources such as coal and gas for much of their needs. Unfortunately, such petroleum sources have become depleted and have lead to other problems. As a replacement, in part, solar energy has been proposed to reduce our reliance on petroleum sources. As merely an example, solar energy can be derived from "solar cells" commonly made of silicon.

[0004] The silicon solar cell generates electrical power when exposed to solar radiation from the sun. The radiation interacts with atoms of the silicon and forms electrons and holes that migrate to p-doped and n-doped regions in the silicon body and create voltage differentials and an electric current between the doped regions. Depending upon the application, solar cells have been integrated with concentrating elements to improve efficiency. As an example, solar radiation accumulates and focuses using concentrating

elements that direct such radiation to one or more portions of active photovoltaic materials. Although effective, these solar cells still have many limitations.

[0005] As merely an example, solar cells rely upon starting materials such as silicon. Such silicon is often made using either polysilicon and/or single crystal silicon materials. These materials are often difficult to manufacture. Polysilicon cells are often formed by manufacturing polysilicon plates. Although these plates may be formed effectively, they do not possess optimum properties for highly effective solar cells. Single crystal silicon has suitable properties for high grade solar cells. Such single crystal silicon is, however, expensive and is also difficult to use for solar applications in an efficient and cost effective manner. Generally, thin-film solar cells are less expensive by using less silicon material but their amorphous or polycrystal structure are less efficient than the more expensive bulk silicon cells made from single-crystal silicon substrates. These and other limitations can be found throughout the present specification and more particularly below.

[0006] From the above, it is seen that a technique for manufacturing large substrates which is cost effective and efficient is desirable.

#### BRIEF SUMMARY OF THE INVENTION

[0007] According to embodiments of the present invention, techniques directed to the manufacture of photovoltaic materials are provided. More particularly, the invention provides a technique including a method and a structure for forming a solar cell structure using layer transfer techniques for photovoltaic applications. But it will be recognized that the invention has a wider range of applicability; it can also be applied to other types of applications such as for three-dimensional packaging of integrated semiconductor devices, photonic devices, piezoelectronic devices, flat panel displays, microelectromechanical systems ("MEMS"), nano-technology structures, sensors, actuators, integrated circuits, biological and biomedical devices, and the like.

[0008] In a specific embodiment, the present invention provides a method for fabricating a photovoltaic cell, e.g., solar cell, solar panel. Embodiments of the method include providing a reusable substrate having a surface region, a cleave region, and a total thickness of material. In a preferred embodiment, the total thickness of material is at least N times greater than a first thickness of material to be removed, which is between the surface region and the cleave region. The method includes coupling (e.g., Tin Oxide, Indium Tin Oxide (ITO), Titanium

Dioxide, Zinc Oxide (ZnO) or other dielectric stack formation material, spin on glass (SOG), or other suitable materials) the surface region of the reusable substrate to a first surface region of an optically transparent substrate, which has the first surface region and a second surface region. The method includes subjecting a voltage differential (e.g., 0.1 Volts to about 100 Volts) between a first region and a second region of the reusable substrate to cause an increase in energy at one or more portions of the cleave region to facilitate removal of the first thickness of material from the reusable substrate. Depending upon the embodiment, the voltage differential is selected to achieve a desired result. In one embodiment, the surface region remains coupled to the first surface region, to form a cleaved surface region coupled to the first surface region of the optically transparent substrate.

[0009] In an alternative specific embodiment, the present invention provides a reusable silicon substrate device for use with layer transfer process, e.g., controlled cleaving, process called Smart Cut™ by Soitec, SA. The device includes a reusable substrate having a surface region, a cleave region, and a total thickness of material, which is preferably at least N times greater than a first thickness of material to be removed. The first thickness of material to be removed is between the surface region and the cleave region. In a specific embodiment, N is an integer greater than about ten, but can be others. In a specific embodiment, N can be about 1000 times or greater as compared to a thickness of material being transferred. The device also has a first electrode member coupled to a first region of the reusable substrate and a second electrode member coupled to a second region of thereusable substrate. The device has a voltage source coupled between the first electrode member and the second electrode member to provide a voltage differential between the first region of the cleave region and the second region of the cleave region to cause an increase in energy at one or more portions of the cleave region to facilitate removal of the first thickness of material from the reusable substrate.

[0010] In an alternative specific embodiment, the present invention provides a cluster tool apparatus, e.g., multi-chambered. The tool has a first chamber, which has a reusable silicon substrate device for use with layer transfer process. The device has a reusable substrate having a surface region, a cleave region, and a total thickness of material, the total thickness of material being at least N times greater than a first thickness of material to be removed. In a specific embodiment, N can be about 1000 times or greater as compared to a thickness of material being transferred. In a specific embodiment, the first thickness of material to be removed is between the surface region and the cleave region, whereupon N is an integer

greater than about ten, but can be others, e.g., one thousand. A first electrode member is coupled to a first region of thereusable substrate. A second electrode member is coupled to a second region of the reusable substrate. In a specific embodiment, the device has a voltage source coupled between the first electrode member and the second electrode member to provide a voltage differential between the first region and the second region to cause an increase in energy at one or more portions of the cleave region to facilitate removal of the first thickness of material from the reusable substrate. In a specific embodiment, cluster tool has a second chamber coupled to the first chamber. The second chamber is provided to perform one or more processes.

[0011] In yet an alternative embodiment, the present invention provides a reusable silicon substrate device for use with layer transfer process. The device has a reusable substrate having a surface region, a cleave region, and a total thickness of material. The total thickness of material is at least N times greater than a first thickness of material to be removed. In a specific embodiment, the first thickness of material to be removed is between the surface region and the cleave region, whereupon N is an integer greater than about ten. In a specific embodiment, N can be about 1000 times or greater as compared to a thickness of material being transferred. The device also has a chuck member adapted to hold a handle substrate member in place. The chuck member is configured to hold the handle substrate in a manner to facilitate bonding the handle substrate to the first thickness of material to be removed. In a preferred embodiment, the device has a mechanical pressure device operably coupled to the chuck member. The mechanical pressure device is adapted to provide a force to cause bonding of the handle substrate to the first thickness of material to be removed.

[0012] Still further, embodiments in accordance with the present invention provide a method fabricating one or more semiconductor substrates, e.g., silicon on glass. The method includes providing a reusable substrate having a surface region, a cleave region, and a total thickness of material. In a specific embodiment, the total thickness of material is at least N times greater than a first thickness of material to be removed, which is between the surface region and the cleave region. The method also includes subjecting a voltage differential between a first region and a second region of the reusable substrate to cause an increase in energy at one or more portions of the surface region to facilitate bonding of the surface region to a first surface region of a handle substrate.

[0013] In still a further embodiment the present invention provides a method fabricating one or more semiconductor substrates, e.g., silicon on glass. The method includes providing a reusable substrate having a surface region, a cleave region, and a total thickness of material. In a specific embodiment, the total thickness of material is at least N times greater than a first thickness of material to be removed, which is between the surface region and the cleave region. The method includes subjecting a voltage differential between a first region and a second region of the reusable substrate to cause an increase in energy at one or more portions of the cleave region to change a characteristic of the cleave region from a first characteristic to a second characteristic. In a specific embodiment, the second characteristic causes removal of the first thickness of material from the reusable substrate.

[0014] Numerous benefits may be achieved over pre-existing techniques using the present invention. In particular, embodiments in accordance with the present invention use controlled energy and selected conditions to preferentially cleave a thin photovoltaic film onto a glass substrate according to a specific embodiment. In a specific embodiment, the present method and device provides a very high quality photovoltaic material on glass, which can be used as a portion of the packaging material. In a preferred embodiment, the present method and structure provide for single crystal silicon from a larger reusable bulk substrate member for providing efficient power using photovoltaic cells. In other embodiments, the present method and device provide an efficient technique for large scale manufacturing of solar cells and/or panels. Depending upon the embodiment, one or more of these benefits may be achieved. These and other benefits may be described throughout the present specification and more particularly below.

[0015] Embodiments in accordance with the present invention achieve these benefits and others in the context of known process technology. However, a further understanding of the nature and advantages of the present invention may be realized by reference to the latter portions of the specification and attached drawings.

#### BRIEF DESCRIPTION OF THE DRAWINGS

[0016] Figures 1 through 14 illustrate a method for fabricating a photovoltaic device according to an embodiment of the present invention;

[0017] Figures 15 to 18 illustrate simplified diagrams of solar cell configurations according to embodiments of present invention;

[0018] Figure 19 is a simplified diagram of a solar cell having a reflective surface region according to an embodiment of the present invention;

[0019] Figure 20 is a simplified diagram of a solar cell having a lens or light redirection region according to an embodiment of the present invention;

[0020] Figure 21 is a simplified diagram of a solar cell having a light redirection region coupled to a waveguide light trapping mode within the thin-film cell thickness according to an embodiment of the present invention;

[0021] Figure 22 is a simplified diagram illustrating a method of using a donor substrate as a donor block for layer transfer according to an embodiment of the present invention;

[0022] Figure 23 is a simplified diagram of a method using an integrated layer transfer tool according to an embodiment of the present invention;

[0023] Figure 24 is a more detailed diagram of a method of using an integrated layer transfer tool according to an embodiment of the present invention;

[0024] Figure 25 is a top-view diagram of a cluster tool for layer transfer according to an embodiment of the present invention;

[0025] Figure 26 is a simplified diagram illustrating an alternative method of using a donor substrate according to an embodiment of the present invention;

[0026] Figure 27 is a simplified diagram illustrating yet an alternative method of using a donor substrate according to an embodiment of the present invention;

[0027] Figure 28 is a simplified diagram illustrating still an alternative method of using a donor substrate according to an embodiment of the present invention;

[0028] Figure 29 is a conventional process for forming conventional photovoltaic cells; and

[0029] Figure 30 is a simplified diagram of a process of forming a photovoltaic cell according to an embodiment of the present invention.

[0030] Figure 31 is a simplified diagram of a process of forming a module including a plurality of photovoltaic cells according to an embodiment of the present invention.

[0031] Figure 32 is a simplified diagram of a process of forming a module including a plurality of photovoltaic cells according to an alternative embodiment of the present invention.

[0032] Figure 33 is a simplified diagram of a conventional process flow for forming raw material for incorporation into a solar apparatus.

[0033] Figure 34 is a simplified diagram of a conventional process flow for forming raw material for incorporation into a solar apparatus in accordance with an embodiment of the present invention.

[0034] Figure 35 is a simplified diagram of an apparatus in accordance with an embodiment of the present invention for controlling a temperature of substrates during bonding or layer transfer processes.

[0035] Figure 35A is a simplified diagram of an apparatus in accordance with an embodiment of the present invention for controlling a temperature during singulation of a film from a parent bulk material.

[0036] Figure 36 shows a simplified flow diagram of an embodiment of a method in accordance with the present invention for fabricating a solar module comprising a plurality of individual photovoltaic cells.

#### DETAILED DESCRIPTION OF THE INVENTION

[0037] According to embodiments of the present invention, techniques directed to the manufacture of photovoltaic materials are provided. More particularly, embodiments of the present invention provide a technique including a method and a structure for forming a solar cell structure using layer transfer techniques for photovoltaic applications. But it will be recognized that the invention has a wider range of applicability; it can also be applied to other types of applications such as for three-dimensional packaging of integrated semiconductor devices, photonic devices, piezoelectronic devices, flat panel displays, microelectromechanical systems ("MEMS"), nano-technology structures, sensors, actuators, integrated circuits, biological and biomedical devices, and the like.

[0038] A method of manufacturing a photovoltaic layer on a semiconductor substrate is briefly outlined below.

1. Provide a semiconductor substrate, which has a surface region, a cleave region and a first thickness of material to be removed between the surface region and the cleave region;



2. Align the semiconductor substrate to an optically transparent substrate;
3. Couple the surface region of the semiconductor substrate to a first surface region of the optically transparent substrate using an organic and/or inorganic film (e.g., silicon on glass);
4. Initiate a controlled cleaving action on a portion of the cleave region;
5. Cleave the semiconductor substrate to remove the first thickness of material from the semiconductor substrate, while the surface region remains coupled to the first surface region, to cause formation of a cleaved surface region; and
6. Optionally, form a second thickness of semiconductor material overlying the cleaved surface region to form a resulting thickness of semiconductor material, which has one or more photovoltaic regions;
7. Provide a cover glass material overlying the second thickness of semiconductor material; and
8. Perform other steps, as desired.

**[0039]** The above sequence of steps provides a method according to an embodiment of the present invention. As shown, the technique includes a method and a structure for forming a solar cell structure using layer transfer techniques for photovoltaic applications. Other alternatives can also be provided where steps are added, one or more steps are removed, or one or more steps are provided in a different sequence without departing from the scope of the claims herein. Alternatively, there can be other ways of forming the structure. That is, the sequence can initially be formed on a cover sheet such as cover glass and other suitable materials and then forming the other layers according to a specific embodiment. The layer transfer occurs on the cover glass, which is used to form the rest of the solar cell device. Other techniques can use transfer substrates that will transfer a layer transferred material onto a handle substrate. Further details of the present method can be found throughout the present specification and more particularly below.

**[0040]** As shown in Figure 1, the method provides a transparent handle substrate, which has a first deflection characteristic, a backside, and a face. The transparent handle substrate can be glass, quartz, polymeric, or other composites, and the like. As merely an example, the transparent substrate has a thickness, a backside surface, and a face. The transparent

substrate is glass, such as those used for covering solar cells or the like. Depending upon the embodiment, the glass is somewhat flexible and should be subjected to a backing plate for rigidity. Of course, there can be other variations, modifications, and alternatives.

**[0041]** In alternative embodiments, the handle substrate can be any homogeneous, graded, or multilayered material, or any combination of these. That is, the handle substrate can be made of almost any monocrystalline, polycrystalline, or even amorphous type substrate. Additionally, the substrate can be made of III/V materials such as gallium arsenide, gallium nitride (GaN), and others. Additionally, the substrate can be silicon carbide, germanium, silicon, glass or quartz combinations, plastics, and polymers, which have flexible characteristics. Preferably, the handle substrate has a somewhat flexible characteristic that is unsuitable for a layer transfer process according to a specific embodiment. The unsuitable nature of the substrate causes excessive roughness, breakage, partial film separation, and the like depending upon the specific embodiment. Any other combinations of materials can also be used, depending upon the specific embodiment.

**[0042]** In one embodiment, the present invention provides a backing substrate to add rigidity to handle substrate structure, as shown in Figure 2. Preferably, the backing substrate has a thickness and material that is adequate to provide an effective deflection characteristic of a multilayered structure composed of at least the backing substrate and handle substrates to be suitable for a thickness of silicon bearing material from the donor substrate to be transferred onto the face of the handle substrate.

**[0043]** As merely an example, the backing substrate is a silicon wafer for the quartz handle substrate. Such backing substrate has a thickness of 725 microns +/- 15 microns and is made of single crystal silicon using, for example, a 200 millimeter donor/handle/backing substrate structures. Such substrate has a Young's modulus of about 130 Giga Pascal. Other types of materials and certain thicknesses such as plastic, metal, glass, quartz, composites, and the like can be used to provide the rigidity to the combined backing and handle substrate structures. Of course, one of ordinary skill in the art would recognize other variations, modifications, and alternatives.

**[0044]** In an optional specific embodiment, the method performs a cleaning and /or activating process (e.g., plasma activated process) on surfaces of the backing and/or transparent handle substrates, as illustrated by Figures 3 and 4. Such plasma activating processes clean and/or activate the surfaces of the substrates. The plasma activated processes

are provided using an oxygen or nitrogen bearing plasma at 20°C - 40°C temperature. The plasma activated processes are preferably carried out in dual frequency plasma activation system manufactured by Silicon Genesis Corporation of San Jose, California. In still other embodiments, there may not be any backing material. Alternatively in still further embodiments, the present method can use a backing material by way of an electrostatic chuck and/or a porous chuck, and the like. Depending upon the specific embodiment, the present backing material can be provided on either the handle or donor substrate or both the handle and donor substrates. Of course, there can be other variations, modifications, and alternatives.

**[0045]** Referring to Figure 5, the method initiates engagement of the backing substrate to the backside of the transparent handle substrate that is often physically separated from other structures. The method preferably temporarily attaches the backing substrate to the backside of the transparent handle substrate to firmly engage the backing substrate to the transparent handle substrate to form a multilayered structure. As merely an example, the silicon wafer backing substrate firmly attaches to the quartz plate without any other modifications and/or alterations according to a preferred embodiment. Here, the silicon wafer has a very thin coating of native oxide, which bonds to surfaces of the quartz plate, although there may be embodiments without such native oxide, as shown in Figure 6. In other embodiments, bonding can occur using an electrostatic process or web bonding, including covalent bonding, any combination of these and the like. In yet alternative embodiments, the bonding can also occur using a spin on glass, glue layer, any combination of these, and the like. Of course, there can be other variations, modifications, and alternatives.

**[0046]** As shown, the method includes providing a donor substrate comprising a cleave region, a face, a backside, and a thickness of silicon bearing material between the face and the cleave region, as shown in Figure 7. As merely an example, the donor substrate can be a silicon wafer, a germanium wafer, silicon germanium materials, silicon carbide bearing materials, Group III/V compounds, any combination of these, and others. In a preferred embodiment, the donor substrate is made using a photosensitive material. Of course there can be other variations, modifications, and alternatives.

**[0047]** Depending upon the embodiment, the cleave region can be formed using a variety of techniques. That is, the cleave region can be formed using any suitable combination of implanted particles, deposited layers, diffused materials, patterned regions, and other

techniques. Referring to Figure 6, the method introduces certain energetic particles using an implant process through a top surface of a donor substrate to a selected depth, which defines a thickness of the material region, termed the "thin film" of material. A variety of techniques can be used to implant the energetic particles into the silicon wafer. These techniques include ion implantation using, for example, beam line ion implantation equipment manufactured from companies such as Applied Materials, Inc. and others. Alternatively, implantation occurs using a plasma immersion ion implantation ("PIII") technique, ion shower, and other non-mass specific techniques. Combination of such techniques may also be used. Of course, techniques used depend upon the application.

**[0048]** Depending upon the application, smaller mass particles are generally selected to reduce a possibility of damage to the material region according to a preferred embodiment. That is, smaller mass particles easily travel through the substrate material to the selected depth without substantially damaging the material region that the particles traverse through. For example, the smaller mass particles (or energetic particles) can be almost any charged (e.g., positive or negative) and or neutral atoms or molecules, or electrons, or the like. In a specific embodiment, the particles can be neutral and or charged particles including ions such as ions of hydrogen and its isotopes, rare gas ions such as helium and its isotopes, and neon, or others depending upon the embodiment. The particles can also be derived from compounds such as gases, e.g., hydrogen gas, water vapor, methane, and hydrogen compounds, and other light atomic mass particles. Alternatively, the particles can be any combination of the above particles, and or ions and or molecular species and or atomic species. The particles generally have sufficient kinetic energy to penetrate through the surface to the selected depth underneath the surface.

**[0049]** Using hydrogen as the implanted species into the silicon wafer as an example, the implantation process is performed using a specific set of conditions. Implantation dose ranges from about  $1 \times 10^{15}$  to about  $1 \times 10^{18}$  atoms/cm<sup>2</sup>, and preferably the dose is greater than about  $1 \times 10^{16}$  atoms/cm<sup>2</sup>. Implantation energy ranges from about 1 KeV to about 1 MeV, and is generally about 50 KeV. Implantation temperature ranges from about 20 to about 600 Degrees Celsius, and is preferably less than about 400 Degrees Celsius to prevent a possibility of a substantial quantity of hydrogen ions from diffusing out of the implanted silicon wafer and annealing the implanted damage and stress. The hydrogen ions can be selectively introduced into the silicon wafer to the selected depth at an accuracy of about +/-

0.03 to +/-0.05 microns. Of course, the type of ion used and process conditions depend upon the application.

[0050] Effectively, the implanted particles add stress or reduce fracture energy along a plane parallel to the top surface of the substrate at the selected depth. The energies depend, in part, upon the implantation species and conditions. These particles reduce a fracture energy level of the substrate at the selected depth. This allows for a controlled cleave along the implanted plane at the selected depth. Implantation can occur under conditions such that the energy state of the substrate at all internal locations is insufficient to initiate a non-reversible fracture (i.e., separation or cleaving) in the substrate material. It should be noted, however, that implantation does generally cause a certain amount of defects (e.g., micro-defects) in the substrate that can typically at least partially be repaired by subsequent heat treatment, e.g., thermal annealing or rapid thermal annealing. A resulting substrate, which has been subject to implant, is illustrated by the simplified diagram of Figure 7.

[0051] Depending upon the embodiment, there may be other techniques for forming a cleave region and/or cleave layer. As merely an example, such cleave region is formed using other processes, such as those called a Nanocleave<sup>TM</sup> process of Silicon Genesis Corporation of Santa Clara, California, a SmartCut<sup>TM</sup> process of Soitec SA of France, and an Eltran<sup>TM</sup> process of Canon Inc. of Tokyo, Japan, any like processes, and others. Of course, there may be other variations, modifications, and alternatives.

[0052] In a specific embodiment, the transparent handle substrate, which has been coupled to the backing, and donor substrate are both subjected to plasma activated processes, as shown in part in Figure 8. Such plasma activated processes clean and/or activate the surfaces of the substrates. The plasma activated processes are provided using an oxygen or a nitrogen bearing plasma at 20°C to 40°C temperature. The plasma activated processes are preferably carried out in dual frequency plasma activation system manufactured by Silicon Genesis Corporation of San Jose, California. Of course, there can be other variations, modifications, and alternatives, which have been described herein, as well as outside of the present specification.

[0053] Thereafter, each of these substrates is bonded together, as also illustrated by Figure 9. As shown, the handle substrate has been bonded to donor wafer. The substrates are preferably bonded using an EVG 850 bonding tool manufactured by Electronic Vision Group or other like processes. Other types of tools such as those manufactured by Karl Suss may

also be used. Of course, there can be other variations, modifications, and alternatives. Preferably, bonding between the transparent handle substrate and the donor is substantially permanent and has good reliability.

**[0054]** Accordingly after bonding, the bonded structure is subjected to a bake treatment. The bake treatment maintains the bonded substrate at a predetermined temperature and predetermined time. Preferably, the temperature ranges from about 200 or 250 Degrees Celsius to about 400 Degrees Celsius and is preferably about 350 Degrees Celsius for about 1 hour or so for silicon donor substrates and transparent handle substrates. In a specific embodiment, the present bake treatment can occur using a conductive heating process with a hot plate and/or surfaces, which directly couple thermal energy directly from the hot plate to the bonded substrate. In other embodiments, the thermal energy can be provided using radiation, conduction, convection, or any combination of these techniques, and the like. Depending upon the specific application, there can be other variations, modifications, and alternatives.

**[0055]** In a specific embodiment, the substrates are joined or fused together using a low temperature thermal step. The low temperature thermal process generally ensures that the implanted particles do not place excessive stress on the material region, which can produce an uncontrolled cleave action. Another consideration of this step in combination with the plasma-activation surface treatment is to allow the bond strength to be increased to eliminate de-lamination of the assembly during the same bake treatment step, usually caused by stresses induced by the coefficient of thermal expansion mismatch of the dissimilar materials used. In a specific embodiment, the low temperature bonding process occurs by a self-bonding process. In particular, one wafer is stripped to remove oxidation there from (or one substrate is not oxidized). A cleaning solution treats the surface of the wafer to form O--H bonds on the wafer surface. An example of a solution used to clean the wafer is a mixture of hydrogen peroxide and sulfuric acid, and other like solutions. A dryer dries the wafer surfaces to remove any residual liquids or particles from the substrate surfaces. Self-bonding occurs by placing surfaces of cleaned substrates together.

**[0056]** Alternatively, an adhesive disposed on either or both surfaces of the substrates, which bond one substrate to another substrate. In a specific embodiment, the adhesive includes an epoxy, polyimide-type materials, and the like. Spin-on-glass layers can be used to bond one substrate surface onto the face of another. These spin-on-glass ("SOG")

materials include, among others, siloxanes or silicates, which are often mixed with alcohol-based solvents or the like. SOG can be a desirable material because of the low temperatures (e.g., 150 to 250 degree C.) often needed to cure the SOG after it is applied to surfaces of the wafers.

**[0057]** Alternatively, a variety of other low temperature techniques can be used to join the donor wafer to the handle substrates. For instance, an electro-static bonding technique can be used to join the two substrates together. In particular, one or both substrate surface(s) is charged to attract to the other substrate surface. Additionally, the donor substrate can be fused to the handle wafer using a variety of other commonly known techniques. In a specific embodiment, the present bonding process to join the donor and handle substrates together can use an in-situ plasma activated bonding process, an in-situ electro-static bonding process, any combination of these, and the like. Of course, the technique used depends upon the application.

**[0058]** In a preferred embodiment, the method uses an optical coupling material between the two substrates. The optical coupling material is any suitable material that has a refractive index of about 1.8 to about 2.2, but can be others. The material can be selected from Tin Oxide, Indium Tin Oxide (ITO), Zinc Oxide (ZnO), Titanium Dioxide or other dielectric stack formation materials, and the like, including combination of these. Depending upon the embodiment, the material can include one or more layers, and other configurations. Of course, there can be other variations, modifications, and alternatives.

**[0059]** The method performs a controlled cleaving process on the bonded substrate structure, as illustrated by Figures 10 and 11. The controlled cleaving process provided a selected energy within a portion of the cleave region of the donor substrate. As merely an example, the controlled cleaving process has been described in U.S. Patent No. 6,013,563 titled Controlled Cleaving Process, commonly assigned to Silicon Genesis Corporation of San Jose, California, and hereby incorporated by reference for all purposes. Next, the method frees the thickness of material from the donor substrate to completely remove the thickness of material from the donor substrate, as shown by Figure 12.

**[0060]** In an embodiment, the method removes the backing substrate from the transparent handle substrate, as illustrated in Figure 13. In an embodiment, the attachment between the backing substrate and handle substrate is temporary and can be removed with mechanical force without damaging either substrates. In a specific embodiment, a separation process

may be used to detaching the backing substrate from the handle substrate. In a specific embodiment, the backing substrate can also be released when the backing substrate member has been provided using an electrostatic, vacuum, or mechanical chuck and/or attachment device. Of course, there can be other variations, modifications, and alternatives.

**[0061]** Referring to Figure 14, the method forms photovoltaic devices onto surfaces of the thickness of material. Such devices can also include integrated semiconductor devices and photovoltaic devices. Such devices can be made using deposition, etching, implantation, photo masking processes, ink jet printing, screen printing, any combination of these, and the like. Of course, there can be other variations, modifications, and alternatives.

**[0062]** In a specific embodiment, the method can also thicken the thickness of transferred material using a deposition process. In a specific embodiment, the method uses a solid phase epitaxial process and/or other forms of deposition processes. The process can form a suitable single crystal silicon or like material according to a specific embodiment. As merely an example, the material can be amorphous silicon, polycrystalline silicon, germanium and silicon germanium alloy. For example, amorphous silicon could advantageously allow for solid-phase epitaxial growth of single-crystal silicon using the underlying transferred silicon film as a template. Another method that can increase the effective rate of silicon material deposition is to spray or coat the surface with silicon nanoparticles (advantageously amorphous silicon) which can be thermally treated to produce single-crystal silicon using the underlying transferred silicon film as a template. This can be applied dry or using a liquid that would be eliminated during subsequent processing. Polycrystalline silicon and other materials may also allow single-crystal re-growth using appropriate treatments such as laser anneals, flash thermal treatments and the like. Of course, there can be other variations, modifications, and alternatives.

**[0063]** In a specific embodiment, the present method includes an etching and/or deposition process (e.g., plasma assisted deposition) for smoothing the cleaved surface region before any step of forming photovoltaic regions and/or forming the thickened layer. The method can use a smoothing process that includes thermal treatment of the cleaved film using a hydrogen and hydrogen chloride containing environment according to a specific embodiment.

Alternatively, the etchant can be a chemical bath (e.g., KOH, TMAH) to etch the cleaved film to a predetermined amount. The etching process can be used to remove about 300 to about 800 Angstroms of hydrogen damaged silicon, as an example. In a specific embodiment, the



etching process can also be preceded by an oxidation process to convert the hydrogen damaged region into oxide, which is later stripped using a buffered oxide etch and/or other suitable etching species. Of course, there can be other variations, modifications, and alternatives.

**[0064]** In a preferred embodiment, the transferred material is thickened using an amorphous silicon layer. In a preferred embodiment, the amorphous silicon layer is re-crystallized or the like. In a specific embodiment, the amorphous silicon layer is deposited using application of nanoparticles (e.g., amorphous silicon, crystalline silicon, polysilicon, or combinations of these), which are later subjected to a thermal treatment to cause formation of a sheet of thickened material. Alternatively, the amorphous silicon layer can be formed using physical vapor deposition or chemical vapor deposition (e.g., plasma enhanced) at low temperature according to a specific embodiment. In a preferred embodiment, the amorphous silicon layer, which has been deposited overlying a glass material, is maintained at a temperature of less than 500 Degrees Celsius during formation of such silicon layer. In a specific embodiment, the resulting film can be a single crystal and/or polycrystalline structure according to a specific embodiment. In preferred embodiments, the resulting film is single crystalline and has suitable electrical characteristics. Of course, there can be other variations, modifications, and alternatives.

**[0065]** Depending upon the embodiment, the thickened material can be subjected to impurities to form the solar cell structures. In a specific embodiment, the impurities can be in-situ doped, diffused, and/or implanted using ion beams, plasma immersion implantation, ion shower, non-mass separated implantation, substantially or partially non-mass separated, or conventional implantation techniques. These solar cell structures can include impurity regions for P-type and N-type impurities according to a specific embodiment. Of course, there can be other variations, modifications, and alternatives.

**[0066]** In a specific embodiment, the method can also form another layer overlying the thickened layer to form the photovoltaic devices. The other layer can be a semiconductor layer, which can be used to enhance the photovoltaic devices provided for the completed solar cell structure, according to a specific embodiment. In an alternative embodiment, the other layer can be germanium, silicon germanium, II/IV, III/V, any combination of these, and others. The other layer can be used to form another set of photovoltaic regions, which can be

coupled to other photovoltaic devices, to enhance the total photovoltaic intensity. Of course, there can be other variations, modifications, and alternatives.

**[0067]** Depending upon the embodiment, the present method and structure can be formed with a specific thickness of the thickened layer and/or combination of the layer transferred layer and thickened layer. In a specific embodiment, the thickened layer can be about 1 micron and 20 microns using a silicon material. In other embodiments, the thickened layer can be less than 1 micron or greater than 20 microns. In other embodiments, the thickened layer can be less than about 50 microns. Of course, there can be other variations, modifications, and alternatives.

**[0068]** Figures 15 to 18 illustrate simplified diagrams of solar cell configurations according to embodiments of present invention. These diagrams are merely illustrations and should not unduly limit the scope of the claims herein. One of ordinary skill in the art would recognize many variations, modifications, and alternatives. As shown in Figure 15, a first contact layer is formed sandwiched between a glass substrate and a first surface of a semiconductor layer. In a specific embodiment, the contact layer can be made of a suitable material such as a transparent conductive material, such as ITO and the like. Other materials can also be used. The first contact layer couples to a first electrode structure for a photovoltaic cell, which often comprises a p-n junction or multiple p-n junctions. As merely an example, the semiconductor layer may comprise material such as suitable single crystal silicon and others. A second contact layer is formed overlying a second surface of the semiconductor layer. The second contact layer is arranged in a direction parallel to the first contact layer. In a specific embodiment, the second contact layer is patterned to form a plurality of electrodes, which couple to each of the photovoltaic regions. Each of the electrodes can be configured in parallel and/or series depending upon the specific embodiment. Of course, there can be other variations, modifications, and alternatives.

**[0069]** In a specific embodiment, additional junctions can be formed between the second contact layer and the semiconductor substrate to increase efficiency of a solar cell as shown by the simplified diagram of Figure 16. As shown, the additional junctions are provided on a thickened layer overlying the thickness of single crystal silicon material according to a specific embodiment. The additional junctions can be separate from the photovoltaic devices in the thickness of single crystal silicon. Each of these additional junctions can be configured

in parallel and/or series to each other and coupled to the photovoltaic devices in the thickness of silicon material. Of course, there can be other variations, modifications, and alternatives.

[0070] Figure 17 illustrates yet another example of solar cell configuration according to an embodiment of the present invention. As shown, a glass substrate is attached to a first surface of a semiconductor layer. A first contact structure and a second contact structure are formed overlying a second surface of the semiconductor layer. The first contact structure is configured substantially parallel to the second contact structure. As shown, each of the photovoltaic devices is coupled to at least the first and second contact structures, which are overlying the thickness of single crystal silicon material. Alternatively, additional junctions can be formed between the contact structures and the semiconductor substrate to increase efficiency of the solar cell as shown Figure 17. Of course there can be other variations, modifications, and alternatives.

[0071] Figure 19 is a simplified diagram of a solar cell having a reflective surface region according to an embodiment of the present invention. As shown, a solar cell is provided. The solar cell includes a glass substrate attached to a first surface of a semiconductor layer. A plurality of contact structures are formed overlying a second surface of the semiconductor layer. As shown, each of the photovoltaic devices is coupled to at least the contact structure, which are overlying the thickness of single crystal silicon material. Light traverses through the glass substrate and the photovoltaic devices in the semiconductor layer and converts into electrical energy. As shown, a reflective surface 1901 is provided to reflect any residual light that passes through the photovoltaic regions to further activate one or more of the photovoltaic devices and convert into electrical energy. The reflective surface can be provided using material such as aluminum, silver, gold, or other suitable reflective material. Alternatively, if a non-conductive reflector is desirable, a dielectric stack reflector can be designed alone or in combination with a conductive reflector. The reflective surface provides means for multiple passes of light in the photovoltaic device and increases efficiency of the solar cell. Of course there can be other variations, modifications, and alternatives.

[0072] Figure 20 is a simplified diagram of a solar cell having a lens region according to an embodiment of the present invention. As shown, a solar cell is provided. The solar cell includes a glass substrate attached to a first surface of a semiconductor layer. A plurality of contact structures are formed overlying a second surface of the semiconductor layer. As shown, each of the photovoltaic devices is coupled to at least the contact structure, which are

overlying the thickness of single crystal silicon material. Light traverses through the glass substrate and the photovoltaic devices in the semiconductor layer and converts into electrical energy. In a specific embodiment, light is redirected and/or scattered using an optical element 2001 coupled to the glass substrate to direct light in more oblique angles and increase collection efficiency of the solar cell. An example of such an optical element can be a Fresnel lens. The Fresnel lens can be made of a plastic material or glass material. Alternatively, the glass substrate can be modified to scatter or redirect light and function like a Fresnel lens. By modifying the shape of optical element 2001, the light trapping action can occur by total internal reflection as allowed by a waveguide effect within the silicon thin-film or by approximating a Lambertian source and thus increasing the effective thickness of the thin-film cell. These effects can be combined to improved and even optimize the total light conversion efficiency of the cell.

[0073] Figure 21 shows a specific embodiment where an optical element is chosen such that the mostly specular light rays are directed at an oblique angle towards the thin-film photovoltaic cell. This diagram is merely an example, which should not unduly limit the scope of the claims herein. One of ordinary skill in the art would recognize other variations, modifications, and alternatives. In a specific embodiment, the thickness of the thin-film is chosen such that the waveguide can not only trap the light impinging at the angle within the glass substrate but supports the propagation within the thin-film for the range of light wavelengths of interest. The design considerations are thus, for all wavelengths of interest (e.g., IR to near-UV which constitutes the largest solar power spectrum wavelength distribution), to allow propagation of transverse electric (TE), transverse magnetic <sup>TM</sup>, and combination modes and higher order modes within the thickness of the thin-film cell to be allowed to propagate. A suitable dispersion curve for the waveguide to accomplish this design goal would generate the range of allowable silicon thicknesses and select the optical coupling layers that would act as the waveguide cladding. The entrance angle of the light within the transparent substrate would also be a design consideration for correct operation of the system throughout the range of cell entrance angles. Once coupled within the thin-film, the propagation will be highly attenuating due to the absorption of the radiation and its conversion to electricity by generating carriers within the thin-film. The longitudinal propagation of the light coincident with the longitudinal PN junction would help maximize light conversion efficiency. The resulting electric power would be collected by contacts 1 and 2. The structure also allows for light coupling layers that can help lower reflections that

can lower the coupled light energy within the active area of the thin-film solar cell. In a specific embodiment, the wave guide can be operable in a multi-mode or single mode. Additionally, the wave can be made using an internal material to cause a difference in refractive index for internal reflection of incoming light according to a specific embodiment. In a preferred embodiment a thin layer of silicon germanium can be sandwiched within a silicon structure to improve and even optimize light confinement to a region (e.g., central region) of one or more photovoltaic regions. Of course there can be other variations, modifications, and alternatives.

[0074] Figure 22 is a simplified diagram 2200 illustrating a method of using a donor substrate as a donor block for layer transfer according to an embodiment of the present invention. This diagram is merely an example, which should not unduly limit the scope of the claims herein. One of ordinary skill in the art would recognize other variations, modifications, and alternatives. As shown, the present method includes providing a reusable substrate 2201 having a surface region 2203, a cleave region 2204, and a total thickness of material 2208. In a specific embodiment, the total thickness of material is at least N times greater than a first thickness of material to be removed, which is between the surface region and the cleave region.

[0075] Depending upon the embodiment, the thickness of material can be composed of a variety of materials. As an example, the material can be silicon, silicon germanium, silicon carbide, any group III/V materials, group II/VI materials, or the like. In a preferred embodiment, the thickness of material is single crystal silicon suitable for solar cell applications. Of course, there can be other variations, modifications, and alternatives.

[0076] In a specific embodiment, the method brings 2213 a surface region of the thickness of material toward a handle substrate member 2211. The handle substrate can be any suitable material such as glass, quartz, plastic, any composites, or multi-layered materials, and the like. The method includes coupling the surface region of the reusable substrate to a first surface region of an optically transparent substrate, which has first surface region, which will be joined to the thickness of material, and a second surface region 2209. In a specific embodiment, the second surface region is often called an aperture region of the photovoltaic cell or material. Of course, there can be other variations, modifications, and alternatives.

[0077] As further shown, the method also includes subjecting a voltage differential between a first region and a second region of the reusable substrate according to a specific

embodiment. In a specific embodiment, the voltage differential causes an increase in thermal and/or strain energy at one or more portions of the cleave region to facilitate removal of the first thickness of material from the reusable substrate, while the surface region remains coupled to the first surface region. Of course, there can be other variations, modifications, and alternatives.

**[0078]** In a specific embodiment, the thermal energy will occur by resistive heating due to the resistivity of the substrate structure. In a specific embodiment, thermal heating increases a temperature of the substrate structure. In a preferred embodiment, the substrate is single-crystal silicon and shaped from a 12-inch silicon boule. The substrate size available from the starting boule size can be about 23 centimeters x 18 centimeters x 1.5 centimeters thickness. Assuming a p+ doped silicon material and the contact being across the long side (23cm), a resistance of about 0.5 ohms is calculated assuming a resistivity of about 0.5 ohm-cm. As an example, to heat the reusable substrate a current of about 10-100 amperes would develop about 50 watts to 5 kilowatts and would be adjustable to accomplish the desired amount of heating and achieving the desired substrate temperature according to a specific embodiment. The use of an adjustable thermal element can be utilized in various ways during the course of the layer-transfer process.

**[0079]** For example, a pre-bond preparation can utilize a thermal treatment to cure an applied glass layer compound or perform a beta anneal of the hydrogen cleave plane according to "Controlled Cleavage Process and Resulting Device Using Beta Annealing" in the names of Henley, Francois J. and Cheung, Nathan W., issued as U.S. Patent No. 6,162,705 on December 19th, 2000, commonly assigned, and hereby incorporated by reference for all purposes.. During a bonding process, the thermal treatment can increase bond strength and further cure and/or optimize the cleave plane. Moreover, the thermal treatment will necessarily generate a thermal gradient in the vertical direction and this can generate strain energies that may also contribute to the cleaving process. These contacts will also serve to set the reusable substrate potential if an electro-static bond process is used according to a preferred embodiment of the invention. In a specific embodiment, continued use of a thermal treatment during bond can generate a thermally-induced cleaving process instead of a controlled-cleave process. In a preferred embodiment, a controlled cleaving action occurs to remove the thickness of material using a propagating cleave front from one portion of the thickness of material to another portion of the thickness of material. The detached material includes a cleaved surface region coupled to the first surface region of the

optically transparent substrate. Of course, there can be other variations, modifications, and alternatives. Further details of the present method can be found throughout the present specification and more particularly below.

**[0080]** In a specific embodiment, the present invention provides a reusable silicon substrate device 2200 for use with layer transfer process, e.g., controlled cleaving, process called Smart Cut™ by Soitec, SA. The device includes a reusable substrate having a surface region, a cleave region, and a total thickness of material, which is preferably at least N times greater than a first thickness of material to be removed. The first thickness of material to be removed is between the surface region and the cleave region. In a specific embodiment, N is an integer greater than about ten, but can be others. The device also has a first electrode 2204 member coupled to a first region of the reusable substrate and a second electrode member 2203 coupled to a second region of thereusable substrate. The device has a voltage source coupled between the first electrode member and the second electrode member to provide a voltage differential between the first region and the second region to cause an increase in thermal and/or strain energy at one or more portions of the cleave region to facility removal of the first thickness of material from the reusable substrate. Of course, there can be other variations, modifications, and alternatives. Further details of the present method can be found throughout the present specification and more particularly below.

**[0081]** In the particular embodiment of Figure 22, a "thick" donor block is used for bonding and cleaving. In accordance with one embodiment, the bond can be made through electrostatic bonding, with the voltage across the block to the glass ( $V_1=V_2$ ,  $V_3$  at high potential). Heating for bonding treatment can be effectuated with differential voltages  $V_1 \neq V_2$ , with the current heating the silicon tile for bond treatment (BT). Depending upon the embodiment, the voltage or voltages can be selectively applied on selective spatial portions of the silicon tile. Of course, there can be other variations modifications, and alternatives.

**[0082]** Such use of a thick tile as shown in the embodiment of Figure 22 confers a number of possible advantages. One advantage is multiple (500x or more) reuses of the tile and the chuck. Such an embodiment also allows for inexpensive re-surfacing, and the use of the Integrated Layer-Transfer Tool (ILT<sup>2</sup>) concept. Depending upon the embodiment, there may also be other advantages.

[0083] A method of manufacturing a photovoltaic layer on a semiconductor substrate is briefly outlined below.

1. Provide a reusable semiconductor substrate, which has a surface region, a cleave region and a first thickness of material to be removed between the surface region and the cleave region;
2. Align the reusable semiconductor substrate to an optically transparent substrate;
4. Form an optical coupling material (e.g., silicon on glass) and/or a bond promotion material on either or both surfaces to be joined on the reusable substrate and the optically transparent substrate;
4. Perform a plasma cleaning process on either or both surfaces to be joined on the reusable substrate and the optically transparent substrate;
5. Couple the surface region of the semiconductor substrate to a first surface region of the optically transparent substrate;
6. Initiate a controlled cleaving action on a portion of the cleave region;
7. Cleave the semiconductor substrate to remove the first thickness of material from the semiconductor substrate, while the surface region remains coupled to the first surface region, to cause formation of a cleaved surface region; and
8. Optionally, form a second thickness of semiconductor material overlying the cleaved surface region to form a resulting thickness of semiconductor material, which has one or more photovoltaic regions;
9. Provide a cover glass material overlying the second thickness of semiconductor material to form a first photovoltaic cell member;
10. Repeat one or more of the above steps using the reusable substrate to form a second photovoltaic cell member;
11. Continue step (10) to form up to N cell members, where N is an integer greater than about 10; and
12. Perform other steps, as desired.



**[0084]** The above sequence of steps provides a method according to an embodiment of the present invention. As shown, the technique includes a method and a structure for forming a solar cell structure using layer transfer techniques for photovoltaic applications. Other alternatives can also be provided where steps are added, one or more steps are removed, or one or more steps are provided in a different sequence without departing from the scope of the claims herein. Alternatively, there can be other ways of forming the structure. That is, the sequence can initially be formed on a cover sheet such as cover glass and other suitable materials and then forming the other layers according to a specific embodiment. The layer transfer occurs on the cover glass, which is used to form the rest of the solar cell device. In preferred embodiments, the layer transferred material is provided from a reusable substrate member. Other techniques can use transfer substrates that will transfer a layer transferred material onto a handle substrate. Further details of the present method can be found throughout the present specification and more particularly below.

**[0085]** Figure 23 is a simplified diagram of a method 2300 using an integrated layer transfer tool according to an embodiment of the present invention. This diagram is merely an example, which should not unduly limit the scope of the claims herein. One of ordinary skill in the art would recognize other variations, modifications, and alternatives. As shown, the method includes providing a donor substrate and a handle substrate. In a specific embodiment, the donor substrate includes a cleave plane, which is provided by implantation, deposition, or other techniques. In a preferred embodiment, the donor substrate is reusable and can be subject to multiple layer transfer processes.

**[0086]** In a specific embodiment, the method includes using an integrated layer transfer tool to carry out more than one processes to form the photovoltaic cells and/or regions. In a specific embodiment, the tool is provided as a cluster tool, which has a robot arm for inputting and outputting one or more substrates. The cluster tool has an integrated chamber that can perform plasma cleaning, plasma activated bonding, including in-situ bonding, and a controlled cleaving tool, as shown. Other techniques such as electrostatic bonding, curing (e.g., silicon on glass), or other processes can be included. In a specific embodiment, more than one chamber can also be provided to perform these and other functions. In a specific embodiment, the cleaved substrate maintained on the handle substrate is subjected to a deposition process.

[0087] In a specific embodiment, the deposition process can be provided to thicken the thickness of transferred material, as also described above. Depending upon the embodiment, the process can include chemical vapor deposition, physical vapor deposition, providing nanoparticles and reflowing them to form epitaxial silicon, any combination of these, and the like. As also shown, the reusable substrate is reclaimed and the method begins again to form another photovoltaic region. Of course, there can be other variations, modifications, and alternatives. Further details of the integrated layer transfer tool can be found throughout the present specification and more particularly below.

[0088] Use of embodiments of the Integrated Layer Transfer Tool (ILT<sup>2</sup>) concept described above may confer certain advantages. A possible advantage of ILT<sup>2</sup> is that it may allow the bundling of key steps in layer transfer processes. Specifically, the process module concept may be applied to layer-transfer. In other embodiments, the process module may include other processes as well.

[0089] Embodiments of the ILT<sup>2</sup> process may also offers lower cost and higher yield layer-transfer. It may allow key bond/cleave steps to occur in a well-controlled environment to eliminate defect-related yield losses. ILT<sup>2</sup> may also offer the lowest cost solution compared with single-tool layer-transfer manufacturing. Various features and advantages of the ILT<sup>2</sup> process are discussed in further detail below.

[0090] Figure 24 is a more detailed diagram of a method 2400 of using an integrated layer transfer tool according to an embodiment of the present invention. This diagram is merely an example, which should not unduly limit the scope of the claims herein. One of ordinary skill in the art would recognize other variations, modifications, and alternatives. In a specific embodiment, the donor substrate includes a cleave plane, which is provided by implantation, deposition, or other techniques. In a preferred embodiment, the donor substrate is reusable and can be subject to multiple layer transfer processes. In a more preferred embodiment, the reusable silicon substrate device 2401 is for use with more than one layer transfer processes simultaneously.

[0091] In a specific embodiment, the reusable device has a donor substrate member, which has a plurality of sites. The device also has a plurality of reusable substrates arranged respectively on the plurality of sites. In a specific embodiment, each of the reusable substrates has a surface region, a cleave region, and a total thickness of material. In a preferred embodiment, the total thickness of material is at least N times greater than a first

thickness of material to be removed. The first thickness of material to be removed is between the surface region and the cleave region, whereupon  $N$  is an integer greater than about ten. Of course, there can be other variations, modifications, and alternatives.

[0092] In a specific embodiment, the method includes using an integrated layer transfer tool to carry out more than one processes to form the photovoltaic cells and/or regions. In a specific embodiment, the tool is provided as a cluster tool, which has a robot arm for inputting and outputting one or more substrates. The cluster tool has an integrated chamber that can perform the formation of an bond promotion or accommodation layer onto the one or both surfaces to be bonded (using SOG-like organic or inorganic materials or the like), plasma activated and/or electro-static bonding, including in-situ bonding, and a controlled cleaving tool, as shown. In a specific embodiment, more than one chamber can also be provided to perform these and other functions. In a specific embodiment, the cleaved substrate maintained on the handle substrate is subjected to a deposition process.

[0093] In a specific embodiment, the deposition process can be provided to thicken the thickness of transferred material, as also described above. Depending upon the embodiment, the process can include chemical vapor deposition, physical vapor deposition, providing nanoparticles and reflowing them to form epitaxial silicon, any combination of these, and the like. As also shown, the reusable substrate is reclaimed and the method begins again to form another photovoltaic region. Of course, there can be other variations, modifications, and alternatives. Further details of the integrated layer transfer tool can be found throughout the present specification and more particularly below.

[0094] Other equipment may be employed in conjunction with the bonding tool discussed above. For example, an ion shower system (Gen 4 glass) may be used to form the cleave plane. Other forms of implantation such as plasma immersion ion implantation, plasma implantation, or the like can also be used. CMP tools may be used for smoothing and to reclaim the donor. Chemical treatment tools can also be used in combination. Ion-beam assisted deposition tools may be utilized for thickening. Of course, there can be other variations, modifications, and alternatives.

[0095] Figure 25 is a top-view diagram of a cluster tool 2500 for layer transfer according to an embodiment of the present invention. This diagram is merely an example, which should not unduly limit the scope of the claims herein. One of ordinary skill in the art would recognize other variations, modifications, and alternatives. As shown, a cluster tool

apparatus, e.g., multi-chambered, is provided. The tool has a first chamber (1), which has a reusable silicon substrate device for use with layer transfer process. The device has a reusable substrate having a surface region, a cleave region, and a total thickness of material, the total thickness of material being at least N times greater than a first thickness of material to be removed. In a specific embodiment, the first thickness of material to be removed is between the surface region and the cleave region, whereupon N is an integer greater than about ten. A first electrode member is coupled to a first region of the reusable substrate, e.g., cleave region, other region. A second electrode member is coupled to a second region of the reusable substrate, e.g., cleave region or other region. In a specific embodiment, the device has a voltage source coupled between the first electrode member and the second electrode member to provide a voltage differential between the first region and the second region to cause an increase in thermal and /or strain energy at one or more portions of the cleave region to facilitate removal of the first thickness of material from the reusable substrate.

[0096] In a specific embodiment, cluster tool has a second chamber (2) coupled to the first chamber. The second chamber is provided to perform one or more processes. Depending upon the embodiment, the second chamber can be adapted to perform preparation for bonding, e.g., formation of a layer that enhances bond quality, thermal anneal and/or etching, smoothing. Additionally, the second chamber can be adapted to perform plasma activation and bonding according to a specific embodiment. Other chambers can be provided to perform bond treatment, cleaving, implantation, deposition, etching, and other processes. Depending upon the embodiment, the chambers can be configured in a parallel and/or serial manner. Of course, there can be other variations, modifications, and alternatives.

[0097] The apparatus shown in Figure 25 allows the bundling of key layer-transfer processes, permitting the process module concept to be applied to layer-transfer. Process modules run on the cluster tool can include, but are not limited to, (optional) bonding preparation, integrated plasma activation (PA)/bonding, bond treatment, cleaving, and rapid thermal processing (RTP).

[0098] Figure 26 is a simplified diagram 2600 illustrating an alternative method of using a donor substrate according to an embodiment of the present invention. This diagram is merely an example, which should not unduly limit the scope of the claims herein. One of ordinary skill in the art would recognize other variations, modifications, and alternatives. As shown, a reusable silicon substrate device for use with layer transfer process is provided. The device

has a reusable substrate 2601 having a surface region 2603, a cleave region 2605, and a total thickness of material 2607. The total thickness of material is at least N times greater than a first thickness of material to be removed. In a specific embodiment, the first thickness of material to be removed is between the surface region and the cleave region, whereupon N is an integer greater than about ten. In other embodiments, N can be other integers. Of course, there can be other variations, modifications, and alternatives.

**[0099]** In a specific embodiment, the device also has a chuck member 2609 adapted to hold a handle substrate member 2611 in place. In a specific embodiment, the handle substrate can be made of any suitable material such as those noted herein, such as glass, quartz, or plastic, including other combinations, and the like. The chuck member is configured to hold the handle substrate in a manner to facilitate bonding the handle substrate to the first thickness of material to be removed. In a specific embodiment, the chuck member can be a mechanical chuck, electrostatic chuck, or vacuum chuck. Of course, there can be other variations, modifications, and alternatives.

**[0100]** In a specific embodiment, the reusable substrate is provided on a movable head device. The head device can be adapted to provide pressure 2602 in a z-direction to cause the surface of the reusable substrate to be applied to the handle substrate according to a specific embodiment. The head device can also move in a lateral y and x direction according to a specific embodiment. The head device is also adapted to be able to tilt relative to the z-direction to facilitate cleaving of the thickness of material. Alternatively, the chuck device, which will be described in more detail below, is provided to tilt relative to the z-direction. Of course, there can be other variations, modifications, and alternatives.

**[0101]** In a preferred embodiment, the device has a mechanical pressure device 2613 operably coupled to the chuck member. The mechanical pressure device is adapted to provide a force 2619 to cause bonding of the handle substrate to the first thickness of material to be removed. In a specific embodiment, the chuck member is a rigid cylindrical roller member, which can be rotated 2617 about an axis and moved laterally 2615. Alternative members such as other annular members, rollers, and any other suitable shapes, sizes, and rigidity can also be used. Of course, there can be other variations, modifications, and alternatives. Further details of other methods and systems according to embodiments of the present invention can be found throughout the present specification and more particularly below.

[0102] Figure 27 is a simplified diagram 2700 illustrating yet an alternative method of using a donor substrate according to an embodiment of the present invention. This diagram is merely an example, which should not unduly limit the scope of the claims herein. One of ordinary skill in the art would recognize other variations, modifications, and alternatives. As shown, the present invention provides use of a plurality of reusable substrates on a larger frame and/or support member 2701 according to a specific embodiment. In a specific embodiment, the frame and/or support member includes a plurality of reusable substrates 2705. Each of the substrates includes a cleave region, surface region, and thickness of material to be removed.

[0103] In a specific embodiment, the plurality of reusable substrates are bonded 2707 to a handle substrate, which is held by a chuck member. Depending upon the embodiment, the bonding process can include a plasma activation and/or an electro-static bonding process, joining process, bond treatment process, and others. In a specific embodiment, the joined plurality of reusable substrates are subjected to a cleaving process. The cleaving process can be a process called the Controlled Cleaving Process™ by Silicon Genesis Corporation, a process called Smart Cut™ by Soitec SA, or others depending upon the specific embodiment. Once cleaving has occurred, the plurality of reusable substrates, which have removed thicknesses of materials, are removed for a next sequence of process steps. Of course, there can be other variations, modifications, and alternatives.

[0104] In the specific embodiment of Figure 27, tiles can be 1, 2, or more deep. Re-surfacing may be accomplished by chemical mechanical planarization. The cleave plane may be formed by ion shower or the like. Optional bond preparation treatment includes coating, etc. The steps may be performed on a tray-like frame that that allows numerous tiles to be simultaneously bonded and cleaved. In accordance with other embodiments, the detached portions can be secured in place by other than physical contact with a common tray, for example by application of a positive gas pressure. Bonding can be through plasma, electro-static, coating enhanced, or a combination of these processes. Bond treatment: heat conduction, current heating etc., may be done in combination with continuing electro-static bond treatment. Cleaving may be achieved by CCP or thermal-based approaches. The processing may be continuous, in-line plate-plate travel, or batch.

[0105] Figure 28 is a simplified diagram 2800 illustrating still an alternative method of using a donor substrate according to an embodiment of the present invention. This diagram is

merely an example, which should not unduly limit the scope of the claims herein. One of ordinary skill in the art would recognize other variations, modifications, and alternatives. As shown, the diagram includes a plurality of reusable substrate members provided in a track-like configuration, which is provided merely for illustration purposes, according to a specific embodiment. Each of the substrates is subjected to a sequence for fabricating a solar cell member or other substrate member according to a specific embodiment. As merely an example, the substrates are provided for cleave plane formation, which uses implantation and/or other techniques according to a specific embodiment. After that, the method uses a bond preparation treatment process such as the formation of a bond promotion layer according to a specific embodiment. The method then performs bonding, bond treatment, and cleaving according to a specific embodiment. The cleaved reusable substrate is subjected to resurfacing, which facilitates the next sequence of steps according to a specific embodiment.

**[0106]** As also shown, the plurality of substrates are provided on a handle substrate, which is maintained on a chuck member. In a specific embodiment, the chuck member can be movable in a continuous manner along a predetermined direction to perform the present method. As the chuck member moves the handle substrate, each of the reusable substrates is subjected to the chuck member in a continuous assembly-like manner. Of course, there can be other variations, modifications, and alternatives.

**[0107]** In a specific embodiment, each of the substrate members can be selectively designed for layer transfer processes. As an example, an edge of the thickness of material can be formed and/or beveled such that a mechanical blade and/or member can be provided within a vicinity of the cleave region to initiate cleaving of the thickness of material from the remaining portion of the reusable substrate member. That is, a blade can be inserted within the vicinity of the cleave region within an outer portion of the substrates to initiate cleaving and energy is provided to either one or both of the substrates to continue the propagation of the cleave front and remove the thickness of material from the remaining portion of the reusable substrate member. In other embodiments, the cleave region can include an exclusion region around its periphery to facilitate bonding and cleaving. Such exclusion region can be formed using a mask and/or blocking material when implanting particles (e.g., hydrogen) into the cleave region to form the cleave region according to a specific embodiment. Of course, there can be other variations, modifications, and alternatives.

[0108] In the specific embodiment of Figure 28, tiles can be 1, 2, or more deep. Re-surfacing may be accomplished by chemical mechanical planarization. The cleave plane may be formed by ion shower or the like. Optional bond preparation treatment includes coating, etc. The steps may be performed on a tray-like frame that that allows numerous tiles to be simultaneously bonded and cleaved. Bonding can be through plasma, electro-static, coating enhanced, or a combination of these processes. Bond treatment: heat conduction, current heating etc., may be done in combination with continuing electro-static bond treatment. Cleaving may be achieved by CCP or thermal-based approaches. The processing may be continuous, in-line plate-plate travel, or batch.

[0109] Figure 29 is a conventional process 2900 for forming conventional photovoltaic cells. As shown, the conventional process includes a process of using gaseous species (e.g., trichlorosilane) to form polysilicon material. The polysilicon material is then made into one or more bulk substrate members. Such bulk substrate members are subjected to wafer processes such as grinding, polishing, and cutting, and others. To complete the solar cell, the wafer undergoes formation of photovoltaic cell regions, application of cover glass, and other processes. The final assembly is provided into a module, which is then formed into a system to be used. As shown, the conventional process is highly difficult to perform, costly, and prone to defects. These and other limitations have been overcome by the present method, which has been described throughout the present specification and more particularly below.

[0110] Figure 30 is a simplified diagram of a process 3000 of forming a photovoltaic cell according to an embodiment of the present invention. This diagram is merely an example, which should not unduly limit the scope of the claims herein. One of ordinary skill in the art would recognize other variations, modifications, and alternatives. As shown, the method begins with glass, which is subjected to a layer transfer process. A layer of single crystalline silicon is now provided on the glass. A second thickening layer of epitaxially grown silicon is provided on the layer transferred silicon. After that, the composite substrate structure is subjected to formation of photovoltaic cells. The cells are provided in a module, as shown. Depending upon the embodiment, there can be other variations, modifications, and alternatives.

[0111] As previously described, use of the Integrated Layer-Transfer Tool (ILT<sup>2</sup>) concept may allow for key layer-transfer steps in the fabrication of a solar module, to be bundled together, thereby improving throughput and lowering cost. Figure 31 shows a simplified



cross-sectional view of a number of steps that may be performed utilizing a  $ILT^2$  according to a first embodiment of a process 3100 of the present invention for fabricating a solar cell.

**[0112]** In a first step 3102, a plurality of tiles 1 bearing residual layers 1a and 1b (such as oxides) on the top and bottom surfaces respectively, are contacted with a process chuck or tray 2. Tiles 1 may be adhered to the process chuck 2 utilizing one of several types of forces, for example by electrostatic attraction or by vacuum suction.

**[0113]** In a second step 3104, tiles 1 are exposed to etching conditions to remove the residual surface layers 1a from their top surfaces. In accordance with certain embodiments, removal of this residual material may be accomplished through exposure to a plasma, for example a fluorine-based plasma. In accordance with other embodiments, removal of this residual layer may be accomplished by exposure to a wet etching chemistry such as HF.

**[0114]** In the next step 3106, a stack of layers 10, 12, and 14 are then formed in succession on the top surface of the tile. First layer 10 comprises a passivating layer, such as silicon nitride, that serves to protect the top surface of the tile. Second layer 12 comprises a light trapping layer (light randomization to make the absorber capture more light), such as an etched or faceted film surface or other suitable material and structure, that will function to cause light that has penetrated through the tile to go back into the tile be collected to improve overall cell efficiency. Third layer 14 comprises a light coupling layer.

**[0115]** Next, while still adhered to the process chuck, as shown in step 3108 the process chuck 2 is inverted ("flipped"), and the light coupling layer 14 of the tiles is placed into contact with and bonded to a substrate 18. In accordance with particular embodiments of the present invention, this substrate may be transparent (for example a glass plate) to allow incident light to access to the tile.

**[0116]** Adhesion between the light coupling layer on the tile, and the underlying substrate, may be achieved in several possible ways. According to one embodiment, the light coupling layer on the tile surface may be adhered to the underlying substrate through the application of a voltage differential, for example utilizing an electro-static bonding technique as previously described.

**[0117]** An alternative approach is to employ an adhesive 16. The adhesive may comprise a soft organic adhesive (such as polyvinyl acetate - PVA) having favorable light coupling abilities. In accordance with certain embodiments, the adhesive may be patterned on the

substrate prior to attachment of the tiles. In accordance with other embodiments, the adhesive may be placed on the top of the tiles prior to their inversion and attachment to the substrate.

[0118] Next, as shown in step 3110, the residual surface layer 1b formerly remaining on the bottom of the tile is removed, either by exposure to plasma or wet etching chemistries, typically fluorine-based.

[0119] Next, as shown in step 3112, the P/N junctions 4 critical to gathering energy from incident light, are formed in the tile. These P/N junctions are typically formed by the introduction of dopant, for example through ion implantation, diffusion-type processes, or non-contact methods such as inkjet printing of dopant precursors. Some heat treatment may be made to allow P/N junctions to form. Finally, as shown in step 3114, a passivation layer 6 (such as silicon nitride) is formed over the tile surface that contains the P/N junctions.

[0120] Processes for forming a solar cell in accordance with the present invention are not limited to the specific embodiment shown in Figure 31. One of skill in the art would recognize that different steps, and different orders of steps, may be employed and still result in formation of a solar cell device. For example, in Figure 31, the order of formation of the P/N junctions and the passivation layer could easily be reversed, with the P/N junctions being formed by implantation of ions through a passivation layer that has already been formed on the surface of the tiles.

[0121] Moreover, certain processing steps may take place after formation of the P/N junctions and the passivation. For example, subsequent processing can include selective removal of passivation to allow the selective formation of conducting contacts with the P/N junctions, to draw the electrical energy away from the tile. In accordance with particular embodiments, conducting contacts to the P- and N-type regions could be formed on the same side of the tile (in this case the backside to allow for simpler contacting with the module. In accordance with other embodiments of the present invention, however, the conducting contacts to the P-type and N-type regions could be positioned on opposite sides of the tile to fabricate a transverse-type cell.

[0122] The diagram of Figure 31 is merely an example, which should not unduly limit the scope of the claims herein. One of ordinary skill in the art would recognize other variations, modifications, and alternatives. As shown, the method begins with glass, which is subjected to a layer transfer process. A layer of single crystalline silicon is now provided on the glass.

A second thickening layer of epitaxially grown silicon is provided on the layer transferred silicon. After that, the composite substrate structure is subjected to formation of photovoltaic cells. The cells are provided in a module, as shown. Depending upon the embodiment, there can be other variations, modifications, and alternatives.

**[0123]** Figure 32 presents a simplified diagram of an alternative process 3200 of forming a photovoltaic cell according to an embodiment of the present invention. In a first step 3202, a plurality of tiles 1 bearing residual layers 1a and 1b (such as an oxide) on the top and bottom surfaces respectively, are contacted with a process chuck or tray 2. Tiles 1 may be adhered to the process chuck 2 utilizing forces such as electrostatic attraction or vacuum suction.

**[0124]** In a second step 3204, tiles 1 are exposed to etching conditions to remove the residual material present on the top tile surface. In accordance with certain embodiments, removal of this residual surface material may be accomplished by exposure to a plasma, for example a fluorine-based plasma. In accordance with other embodiments, removal of this residual surface layer may be accomplished by exposure to a wet etchant such as HF.

**[0125]** In the next step 3206, the P/N junctions 4 critical to gathering energy from incident light, are formed in the tile. These P/N junctions are typically formed by the introduction of dopant, for example through implantation, diffusion-type processes, or non-contact methods such as inkjet printing of dopant precursors. Some heat treatment may be made to allow P/N junctions to form.. In step 3206 a passivation layer (such as silicon nitride) is then formed over the tile surface containing the P/N junctions. As described above in connection with the previous embodiment of Figure 31, the order of formation of the P/N junctions and the passivation layer could readily be reversed, with implantation to form the P/N junctions occurring through a passivation layer that has already been formed.

**[0126]** Next, with tiles still attached, in step 3210 the process chuck 2 is inverted ("flipped") and the tiles are placed into contact with a second process chuck 8. The tiles may be adhered to the second chuck 8 utilizing different forces, for example vacuum suction or electrostatic force. The tiles are then released from the first process chuck.

**[0127]** Next, as shown in step 3212, the residual surface layer formerly remaining on the bottom of the tile is removed, either by exposure to a plasma etching ambient or wet etching chemistry, typically fluorine-based.

[0128] As shown in step 3214, layers 10, 12, and 14 are then formed in succession on the top surface of the tile. First layer 10 comprises a passivating layer, such as silicon nitride, that serves to protect the top surface of the tile. Second layer 12 comprises a light trapping layer (light randomization to make the absorber capture more light), such as an etched and faceted film surface or other suitable material and structure that will function to cause light that has penetrated through the tile to go back into the tile be collected to improve overall cell efficiency. Third layer 14 comprises a light coupling layer.

[0129] Next, while still adhered to the second process chuck 8, as shown in step 3216 the process chuck is inverted ("flipped") and the light coupling layer on the tiles is placed into contact with and bonded to a substrate 18. In accordance with particular embodiments of the present invention, this substrate 18 may be transparent (for example a glass plate) to allow incident light access to the tile.

[0130] Adhesion between the light coupling layer on the tile, and the underlying substrate, may be achieved in several possible ways. According to one embodiment, the light coupling layer on the tile surface may be adhered to the underlying substrate through the application of a voltage differential, utilizing an electro-static bonding technique as previously described.

[0131] An alternative approach is to utilize an adhesive 16. The adhesive may comprise a soft organic adhesive (such as polyvinyl acetate - PVA) having favorable light coupling abilities. In accordance with certain embodiments, the adhesive may be patterned on the substrate prior to attachment of the tiles. In accordance with other embodiments, the adhesive may be patterned on the top of the tiles prior to their inversion and attachment to the substrate.

[0132] As with the previous embodiment of Figure 31, certain further processing steps may take place to complete formation of the solar module or cell. For example, subsequent processing can include selective removal of passivation to allow the selective formation of conducting contacts with the P/N junctions, to draw the electrical energy away from the tile. In accordance with particular embodiments, conducting contacts to the P- and N-type regions could be formed on the same side of the tile (in this case the backside to allow for simpler contacting with the module. In accordance with other embodiments of the present invention, however, the conducting contacts to the P-type and N-type regions could be positioned on opposite sides of the tile to form a transverse-type cell.

[0133] The processes described above in connection with Figures 31 and 32 differ in certain respects. For example, the process of Figure 31 requires use of only one process chuck or tray, and employs only a single inversion step prior to bonding of the tile to the substrate. However, the process embodiment of Figure 31 involves formation of the P/N junctions in the tile while it is attached to the substrate. This may mean that the substrate is exposed to higher temperatures, requiring the use of more expensive substrate materials able to withstand higher temperatures. Exposure of the substrate to a higher temperature may also require careful consideration of the method by which the tile is bonded to the substrate, so that this bond is not disrupted by the later processing.

[0134] By contrast, in the process embodiment of Figure 32, the tile is subjected to high temperature steps prior to being bonded to the substrate. This advantage, however, comes at the expense of more complex processing requiring two different process chucks and inversion steps prior to bonding to the substrate.

[0135] Both of the embodiments just discussed offer the advantage that all of the tiles within a given solar module would be processed simultaneously, thereby lowering the cost of fabricating the module. Moreover, such simultaneous batch processing of all tiles grouped within a particular module, is also consonant with the simultaneous processing of those tiles to accomplish layer transfer.

[0136] For example, Figure 33 shows a simplified conventional process flow 3300 for the forming individual elements of a module. In step 3302, an ingot or boule of monocrystalline silicon exhibiting a substantially cylindrical shape is created by growing or casting. In step 3304, the ends of the ingot or boule are cropped by sawing. In step 3306, the shape of the cropped ingot or boule is converted to square rather than circular, by shaving the sides. In step 3308, the edges of the shaved sides are ground to smoothen them, resulting in the brick shape having a square cross-section.

[0137] Conventionally, as shown in steps 3310-12, the brick ingot is then glued to a support and then exposed to a plurality of cutting processes (for example wire sawing) in order to form individual wafers. As shown in steps 3314 and 3316, these wafers are then separated and individually cleaned to yield step 3318 in the plurality of individual substrates. A disadvantage of the conventional approach shown in Figure 33 is its relatively high consumption of both consumables (such as glues, wire saws, and cleaning fluids), and valuable single crystal silicon material lost during processing.

[0138] The conventional process of Figure 33 may be contrasted with that of Figure 34, which shows a simplified view of a process for preparing elements of a module in accordance with an embodiment of the present invention. Specifically, the process embodiment shown in Figure 34 shares with the conventional process, the preliminary steps 3308-3308 of ingot/boule creation, cropping, squaring, and edge grinding.

[0139] However, as shown in step 3402 of Figure 34, rather than being cut into the individual (wafer) elements of the module, embodiments of the present invention separate the brick ingot into a plurality of thick tiles, whose surfaces are then planarized/polished (step 3406). In step 3408, a notch feature may then be formed in each tile.

[0140] As shown in step 3410, individual elements of the module are then detached and singulated from the tile, for example by ion implantation followed by the separation of films that may or may not be of sufficient thickness to be free-standing. As shown in step 3412, detachment and singulation yields the individual elements of a larger module. In accordance with one embodiment of the present invention, these individual elements may represent substantially square single crystal silicon wafers having lateral dimensions of 125x125mm.

[0141] Various aspects of the detachment and singulation of films of material according to embodiments of the present invention have been described in other United States Patent Applications co-pending herewith. For example, U.S. Provisional Patent Applications 60/886,827 and 60/886,912 filed January 26, 2007, both of which are incorporated by reference in their entireties herein, describe the singulation of thin films from a parent bulk material by implantation of ions at energies of at least 600 keV into the surface of the parent bulk material.

[0142] The avoidance of having to saw and clean each individual wafer avoids many of the consumables cost and loss of single crystal material associated with the conventional process. Moreover, this final step of detaching and singulation of wafers can be performed with a batch of ingots, allowing the simultaneous fabrication of the plurality of elements making up the module.

[0143] Figure 36 shows a simplified flow diagram of an embodiment of a method 3600 in accordance with the present invention for fabricating a solar module comprising a plurality of individual photovoltaic cells. In step 3602, a plurality of single crystal silicon tiles is provided, each of the single crystal tiles having a thickness capable of forming a plurality of free standing slices, and each of the single crystal tiles having a surface region. Next, in step

3604, ions are implanted through the surface region of each of the single crystal silicon tiles. In step 3606, at least one of the slices is detached from each of the single crystal silicon tiles to form a plurality of detached slices. In step 3608, a plurality of detached slices are supported on a common process chuck. In step 3610, one or more photovoltaic regions are formed on each of the plurality of supported detached slices. In step 3612, each of the plurality of detached slices is coupled to an optical plate. In step 3614, one or more contact regions are formed on each of the plurality of detached slices, which is coupled to the optical plate, to form a solar module.

**[0144]** As discussed above, and particularly in connection with Figure 22, embodiments in accordance with the present invention allow substrates to be bonded together utilizing a voltage potential difference applied between them. This voltage potential can give rise to heating along the interface between the substrates that causes bonding to occur. Moreover, the successful singulation of thin films of material to form the individual units making up a module, may depend upon the exercise of precise temperature control, for example at the time of implanting an ion into the surface of a bulk material.

**[0145]** Accordingly, Figure 35 presents a simplified cross-sectional view of an embodiment of an apparatus 3500 in accordance with the present invention, for controlling the temperature along an interface between a pair of substrates during bonding or layer transfer processes. Specifically, substrate pair 3502 and 3504 are in contact along interface 3506. A plurality of electrical contacts 3508 in electrical communication with a voltage supply 3510, are present with the substrates at different regions, in order to create voltage differentials having an intensity and polarity optimal for bonding.

**[0146]** For example, a first electrical contact 3508a may be positioned in the member 3512 supporting the lower substrate. A second electrical contact 3508b may be positioned on top of the upper substrate. Application of a potential difference between contacts 3508a and 3508b creates a gradient of resistive heating in the Z- direction. Depending upon variables such as the thickness of the respective substrates, and their composition and hence electrical resistance, this thermal gradient can be tailored to yield a particular value at the substrate interface, thereby producing a particular type of bonding.

**[0147]** Similarly, a thermal gradient having a lateral component can also be created by the application of potential differences between electrical contacts selectively positioned along the X- and Y- directions. For example, a third electrical contact 3508c may be positioned on

one side of the first substrate, and a fourth electrical contact 3508d may be positioned on the opposite side of the first substrate, at approximately the height of the interface 3506.

Application of a potential difference between these contacts would give rise to resistive heating in the first substrate along the interface, again producing bonding of a particular character.

[0148] Of course, by selective placement of a multitude of electrical contacts at different points in space, resistive heating resulting in thermal gradients of almost any variety, can be generated to suit a particular bonding property.

[0149] Moreover, the character of the thermal gradients created by resistive heating could be further modified by the presence of outside energy sources 3514 or sinks 3516. For example, the top substrate 3502, bottom substrate, or both, could be exposed to additional convection or conduction heating, for example from a laser, lamp, or flow of heated gas, resulting in modification of thermal gradient resulting at the interface. Alternatively, a portion of one or both of the substrates could be in contact with a cooling member, for example a Peltier cooler or a flow of cooled gas, thereby also modifying the thermal gradient and hence the bonding occurring at the substrate interface.

[0150] The points of application of the potential difference may serve other functions as well. For example, as shown in the embodiment of Figure 35, the voltage potential could be applied to the lower substrate through a mechanical clamp structure 3518 configured to engage a notch in the base of that substrate, for example as has been described above in connection with single crystal silicon tiles.

[0151] Apparatuses in accordance with embodiments of the present invention are not limited to providing resistive heating along an interface between multiple substrates that are to be bonded together. For example, Figure 33A shows a simplified cross-sectional view of an alternative embodiment of an apparatus 3550 in accordance with the present invention, which features plurality of conductive contacts 3352 in connection with a voltage supply 3353 and in contact with a bulk material 3354 disposed on a support 3356. Surface 3354a of the bulk material is configured to receive ions implanted from a source 3358, and contacts 3352 are disposed to provide temperature control in the form of resistive heating at the expected depth of singulation of a film from the bulk material. Source 3358 can also be a capacitively coupled electrode structure that coupled electrical heat energy into the tile area.



[0152] While the above is a full description of the specific embodiments, various modifications, alternative constructions and equivalents may be used. Although the above has been described using a selected sequence of steps, any combination of any elements of steps described as well as others may be used. Additionally, certain steps may be combined and/or eliminated depending upon the embodiment. Furthermore, the particles of hydrogen can be replaced using co-implantation of helium and hydrogen ions to allow for formation of the cleave plane with a modified dose and/or cleaving properties according to alternative embodiments. In certain embodiments, the backing substrate can be applied to each of the substrates, including handle and donor. In alternative embodiments, coatings may also be provided on surfaces or other regions of the transparent material. Therefore, the above description and illustrations should not be taken as limiting the scope of the present invention which is defined by the appended claims.

WHAT IS CLAIMED IS:

- 1                   1. A method for fabricating a photovoltaic cell, the method comprising:  
2                    providing a reusable substrate having a surface region, a cleave region, and a  
3 total thickness of material, the total thickness of material being at least N times greater than a  
4 first thickness of material to be removed, the first thickness of material to be removed being  
5 between the surface region and the cleave region;  
6                    coupling the surface region of the reusable substrate to a first surface region of  
7 an optically transparent substrate, the optically transparent substrate comprising the first  
8 surface region and a second surface region; and  
9                    subjecting a voltage differential between a first region and a second region of  
10 the reusable substrate to cause an increase in energy at one or more portions of the cleave  
11 region to facilitate removal of the first thickness of material from the reusable substrate,  
12 while the surface region remains coupled to the first surface region, to form a cleaved surface  
13 region coupled to the first surface region of the optically transparent substrate.
- 1                   2. The method of claim 1 further comprising subjecting the cleaved surface  
2 region using at least a mechanical polishing or a chemical etching process to remove a  
3 portion of a hydrogen damaged layer from the cleaved surface region.
- 1                   3. The method of claim 1 further comprising plasma activating the surface  
2 region and the first surface region before coupling the surface region to the first surface  
3 region.
- 1                   4. The method of claim 1 wherein the coupling comprises an optical coupling  
2 material between the surface region and the first surface region.
- 1                   5. The method of claim 4 wherein the optical coupling material comprises a  
2 tin oxide, indium tin oxide, zinc oxide, or titanium dioxide.
- 1                   6. The method of claim 1 further comprising forming a second thickness of  
2 semiconductor material using at least a solid phase epitaxy process or a gaseous phase  
3 epitaxial process to form substantially single crystal silicon material
- 1                   7. The method of claim 1 further comprising forming a second thickness of  
2 semiconductor material using at least forming an amorphous silicon layer.

1                   8. The method of claim 7 further comprising crystallizing the amorphous  
2 silicon layer.

1                   9. The method of claim 1 wherein the first thickness of semiconductor  
2 material comprises a single crystal silicon material.

1                   10. The method of claim 1 wherein the optically transparent material  
2 comprises a glass substrate or a quartz substrate or a plastic substrate

1                   11. The method of claim 1 wherein the optically transparent material  
2 comprises a conductive material including indium tin oxide or tin oxide

1                   12. The method of claim 1 wherein the thickness of material comprises one or  
2 more photovoltaic regions, the one or more photovoltaic regions comprising a first electrode  
3 and a second electrode.

1                   13. The method of claim 1 wherein the cleaving comprises a controlled  
2 cleaving process.

1                   14. The method of claim 1 wherein the cleaving comprises an initiation  
2 process and a propagation process to free the first thickness of material from a remaining  
3 portion of the semiconductor substrate.

1                   15. The method of claim 1 wherein the voltage differential ranges from about  
2 0.1 Volts to about 100 Volts.

1                   16. The method of claim 1 wherein the voltage differential is provided  
2 between a first electrode and a second electrode, the first electrode being coupled to the first  
3 region and the second electrode being coupled to the second region

1                   17. A reusable silicon substrate device for use with layer transfer process, the  
2 device comprising:

3                   a reusable substrate having a surface region, a cleave region, and a total  
4 thickness of material, the total thickness of material being at least N times greater than a first  
5 thickness of material to be removed, the first thickness of material to be removed being  
6 between the surface region and the cleave region, whereupon N is an integer greater than  
7 about ten;

8 a first electrode member coupled to a first region of the reusable substrate;  
9 a second electrode member coupled to a second region of the reusable  
10 substrate; and

11 a voltage source coupled between the first electrode member and the second  
12 electrode member to provide a voltage differential between the first region and the second  
13 region to cause an increase in energy at one or more portions of the cleave region to facilitate  
14 removal of the first thickness of material from the reusable substrate.

1 18. The device of claim 17 wherein the total thickness of material comprises  
2 single crystal silicon material.

1 19. The device of claim 17 wherein the total thickness of material comprises  
2 silicon material.

1 20. The device of claim 17 wherein the thickness of material is provided by a  
2 cleaving process.

1 21. The device of claim 20 wherein the cleaving process comprises an  
2 initiation process and a propagation process to free the first thickness of material from a  
3 remaining portion of the semiconductor substrate.

1 22. The device of claim 17 wherein the cleave region comprises a plurality of  
2 hydrogen particles.

1 23. The device of claim 17 further comprising a chuck member adapted to  
2 hold a handle substrate member in place, the chuck member being subjected to a second  
3 voltage potential between the first thickness of material and the handle substrate, the second  
4 voltage potential being provided to facilitate bonding of the first thickness of material and the  
5 handle substrate.

1 24. A cluster tool apparatus comprising:  
2 a first chamber, the first chamber comprising a reusable silicon substrate  
3 device for use with layer transfer process, the device comprising:  
4 a reusable substrate having a surface region, a cleave region, and a  
5 total thickness of material, the total thickness of material being at least N times greater than a  
6 first thickness of material to be removed, the first thickness of material to be removed being

7 between the surface region and the cleave region, whereupon N is an integer greater than  
8 about ten;

9 a first electrode member coupled to a first region of thereusable  
10 substrate;

11 a second electrode member coupled to a second region of thereusable  
12 substrate; and

13 a voltage source coupled between the first electrode member and the  
14 second electrode member to provide a voltage differential between the first region and the  
15 second region to cause an increase in energy at one or more portions of the cleave region to  
16 facilitate removal of the first thickness of material from the reusable substrate;

17 a second chamber coupled to the first chamber, the second chamber being  
18 provided to perform one or more processes.

1 25. The apparatus of claim 24 further comprising a third chamber, the third  
2 chamber comprising one or more thermal energy providing elements therein.

1 26. The apparatus of claim 24 further comprising an input/output chamber,  
2 the input/output chamber being coupled to the first chamber.

1 27. A reusable silicon substrate device for use with layer transfer process, the  
2 device comprising:

3 a reusable substrate having a surface region, a cleave region, and a total  
4 thickness of material, the total thickness of material being at least N times greater than a first  
5 thickness of material to be removed, the first thickness of material to be removed being  
6 between the surface region and the cleave region, whereupon N is an integer greater than  
7 about ten;

8 a chuck member adapted to hold a handle substrate member in place, the  
9 chuck member, the chuck member being configured to hold the handle substrate in a manner  
10 to facilitate bonding the handle substrate to the first thickness of material to be removed; and

11 a mechanical pressure device operably coupled to the chuck member, the  
12 mechanical pressure device being adapted to provide a force to cause bonding of the handle  
13 substrate to the first thickness of material to be removed.

1 28. The device of claim 27 wherein the mechanical pressure device is a  
2 cylindrical roller device, the cylindrical roller device being adapted to provide pressure in a

3 spatial manner from a first portion of the chuck device to a second portion of the chuck  
4 device.

1 29. A reusable silicon substrate device for use with more than one layer  
2 transfer processes, the device comprising:

3 a donor substrate member, the donor substrate member having a plurality of  
4 sites; and

5 a plurality of reusable substrates arranged respectively on the plurality of sites,  
6 each of the reusable substrates having a surface region, a cleave region, and a total thickness  
7 of material, the total thickness of material being at least N times greater than a first thickness  
8 of material to be removed, the first thickness of material to be removed being between the  
9 surface region and the cleave region, whereupon N is an integer greater than about ten.

1 30. A method fabricating one or more semiconductor substrates, the method  
2 comprising:

3 providing a reusable substrate having a surface region, a cleave region, and a  
4 total thickness of material, the total thickness of material being at least N times greater than a  
5 first thickness of material to be removed, the first thickness of material to be removed being  
6 between the surface region and the cleave region; and

7 subjecting a voltage differential between a first region and a second region of  
8 the reusable substrate to cause an increase in energy at one or more portions of the surface  
9 region to facilitate bonding of the surface region to a first surface region of a handle  
10 substrate.

1 31. The method of claim 30 wherein the increase in energy causes an increase  
2 in temperature of the surface region from a first temperature to a second temperature.

1 32. The method of claim 30 wherein the increase in energy causes an increase  
2 in temperature from a first temperature to a second temperature, the second temperature being  
3 greater than about 200 Degrees Celsius.

1 33. A method of fabricating one or more semiconductor substrates, the  
2 method comprising:

3 providing a reusable substrate having a surface region, a cleave region, and a  
4 total thickness of material, the total thickness of material being at least N times greater than a

5 first thickness of material to be removed, the first thickness of material to be removed being  
6 between the surface region and the cleave region; and  
7           subjecting a voltage differential between a first region and a second region of  
8 the reusable substrate to cause an increase in energy at one or more portions of the cleave  
9 region to change a characteristic of the cleave region from a first characteristic to a second  
10 characteristic, the second characteristic causing removal of the first thickness of material  
11 from the reusable substrate.

1           34. The method of claim 33 wherein the voltage differential increases a  
2 temperature of one or more portions of the surface region to cure a bonding layer provided  
3 overlying the one or more portions of the surface region.

1           35. A method of fabricating a module comprising a plurality of individual  
2 photovoltaic cells, the method comprising:  
3           providing a plurality of single crystal silicon tiles;  
4           implanting ions into surfaces of the single crystal silicon tiles;  
5           adhering a process chuck to the implanted surfaces of the single crystal silicon  
6 tiles;  
7           detaching from the single crystal silicon tiles, a plurality of single crystal  
8 silicon films adhered to the process chuck; and  
9           batch processing the detached single crystal silicon films adhered to the  
10 process chuck.

1           36. The method according to claim 35 wherein the batch processing  
2 comprises removing a residual layer from a top surface of the single crystal silicon films.

1           37. The method according to claim 35 wherein the batch processing  
2 comprises forming a P/N junction.

1           38. The method according to claim 35 wherein the batch processing  
2 comprises forming a passivation layer.

1           39. The method according to claim 35 wherein the batch processing  
2 comprises placing the films and any layers present thereon into contact with a common  
3 substrate.

1           40. The method according to claim 35 wherein the batch processing  
2 comprises placing the films and any layers present thereon into contact with a common  
3 substrate.

1           41. The method according to claim 40 wherein the films and any layers  
2 present thereon are bonded to the common substrate by an adhesive.

1           42. The method according to claim 35 wherein the films and any layers  
2 present thereon are bonded to the common adhesive by the application of a voltage  
3 differential.

1           43. The method according to claim 35 wherein the batch processing  
2 comprises placing the films and any layers present thereon into contact with a second process  
3 chuck.

1           44. The method according to claim 35 wherein the process chuck is adhered  
2 to the implanted surface utilizing a vacuum or an electrostatic force.

1           45. The method according to claim 35 wherein the ions are implanted at an  
2 energy of 600 keV or more.

1           46. The method according to claim 35 wherein the tiles have a lateral  
2 dimension of about 125mm x 125mm.

1           47. An apparatus comprising:  
2 a substrate support member;  
3 a voltage source;  
4 a first conducting contact in electrical communication with the voltage source;  
5 and  
6 a second conducting contact in electrical communication with the voltage  
7 source, such that application of a potential difference between the first and second contacts  
8 produces a thermal gradient in a substrate supported on the substrate support member.

1           48. The apparatus of claim 47 wherein the first conducting contact and the  
2 second conducting contact are configured to produce the thermal gradient at an interface  
3 between the substrate and a second substrate.



1           49. The apparatus of claim 47 wherein the first conducting contact and the  
2 second conducting contact are configured to produce the thermal gradient during  
3 implantation of ions into the substrate.

1           50. The apparatus of claim 47 further comprising an energy sink or source in  
2 thermal communication with the substrate.

1           51. The apparatus of claim 47 wherein the first contact and the second contact  
2 are positioned at a same height relative to the substrate.

1           52. The apparatus of claim 47 wherein the first contact and the second contact  
2 are positioned at different heights relative to the substrate.

1           53. A method of fabricating a solar module comprising a plurality of  
2 individual photovoltaic cells, the method comprising:

3           providing a plurality of single crystal silicon tiles, each of the single crystal  
4 tiles having a thickness capable of forming a plurality of free standing slices, each of the  
5 single crystal tiles having a surface region;

6           implanting ions through the surface region of each of the single crystal silicon  
7 tiles;

8           detaching at least one of the slices from each of the single crystal silicon tiles  
9 to form a plurality of detached slices;

10          supporting the plurality of detached slices on a process chuck;

11          forming one or more photovoltaic regions on each of the plurality of detached  
12 slices;

13          coupling each of the plurality of detached slices to an optical plate; and

14          forming one or more contact regions on each of the plurality of detached  
15 slices, which is coupled to the optical plate, to form a solar module.

1           54. A method fabricating one or more semiconductor substrates, the method  
2 comprising:

3           providing a reusable substrate having a surface region, a cleave region, and a  
4 total thickness of material, the total thickness of material being at least N times greater than a  
5 first thickness of material to be removed, the first thickness of material to be removed being  
6 between the surface region and the cleave region; and

7                   subjecting a voltage differential between a first region and a second region of  
8 the reusable substrate to cause an increase in energy at one or more portions of the surface  
9 region.

1                   55. The method of claim 54 wherein the increase in energy facilitates bonding  
2 of the surface region to a first surface region of a handle substrate.

1                   56. The method of claim 54 wherein the increase in energy causes an increase  
2 in temperature of the surface region from a first temperature to a second temperature.

1                   57. The method of claim 54 wherein the increase in energy causes an increase  
2 in temperature from a first temperature to a second temperature, the second temperature being  
3 greater than about 200 Degrees Celsius.

1                   58. A method of fabricating a module comprising a plurality of individual  
2 photovoltaic cells, the method comprising:  
3                   providing a plurality of single crystal silicon tiles;  
4                   implanting ions into surfaces of the single crystal silicon tiles;  
5                   detaching from the single crystal silicon tiles, a plurality of single crystal  
6 silicon films adhered to the process chuck; and  
7                   processing the detached single crystal silicon films in a batch corresponding to  
8 a module.

1                   59. The method of claim 58 wherein the plurality of single crystal silicon  
2 films are detached by adhesion to a common processing chuck.

1                   60. The method of claim 58 wherein the plurality of single crystal silicon  
2 films are maintained in contact against the tile for a period of time after the detaching.

1                   61. The method of claim 60 wherein the plurality of single crystal silicon  
2 films are maintained in contact against the tile by a positive pressure.

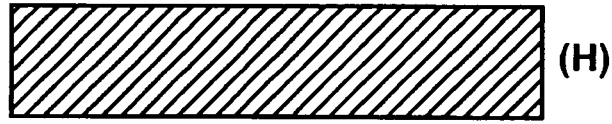


FIGURE 1



FIGURE 2

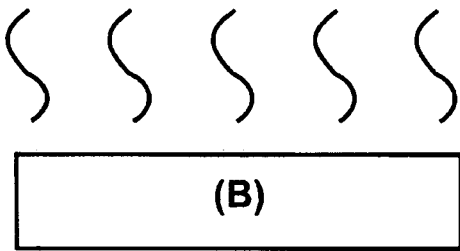


FIGURE 3

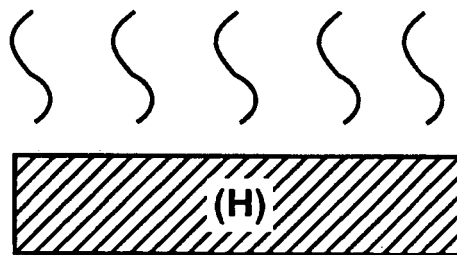


FIGURE 4

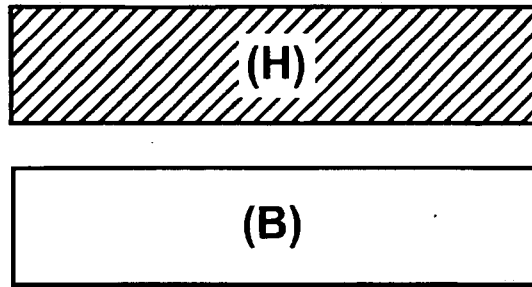


FIGURE 5

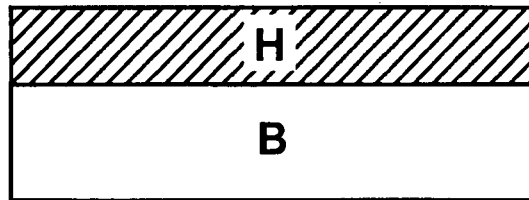


FIGURE 6

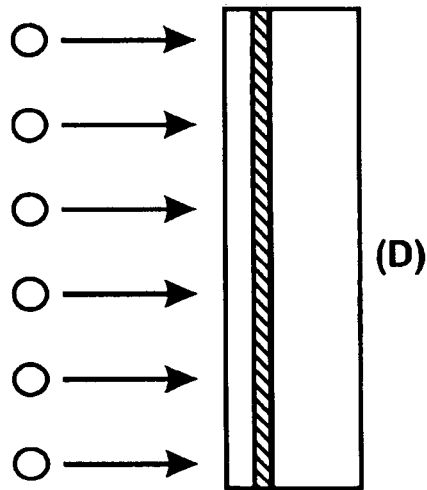


FIGURE 7

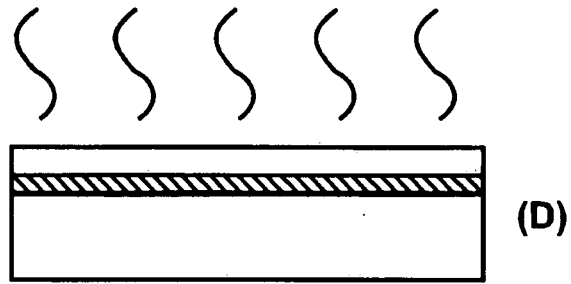


FIGURE 8

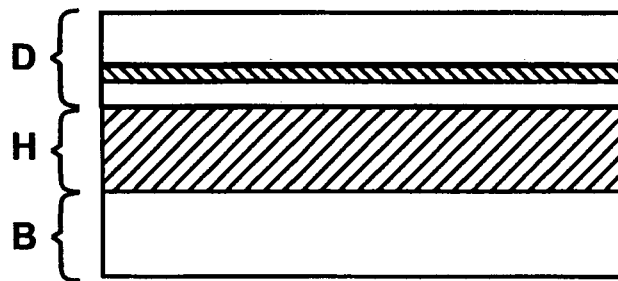


FIGURE 9

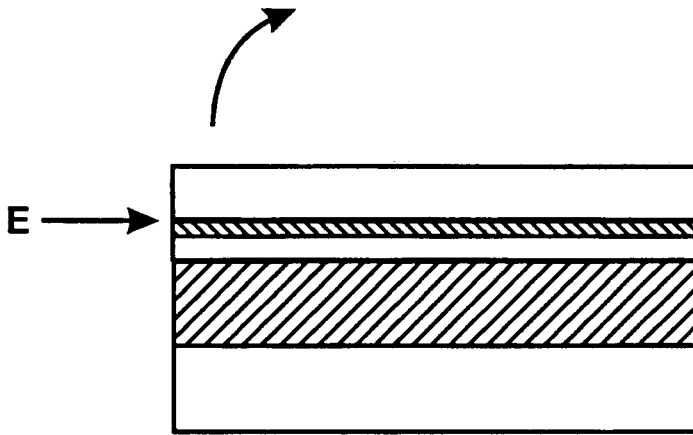


FIGURE 10

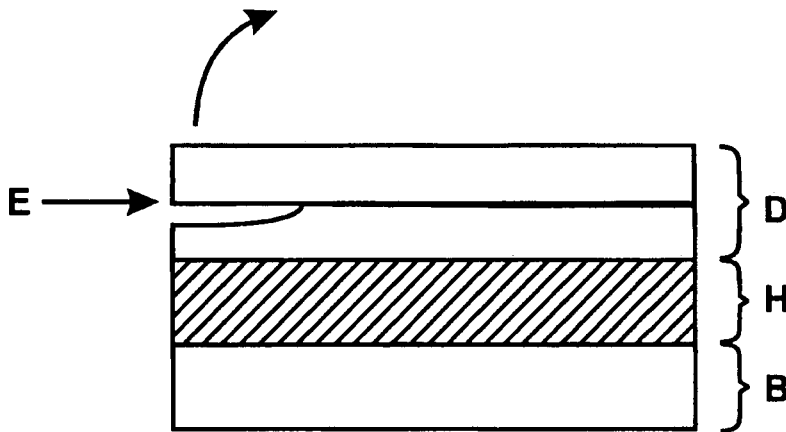
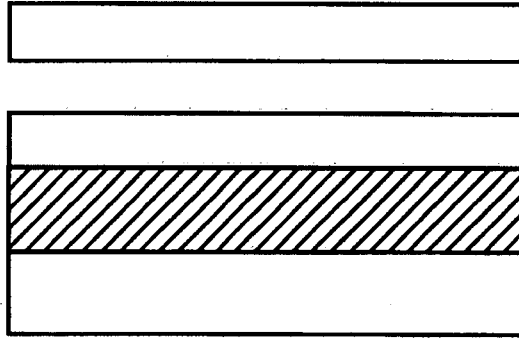
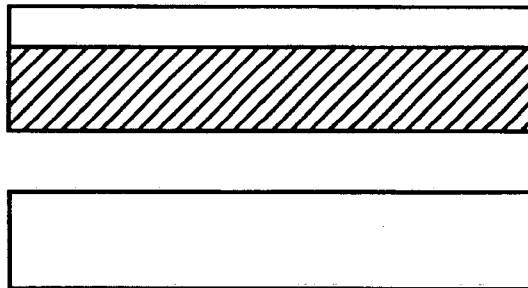


FIGURE 11



**FIGURE 12**



**FIGURE 13**

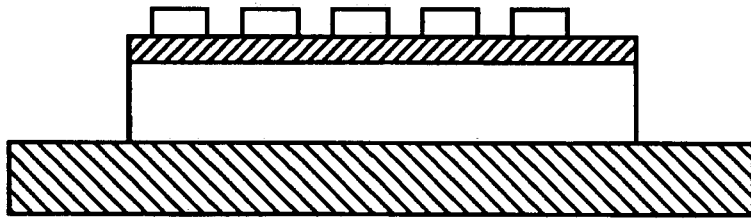
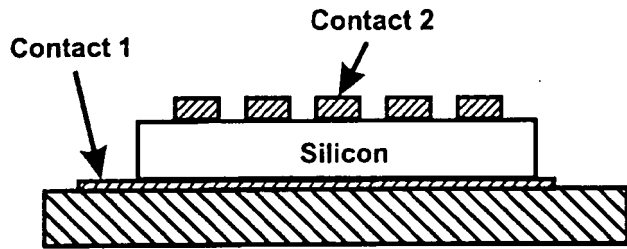


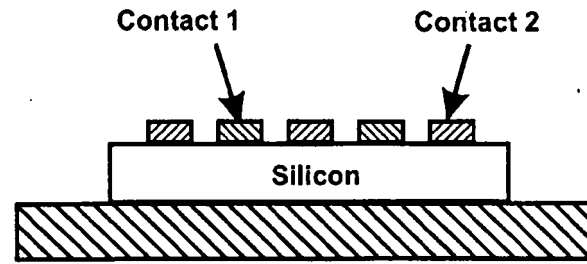
FIGURE 14





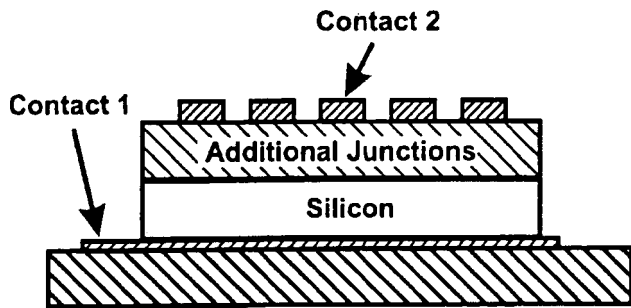
Glass Substrate  
1. Single-Crystal Silicon  
(Transverse Contacts)

FIGURE 15



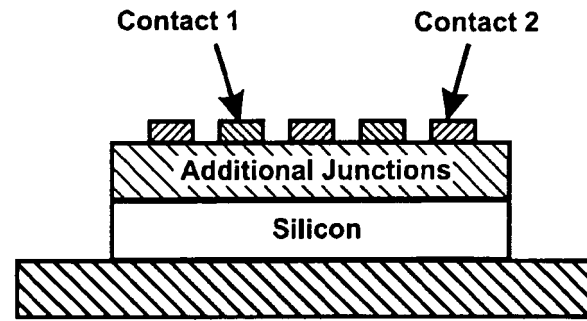
Glass Substrate  
2. Single-Crystal Silicon  
(Longitudinal Backside Contacts)

FIGURE 17



Glass Substrate  
1. Single-Crystal Silicon &  
Additional Junctions  
(Transverse Contacts)

FIGURE 16



Glass Substrate  
2. Single-Crystal Silicon  
(Longitudinal Backside Contacts)

FIGURE 18

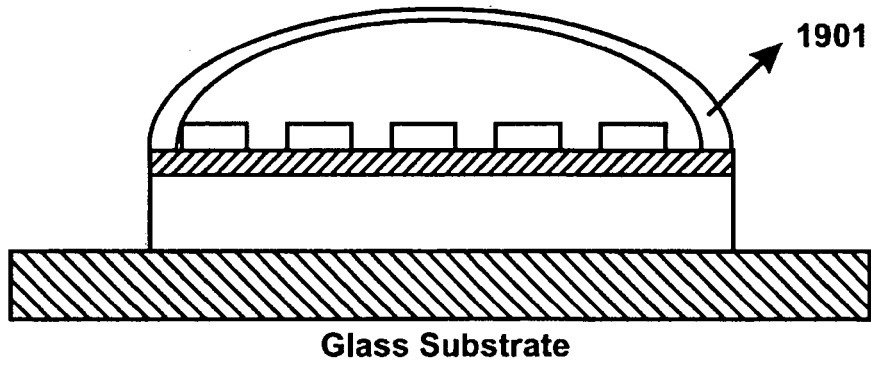


FIGURE 19

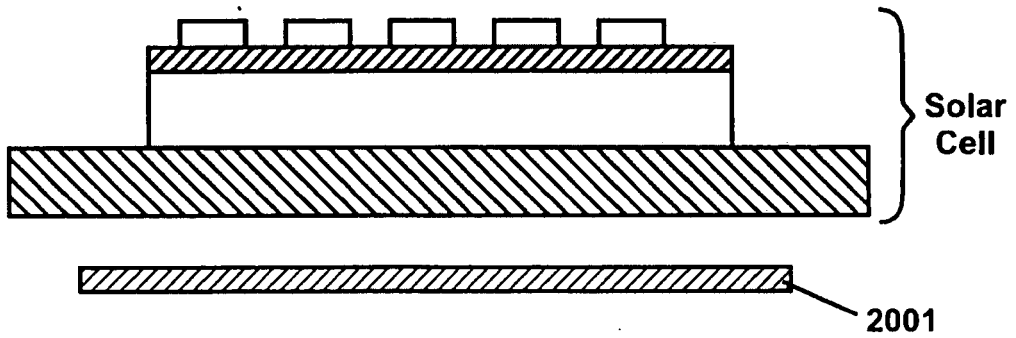


FIGURE 20

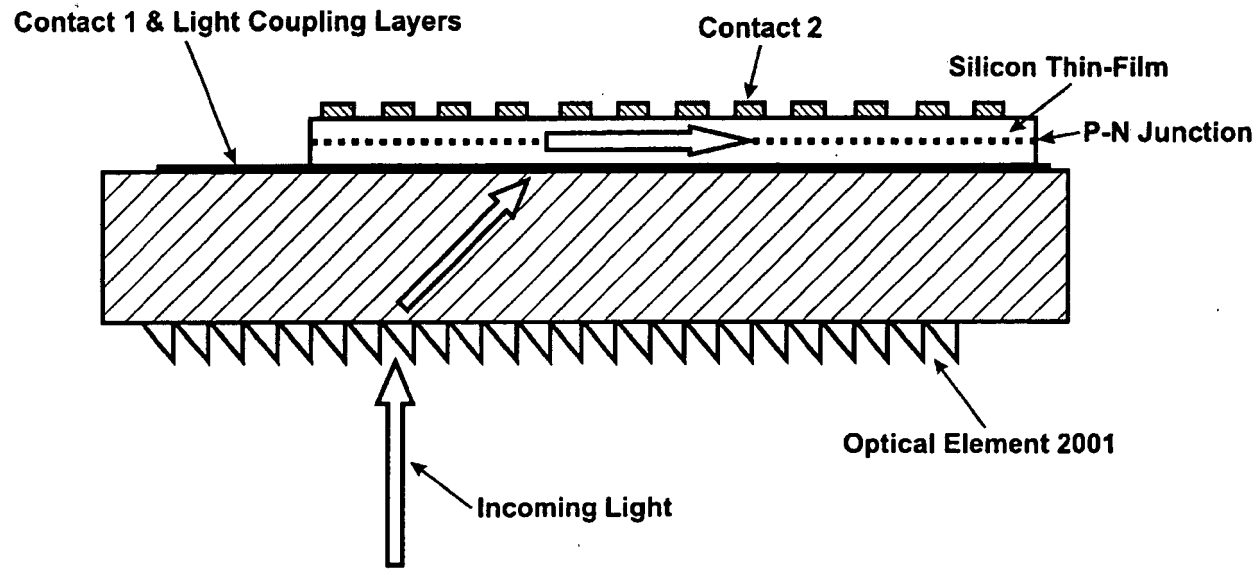


FIGURE 21

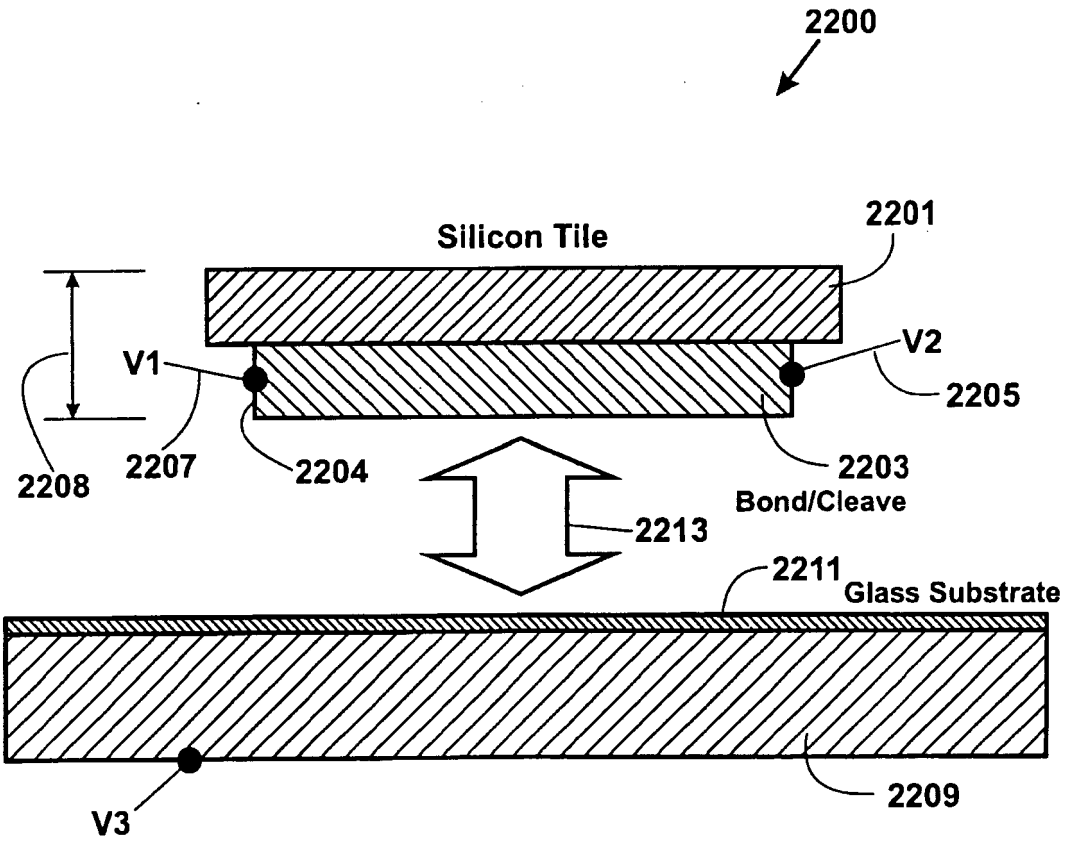


FIGURE 22

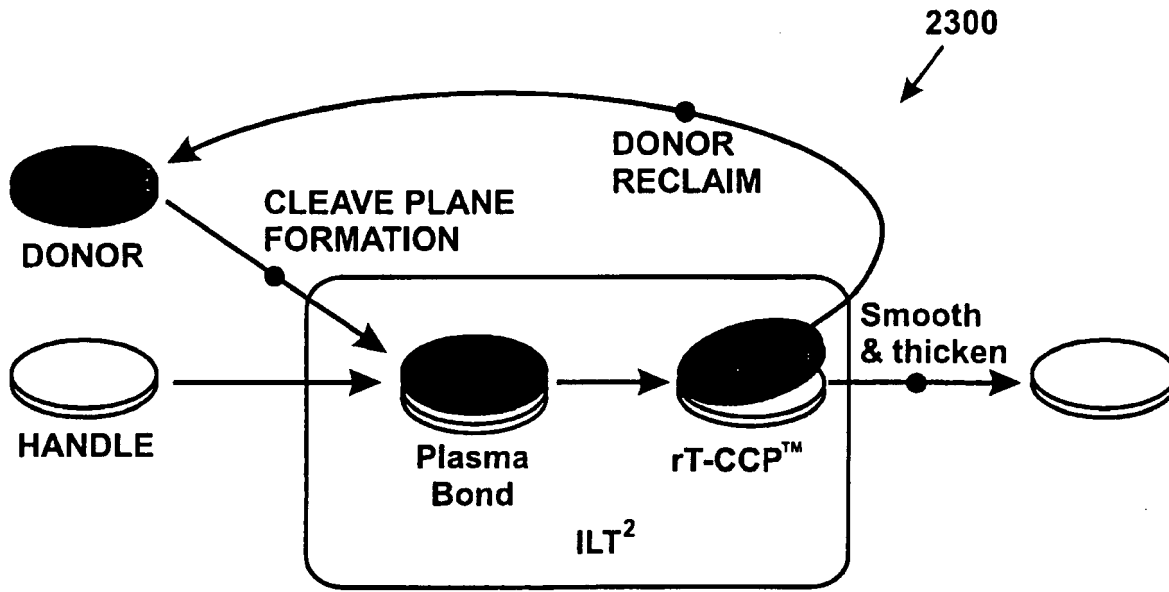


FIGURE 23

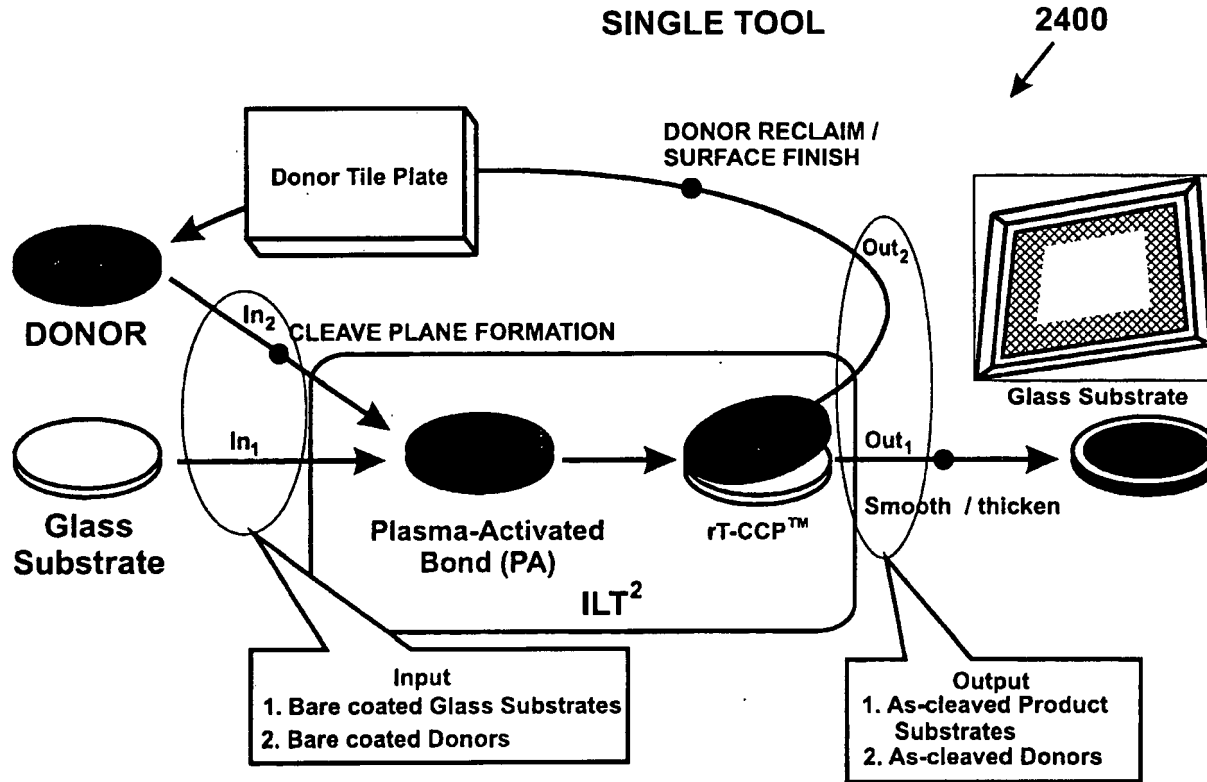


FIGURE 24

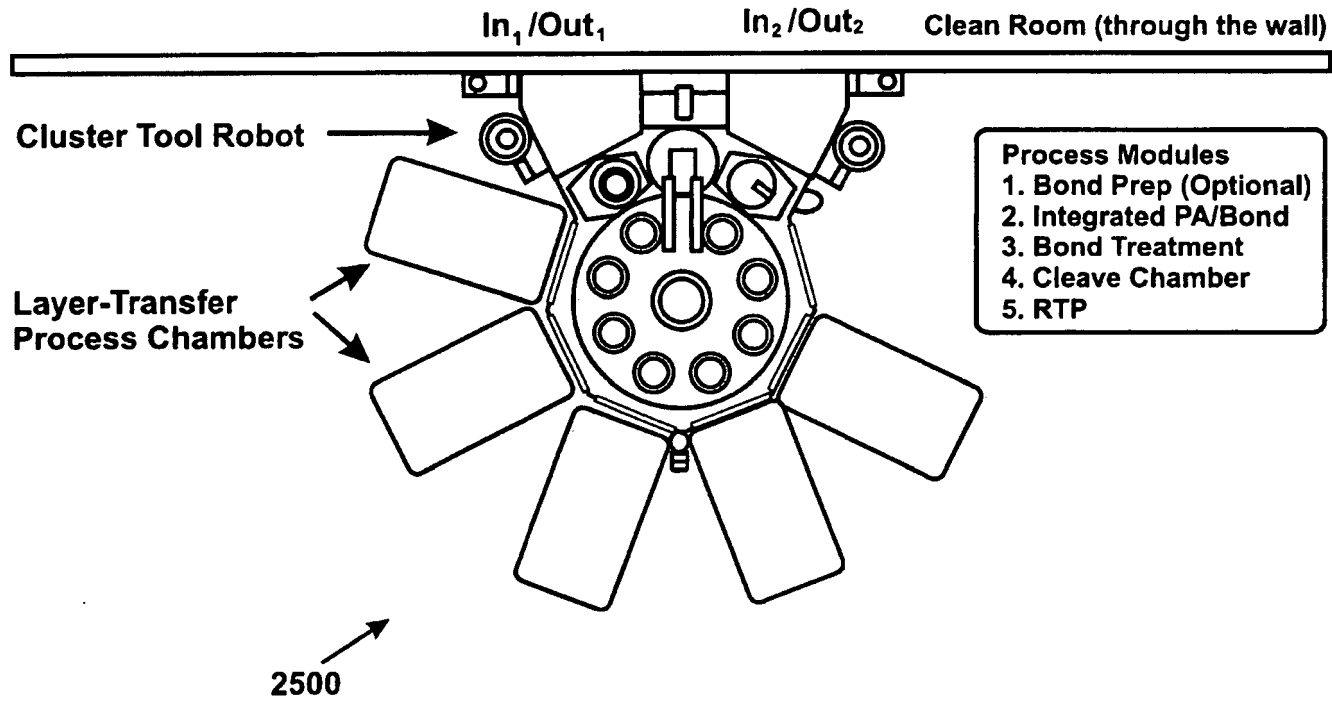


FIGURE 25

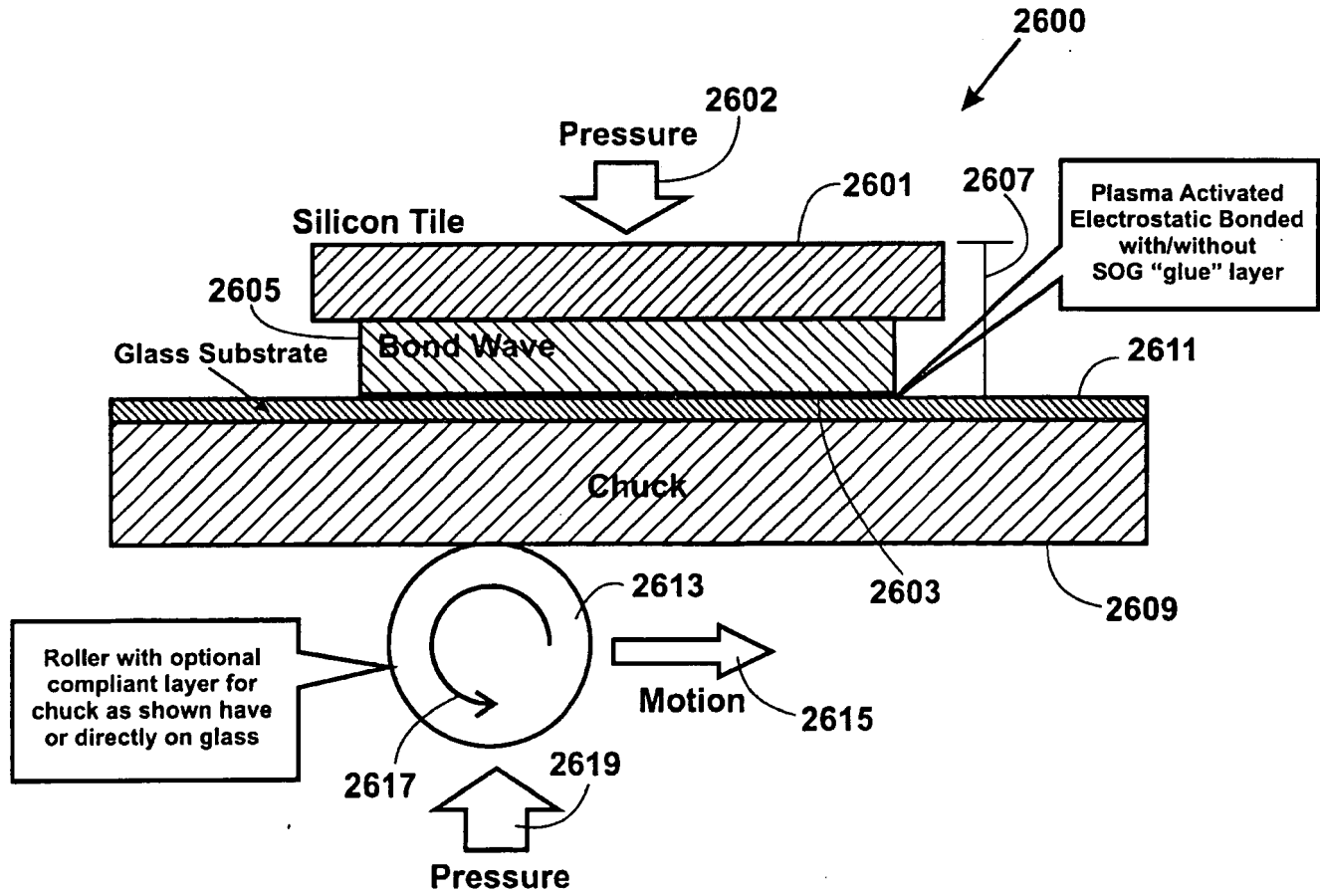


FIGURE 26



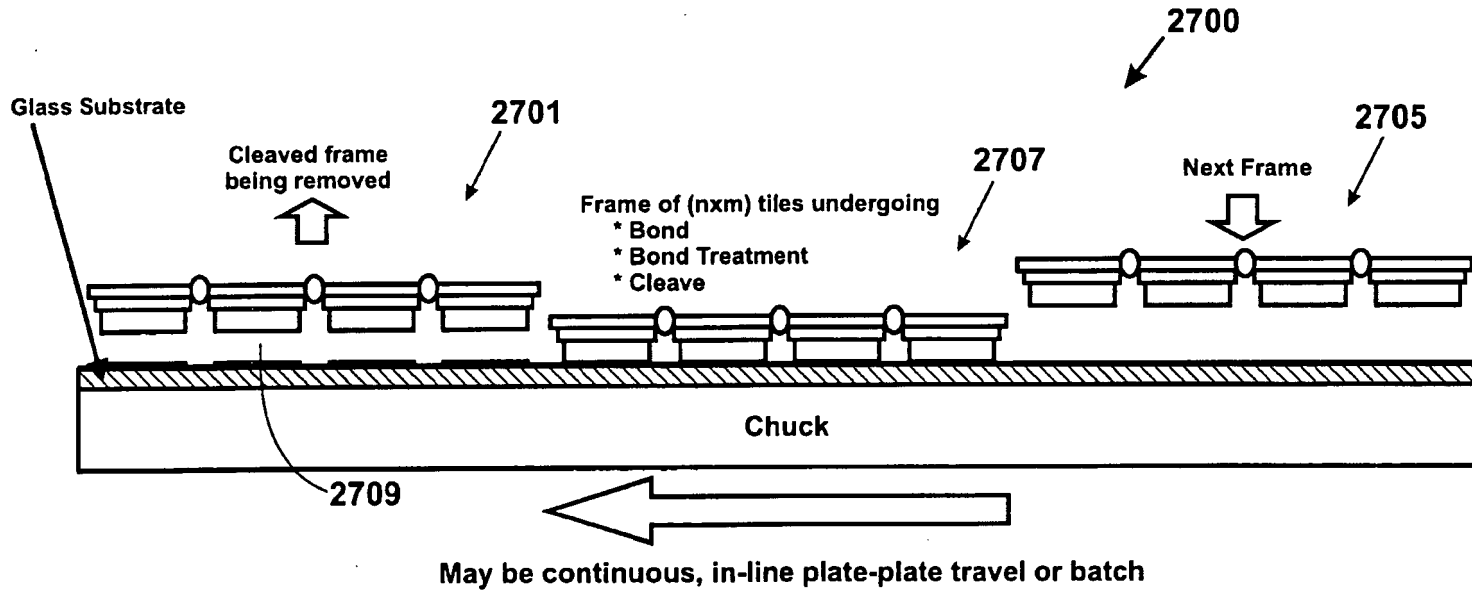


FIGURE 27

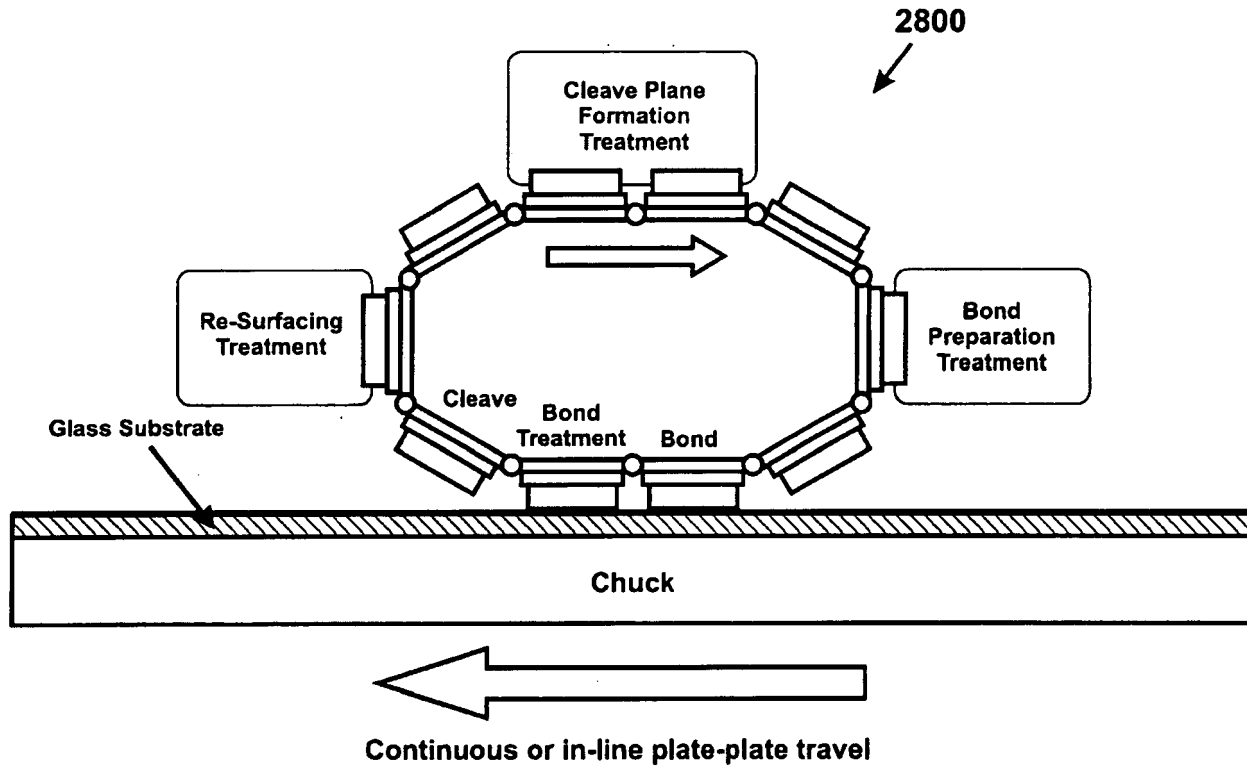


FIGURE 28

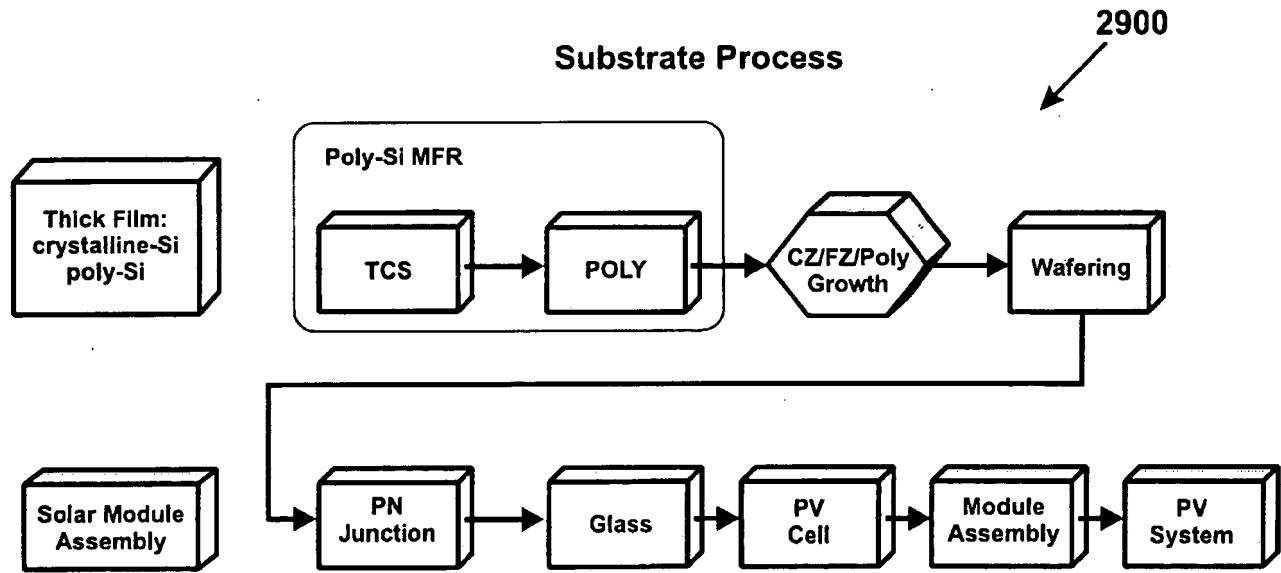


FIGURE 29

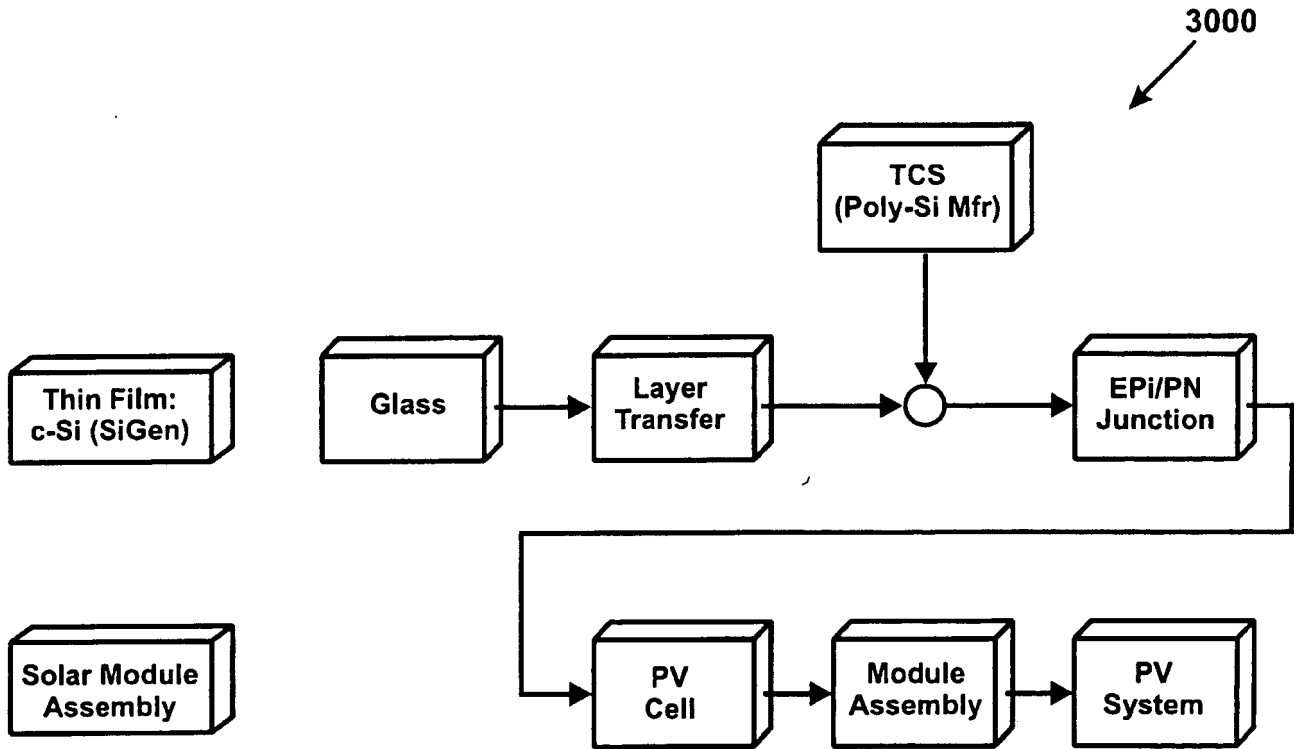


FIGURE 30

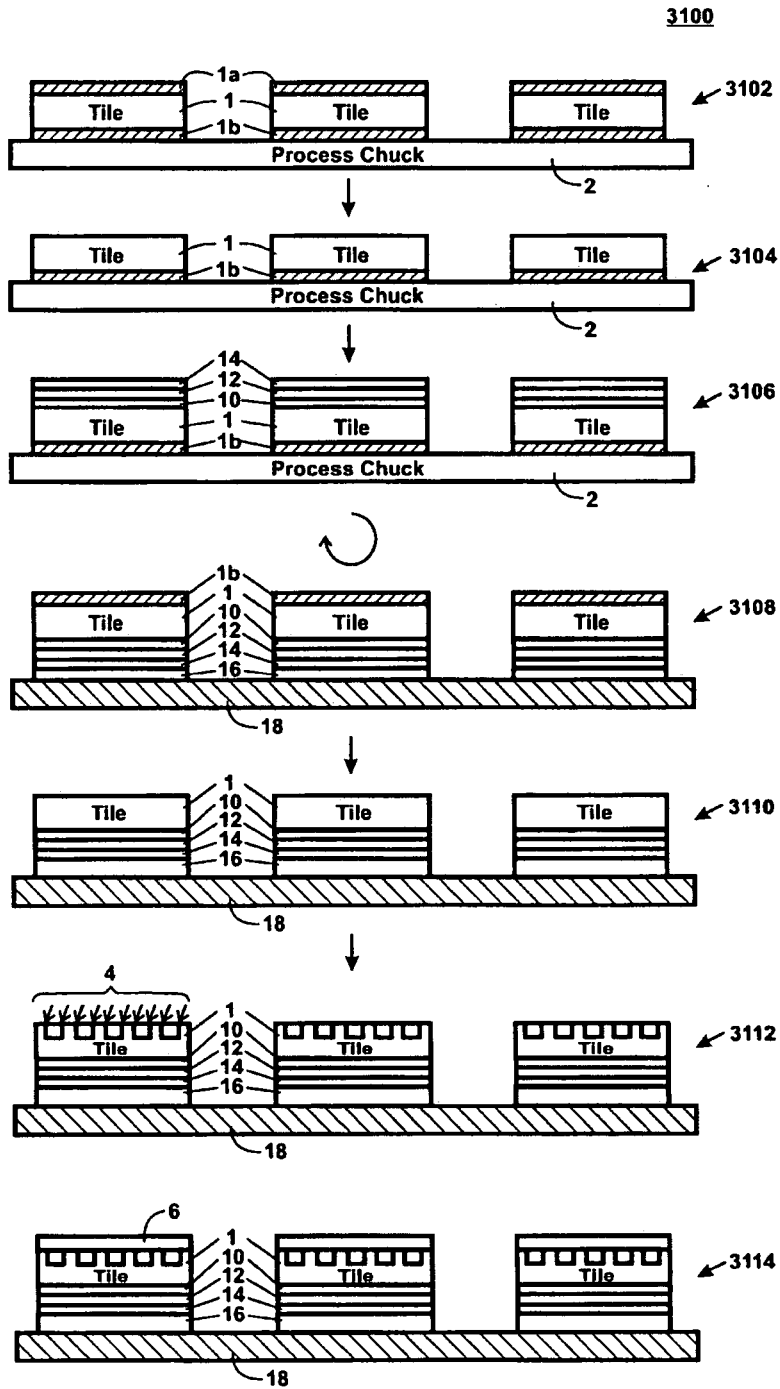


FIGURE 31

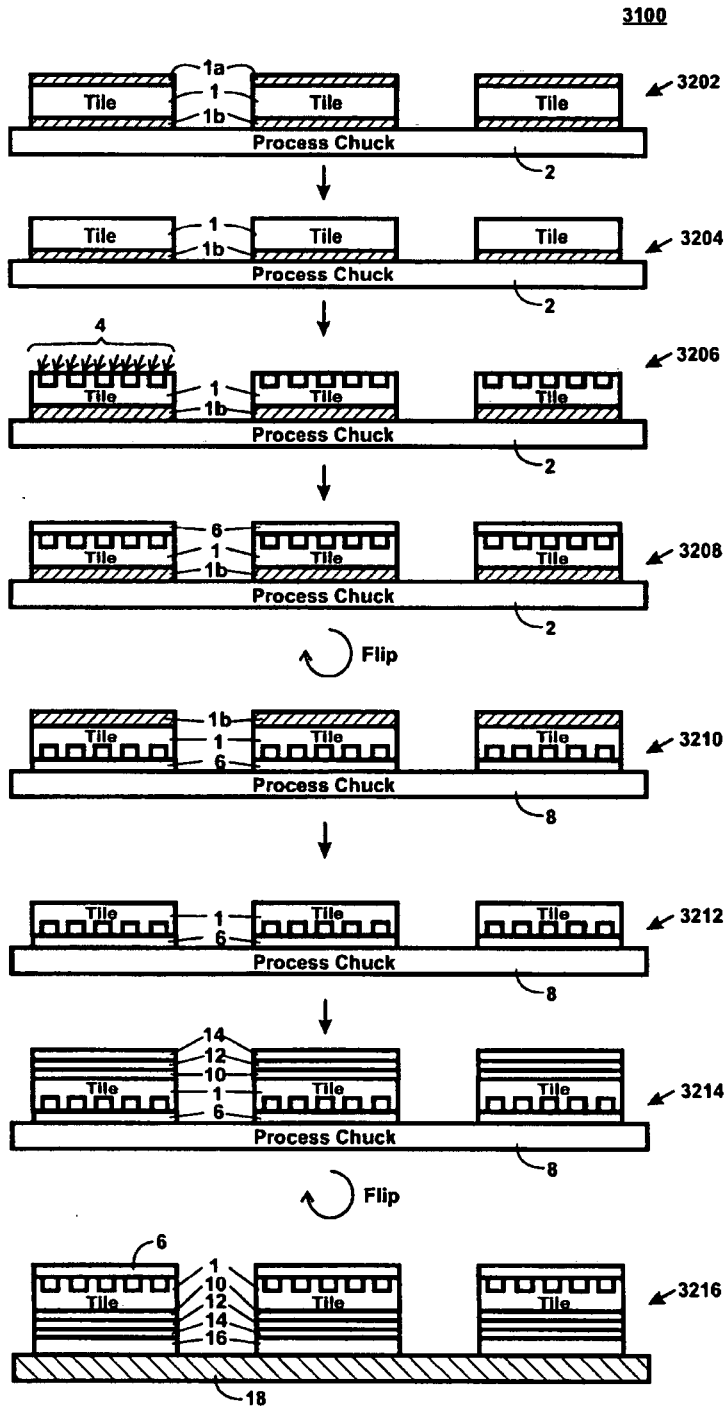


FIGURE 32

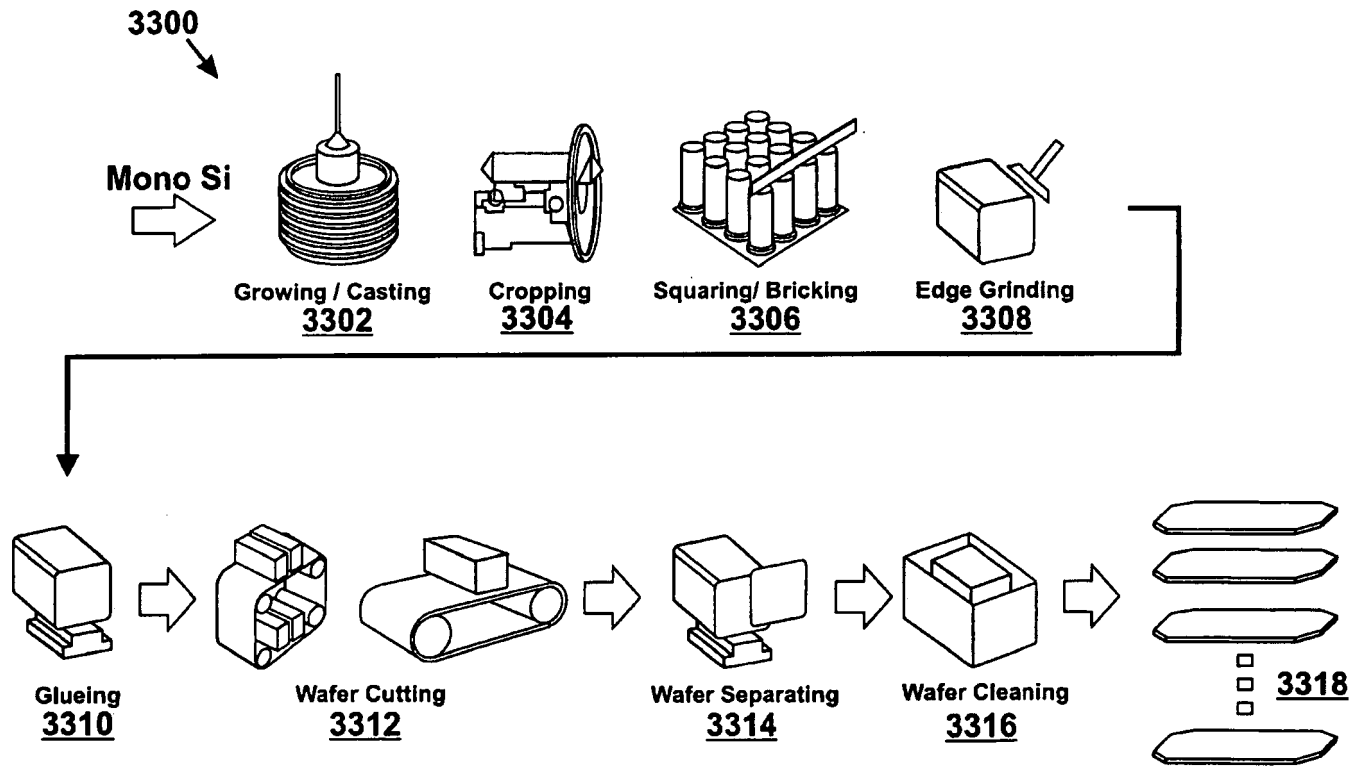


FIGURE 33

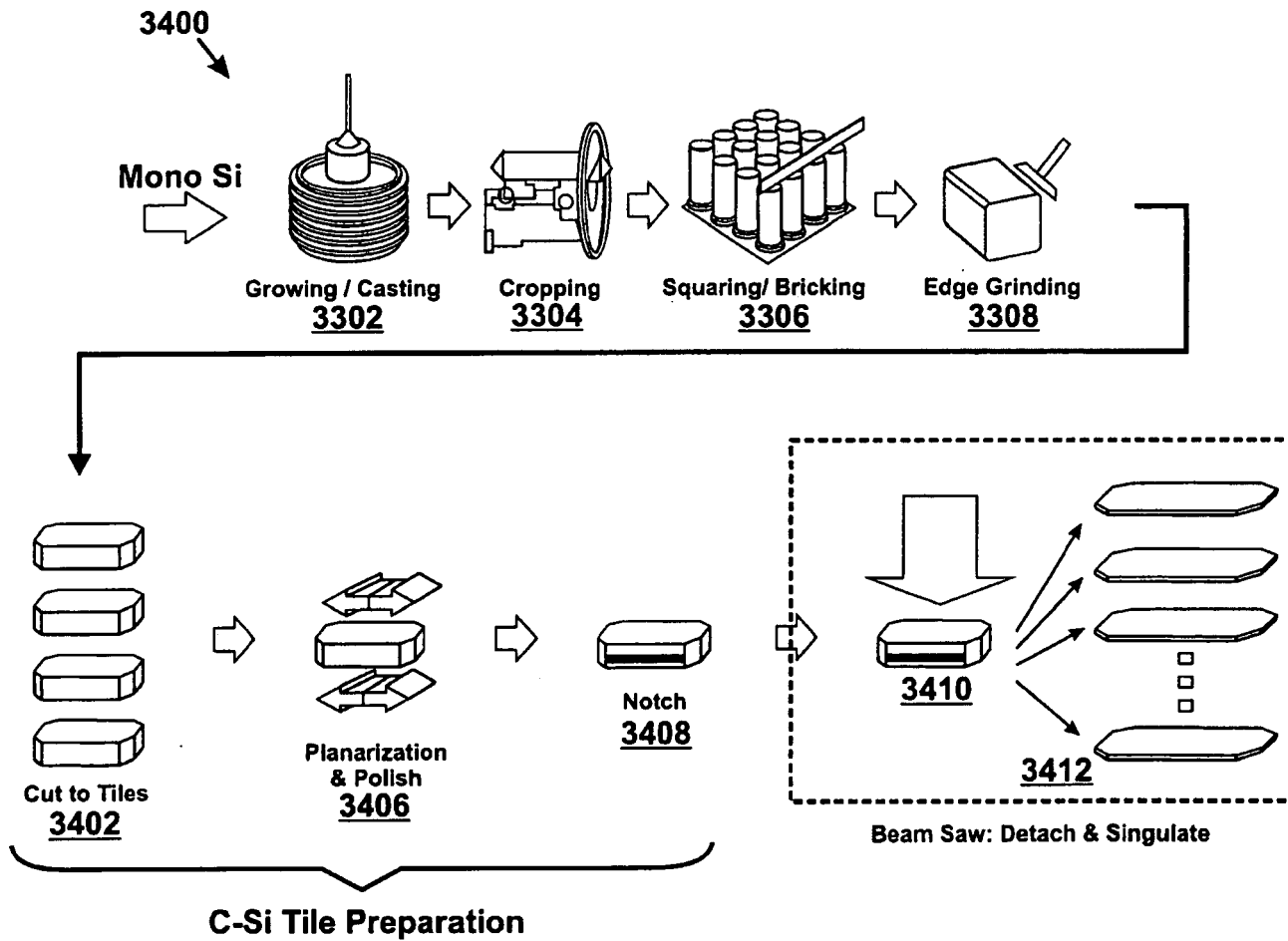


FIGURE 34



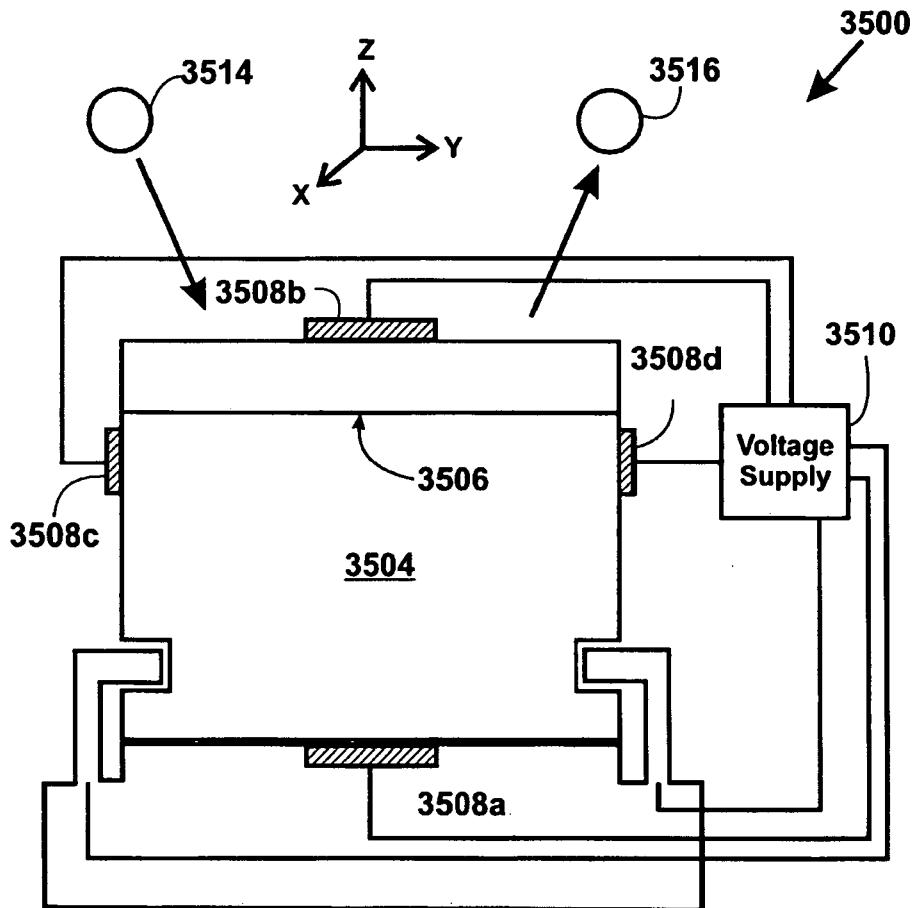


FIGURE 35

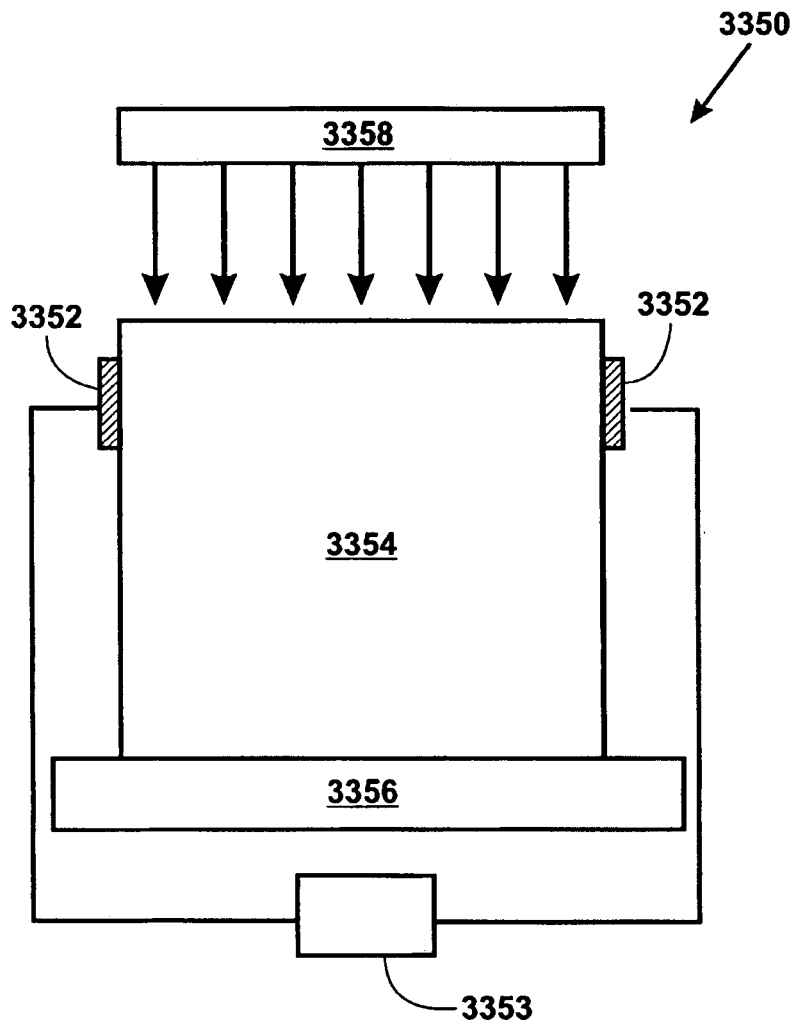
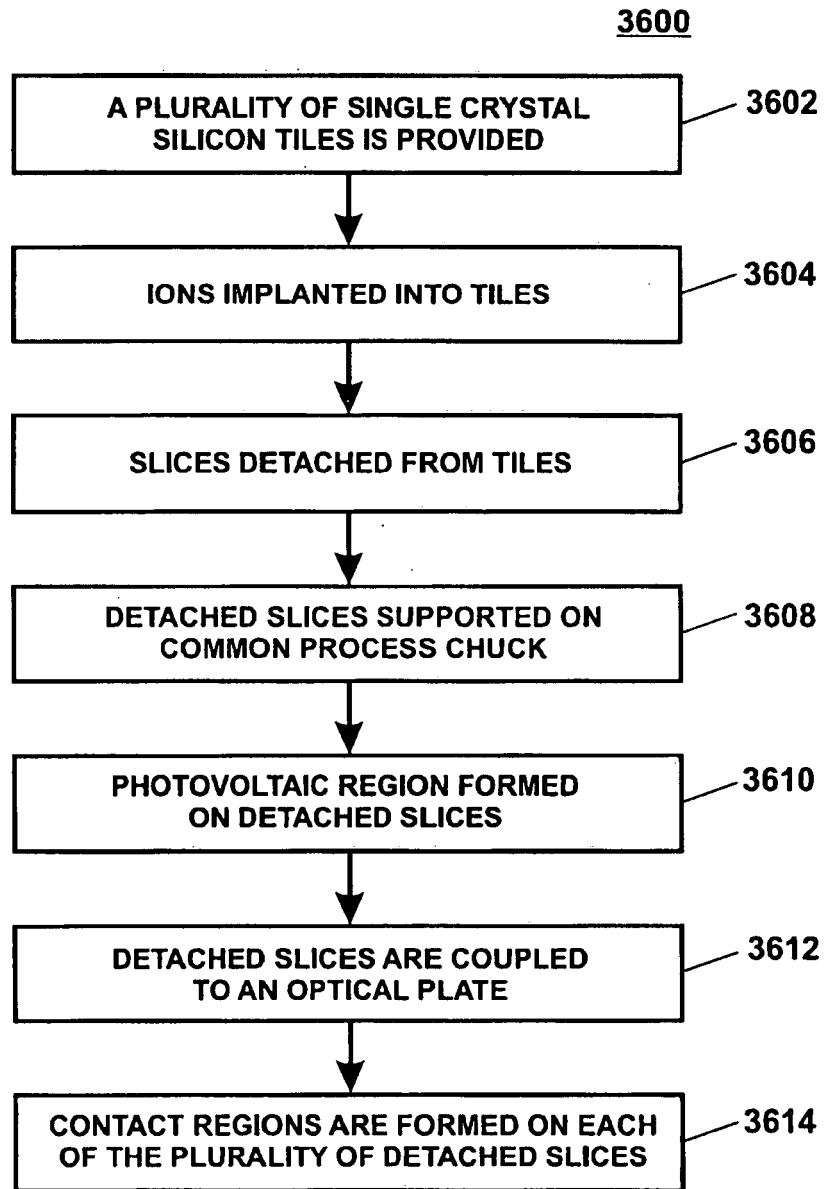


FIGURE 35A



**FIGURE 36**