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(54) Title: CELL MEASUREMENT BASED ON PRIMARY SYNCHRONIZATION SIGNAL

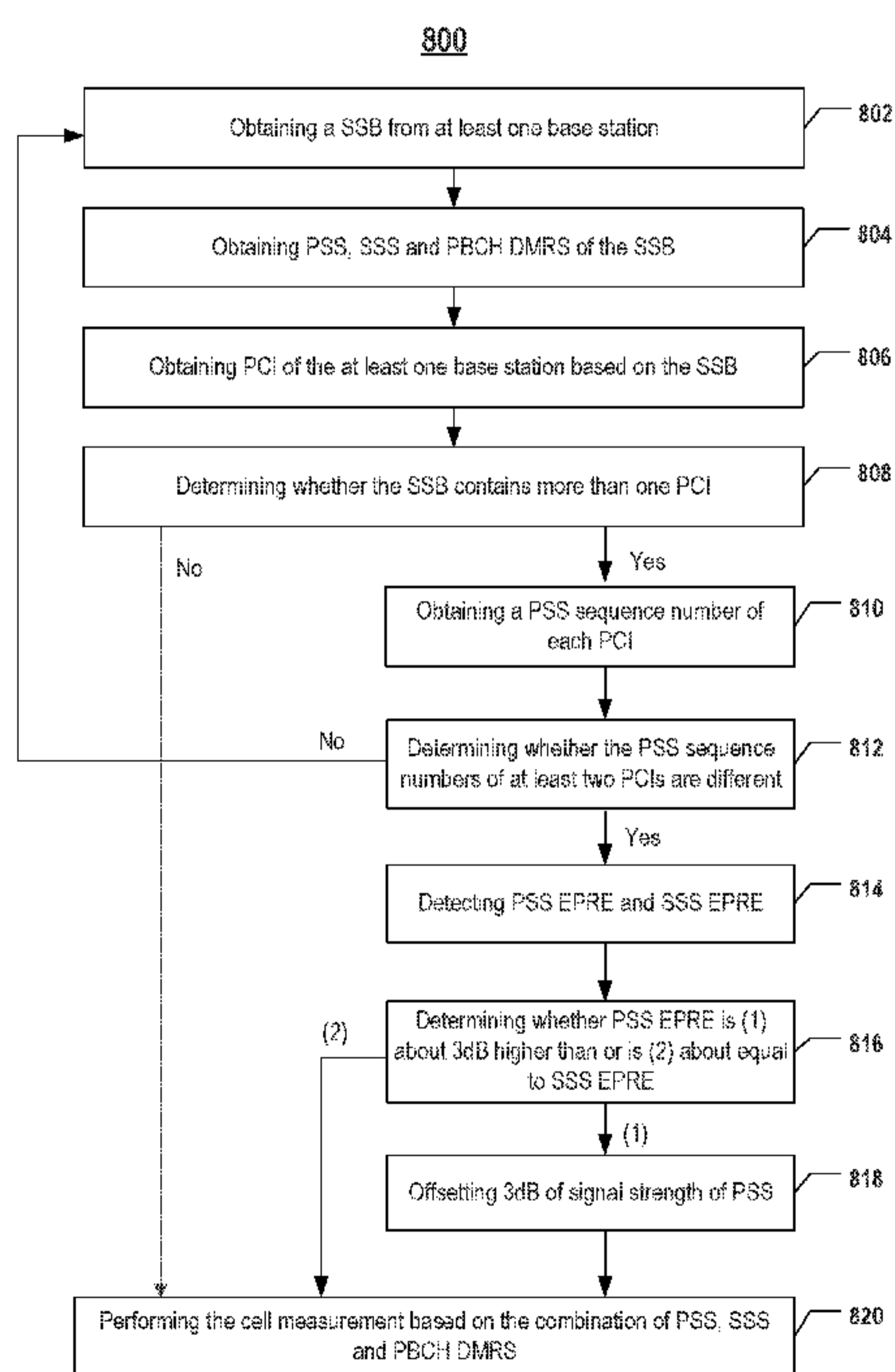


FIG. 8

(57) Abstract: A terminal device includes at least one processor and memory storing instructions. The instructions, when executed by the at least one processor, cause the terminal device at least to obtain a Synchronization Signal/ Physical Broadcast Channel (PBCH) Block (SSB) from at least one base station; obtain a Primary Synchronization Signal (PSS), a Secondary Synchronization Signal (SSS) and a PBCH Demodulation Reference Signal (DMRS) of the SSB; and perform a cell measurement based on a combination of the PSS, the SSS, and the PBCH DMRS.



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B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)
See Search History document

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched
See Search History document

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)
See Search History document

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X — Y — A	WO 2020/041196 A1 (INTEL CORPORATION) 27 February 2020; abstract, paragraphs [0019]-[0029], [0033], [0044], [0045], [0047], claim 1	1, 10-11, 14, 19 — 2-3, 7, 12, 15, 17 — 4-6, 8-9, 13, 16, 18, 20
Y — A	US 2017/0181065 A1 (ZTE WINSTRON TELECOM AB) 22 June 2017; paragraphs [0029], [0034]-[0036], [0040], [0050], claims 1 & 15	2-3, 7, 12, 15, 17 — 4-6, 8-9, 13, 16, 18, 20
A	US 2019/0223163 A1 (LG ELECTRONICS INC.) 18 July 2019; paragraph [0116]	1-20
A	US 2020/0015214 A1 (SAMSUNG ELECTRONICS CO., LTD.) 09 January 2020; see entire document	1-20
A	US 2020/0022068 A1 (QUALCOMM INCORPORATED) 16 January 2020; see entire document	1-20
A	US 2019/0363809 A1 (LG ELECTRONICS INC.) 28 November 2019; see entire document	1-20

Further documents are listed in the continuation of Box C.

See patent family annex.

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