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(54) **Bus system optimization**

(57) A bus system comprising a master connected to one or more slave devices via a bus is disclosed. The bus system is able to effectively communicate control information during a calibration phase and to individually determine appropriate timing and/or voltage offsets for each slave device. The offsets are used to optimise transfer timing (including duty cycle characteristics), signal equalization, and voltage levels for data exchanged between the master and the slave devices.

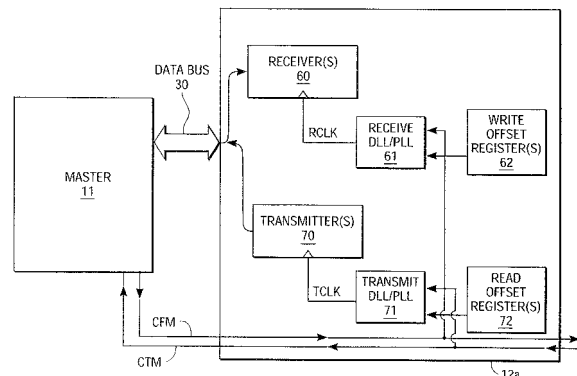


FIG. 6

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Place of search Munich		Date of completion of the search 22 January 2008	Examiner Ghidini, Mario
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**ANNEX TO THE EUROPEAN SEARCH REPORT  
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The members are as contained in the European Patent Office EDP file on  
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