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- (71) Applicant: TESSERA, INC. [US/US]; 3099 Orchard Drive, San Jose, CA 95134 (US).
- (72) Inventors: KANG,teck-Gyu; 7112 Clarendon Street, San Jose, CA 95129 (US). KUBOTA, Yoichi; 6114 Allbrook Circle, Pleasanton, CA 94588 (US).

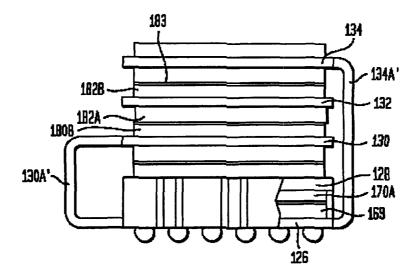
- (74) Agents: MILLET, Marcus, J. et al.; Lerner, David, Littenberg, Krumholz & Mentlik, LLP, 600 South Avenue West, Westfield, NJ 07090 (US).
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(54) Title: ASSEMBLIES HAVING STACKED SEMICONDUCTOR CHIPS



(57) Abstract: A stacked microelectronic assembly comprises a continuous sheet (20) including a core panel (26) and a plurality of side panels (28, 30, 32, 34), each having a folded portion (28a', 30a', 32a', 34a') that connects the side panel to an edge of the core panel. At least two of the panels are at least partially horizontally aligned with one another in a stack. During manufacture of a stacked microelectronic assembly, failed microelectronic elements are identified and associated side panels thereof are simply cut-off. This results in the production of a usable stacked microelectronic assembly albeit of reduced capacity, or reduced functionality.

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B. FIELDS SEARCHED			
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C. DOCUMENTS CONSIDERED TO BE RELEVANT			
Category *	Citation of document, with indication, where a		Relevant to claim No.
Α	US 5,117,282 A (Salatino) 26 May 1992 (26.05.1992), Fig. 1		1-3
A	US 5,448,511 A (Paurus et al.) 5 September 1995 (05.09.1995), Abstract.		1-40
A	US 6,225,688 B1 (Kim et al.) 1 May 2001 (01.05.2001), Abstract and Fig. 5.		1-40
Α	US 6,351,029 B1 (Isaak) 26 February 2002 (26.02.2002), Figs 1-2		1-40
A	US 5,776,797 A (Nicewarner, Jr. et al.) 7 July 1998 (07.07.1998), Column 11, lines 10+.		1-3
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P.O. Box 1450		Telephone No. (571) 272-1726	
	kandria, Virginia 22313-1450 . (703) 305-3230		

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