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(54) DISPLAY PANEL AND DISPLAY DEVICE

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ABSTRACT (57)

The present disclosure provides a display panel and a display device, which can reduce damage to a frame sealant caused by impact of a photoelectric material on the frame sealant during cell-assembling of the display. The display panel includes a first substrate, a second substrate, and a frame sealant and a photoelectric material layer between them; the photoelectric material is in a liquid and flowing status, the photoelectric material layer is located in and covers a display region of the display panel, and the frame sealant is located in a non-display region of the display panel. The display panel further includes a blocking portion provided in the non-display region and at a side of the frame sealant near the display region for blocking the photoelectric material. The blocking portion includes a channel for shunting the photoelectric material so as to block impact of the photoelectric material on the frame sealant.





Fig.1a



Fig.1b



Fig.2





Fig.4



Fig.5



Fig.6



Fig.7



Fig.8



Fig.9



Fig.10



Fig.11



Fig.12



Fig.13

DISPLAY PANEL AND DISPLAY DEVICE

RELATED APPLICATIONS

[0001] The present application is the U.S. national phase entry of PCT/CN2016/082608, with an international filing date of May 19, 2016, which claims the benefit of Chinese Patent Application No. 201620240906.5, filed on Mar. 25, 2016, the entire disclosures of which are incorporated herein by reference.

TECHNICAL FIELD

[0002] The disclosure relates to the technical field of display, and in particular to a display panel and a display device.

BACKGROUND

[0003] With the increasing maturity of display technology, various displays have been developed. For example, a liquid crystal display panel comprises a first substrate, a second substrate and a liquid crystal layer located therebetween. There are two ways of manufacturing a liquid crystal injection at present, one is suck-filling and the other is drop-filling. The suck-filling slowly sucks liquid crystals based on capillary principle after cell-assembling of the first substrate and the second substrate. This way of liquid crystal injection is very time-consuming and wastes liquid crystals, and hence liquid crystal injection by drop-filling is generally used.

SUMMARY

[0004] The object of the present disclosure is at least to provide a display panel and a display device to solve the problem of damage to a frame sealant caused by impact of a photoelectric material (e.g. liquid crystals) on the frame sealant during cell-assembling of the display.

[0005] To achieve the object, in one aspect of the present disclosure, a display panel is provided, which comprises a first substrate, a second substrate, and a frame sealant and a photoelectric material layer between them; wherein the photoelectric material layer is located in a display region of the display panel, and the frame sealant is located in a non-display region of the display panel. The display panel further comprises a blocking portion provided in the non-display region and at a side of the frame sealant near the display region for blocking the photoelectric material.

[0006] In some implementations, the blocking portion comprises a channel for shunting the photoelectric material. [0007] In some implementations, the blocking portion comprises a plurality of first blocking units.

[0008] In some implementations, the first blocking units have a higher density in four corner regions of the display panel than in other regions.

[0009] In some implementations, the first blocking units are blocking walls.

[0010] In some implementations, the blocking walls have a shape of a straight line, a polyline or a curve.

[0011] In some implementations, the first blocking units are spacer arrays.

[0012] In some implementations, the spacer arrays have a shape of a parallelogram. A pair of opposite sides of the parallelogram are parallel to a boundary of the display region of the display panel.

[0013] In some implementations, the spacer arrays have an irregular shape with two straight-line sides and two curved-line sides. The two straight-line sides are parallel to a boundary of the display region of the display panel.

[0014] In some implementations, in a direction from a boundary of the display region to the frame sealant, angles between tangent lines of the curved-line sides of the spacer array and the boundary of the display region decrease gradually.

[0015] In some implementations, width L1 of a first blocking unit at a side of a boundary of the display region along an orientation of the boundary and width L2 of a channel between the first blocking unit and an adjacent first blocking unit have a relation of $0.7L1 \le L2 \le 1.3L1$.

[0016] In some implementations, width L1 of a first blocking unit at a side of a boundary of the display region along an orientation of the boundary and width L2 of a channel between the first blocking unit and an adjacent first blocking unit have a relation of $0.9L1 \le L2 \le 1.1L1$.

[0017] In some implementations, straight-line shaped blocking walls at a side of a boundary of the display region are inclined relative to the boundary.

[0018] In some implementations, an acute angle between straight-line shaped blocking walls at a side of a boundary of the display region and the boundary is between 30° - 60° .

[0019] In some implementations, another pair of opposite sides of the spacer array arranged as a parallelogram at a side of a boundary of the display region are inclined relative to the boundary.

[0020] In some implementations, another pair of opposite sides of the spacer array arranged as a parallelogram are vertical relative to the boundary. In some implementations, an acute angle between the other pair of opposite sides of the spacer array and the boundary is between $30^{\circ}-60^{\circ}$.

[0021] In some implementations, the blocking portion further comprises a plurality of second blocking units uniformly arranged, which are provided at a side of the first blocking units far away from the display region; the second blocking units are blocking walls, and the second blocking units at a side of a boundary of the display region are parallel to the boundary.

[0022] In some implementations, the blocking portion further comprises third blocking units, and the third blocking units are spacers that are provided between the blocking walls.

[0023] In a second aspect according to the present disclosure, a display device is provided, which comprises the display panel according to the first aspect of the present disclosure.

BRIEF DESCRIPTION OF DRAWINGS

[0024] FIG. 1*a* is a schematic top view of a liquid crystal display panel according to the prior art;

[0025] FIG. 1b is a schematic sectional view of the liquid crystal display panel according to FIG. 1a in the direction AA;

[0026] FIG. **2** is a schematic sectional view of a liquid crystal display panel according to an embodiment of the present disclosure;

[0027] FIG. **3** is a schematic top view of a liquid crystal display panel according to a first embodiment of the present disclosure;

[0029] FIG. **5** is a schematic top view of a liquid crystal display panel according to a third embodiment of the present disclosure;

[0030] FIG. **6** is a schematic top view of a liquid crystal display panel according to a fourth embodiment of the present disclosure;

[0031] FIG. 7 is a schematic top view of a liquid crystal display panel according to a fifth embodiment of the present disclosure;

[0032] FIG. **8** is a schematic top view of a liquid crystal display panel according to a sixth embodiment of the present disclosure;

[0033] FIG. **9** is a schematic top view of a liquid crystal display panel according to a seventh embodiment of the present disclosure;

[0034] FIG. **10** is a schematic top view of a liquid crystal display panel according to an eighth embodiment of the present disclosure;

[0035] FIG. **11** is a schematic top view of a liquid crystal display panel according to a ninth embodiment of the present disclosure;

[0036] FIG. **12** is a schematic top view of a liquid crystal display panel according to a tenth embodiment of the present disclosure; and

[0037] FIG. **13** is a schematic top view of a liquid crystal display panel according to an eleventh embodiment of the present disclosure.

[0038] In the present disclosure, like reference numerals generally indicate like components.

DETAILED DESCRIPTION OF EMBODIMENTS

[0039] Technical solutions in embodiments of the present disclosure will be described below clearly and completely in connection with the drawings in embodiments of the present disclosure. Obviously, the described embodiments are merely some instead of all of the embodiments of the present disclosure. All other embodiments that can be obtained by those ordinary skilled in the art on the basis of the embodiments in the present disclosure without undue experiments shall fall into the protection scope of the present disclosure. [0040] Unless otherwise specified, technical terms or scientific terms used herein shall have meanings that are commonly appreciated by those skilled in the art. The words "first", "second" and the like used in the specification and claims of the present patent application do not indicate any sequence, quantity or importance, but are merely used for differentiating different components.

[0041] As shown in FIGS. 1a and 1b, drop-filling is achieved in a first substrate 10. A frame scalant 30 is provided in a vertical direction surrounding edges of a display region of a first substrate 10 in a non-display region of the first substrate 10, thereby forming walls. Although FIGS. 1a and 1b do not explicitly show the display region and non-display region of the first substrate 10, it should be realized by those skilled in the art shall that the display regions of the first substrate 10 and the display panel correspond to a display region 01 of a second substrate 20, and the non-display regions of the first substrate 10 and the display panel correspond to a non-display region 02 of the second substrate 20. Liquid crystals are drop-filled within the walls formed by the frame scalant 30, to form a liquid

crystal layer 40. Then the liquid crystal layer 40 is covered by the second substrate 20 and the frame sealant 30 is cured. When the frame sealant 30 is cured, liquid crystals have been sealed between the two substrates.

[0042] The inventor of the present disclosure realized that liquid crystal injection by drop-filling as currently used is problematic. Take FIGS. 1a and 1b as examples, in the process of drop-filling liquid crystals within the walls formed by the frame sealant 30 and then covering the liquid crystals by the second substrate 20, the liquid crystals would have impact on the frame sealant 30, and thus result in damage of the frame sealant 30, such as crack, so that the whole liquid crystal display panel become scrapped.

[0043] At least for this problem, the present disclosure provides a display panel and a display device. By providing a blocking portion at a side of the frame sealant near the display region of the substrate in the non-display region of the substrate, a fluid and flowing photoelectric material can be blocked in the process of cell-assembling, thereby reducing and even eliminating damage to the frame sealant caused by impact of the photoelectric material on the frame sealant in the process of cell-assembling, and thus solving the possible problem of scrapping the display panel. Based on this, the blocking portion may further comprise channels. The channels can shunt the photoelectric material during cell-assembling so as to scatter the impact force of the photoelectric material and to slowly release the impact force as the photoelectric material flows along the channels, and thus they can also prevent the blocking portion from being damaged.

[0044] A display panel according to an embodiment of the present disclosure is provided, as shown in FIG. 2, which comprises a first substrate 10, a second substrate 20, and a frame sealant 30 and a photoelectric material layer 40 between them. A photoelectric material in the photoelectric material layer 40 is in a liquid and flowing status. The photoelectric material layer 40 is located in a display region 01 of the display panel (which corresponds to display region 01 of the first substrate 10 and the second substrate 20), and the frame sealant 30 is located in a non-display region 02 of the display panel (which corresponds to non-display regions 02 of the first substrate 10 and the second substrate 20). In some embodiments, the display region 01 of the display panel is a rectangle.

[0045] Further, the display panel also comprises: a blocking portion **50** provided in the non-display region **02** and at a side of the frame sealant **30** near the display region **01** for blocking the photoelectric material. In some embodiments, the blocking portion **50** may comprise channels (not shown in FIG. **2**) for shunting the photoelectric material. In some embodiments, the channels may be holes of various shapes in the blocking portion **50**. In some other embodiments, the channels may be gaps between independent first blocking units as described below.

[0046] It shall be noted that first, according to the present disclosure, types of the first substrate **10** and the second substrate **20** are not limited, and the first substrate **10** can be an array substrate and the second substrate **20** can be a cell-assembling substrate. In some embodiments, a color film layer may be provided on the cell-assembling substrate, and then the cell-assembling substrate is called a color film substrate. Of course, the color film layer can also be provided on the array substrate.

[0047] Of course, in some embodiments, the second substrate **20** can be an array substrate while the first substrate **10** is a cell-assembling substrate, which is not set forth in detail any more.

[0048] The frame sealant 30 and the blocking portion 50 can both be formed on the first substrate 10 or the second substrate 20, or they can be formed on the first substrate 10 and the second substrate 20, respectively.

[0049] Second, the blocking portion 50 is formed between the first substrate 10 and the second substrate 20 for blocking the photoelectric material so as to prevent the frame sealant 30 from being damaged by the impact force of the photoelectric material during cell-assembling. In some embodiments, the blocking portion 50 thus has the same height as the frame sealant 30 so as to better reduce damage to the frame sealant 30 by the impact force. In some embodiments, the height of the blocking portion 50 may be smaller than the height of the frame sealant 30.

[0050] According to the present disclosure, widths of the channels in the blocking portion **50** are not limited, as long as they can enable shunting of the photoelectric material.

[0051] In addition, the material and forming way of the blocking portion **50** are not limited. For example, it may be formed by lithography and etching processes or by any other processes. Moreover, the blocking portion **50** can be made of any material suitable for blocking.

[0052] Third, according to the present disclosure, arrangement of the blocking portion **50** is not limited. It can be arranged in any way in the non-display region **02** of the display panel as long as it can block the photoelectric material.

[0053] Fourth, according to the present disclosure, no limitation is made to the photoelectric material. For example, it may be a liquid crystal or any other kinds of photoelectric materials.

[0054] In the display panel according to the present disclosure, by provided the blocking portion 50 at a side of the frame sealant 30 near the display region 01 and in the non-display region 02, the liquid and flowing photoelectric material can be blocked in the process of cell-assembling, thereby reducing damage to the frame sealant 30 caused by impact of the photoelectric material on the frame sealant 30 during cell-assembling, and thus solving the possible problem of scrapping the display panel. Based on this, the blocking portion 50 may further comprise channels 502 as shown in FIGS. 3-11, for example. The channels can shunt the photoelectric material during cell-assembling so as to scatter the impact force of the photoelectric material and to slowly release the impact force as the photoelectric material flows along the channels, and thus they can also prevent the blocking portion 50 from being damaged.

[0055] Alternatively, as shown in FIGS. 3-13, the blocking portion 50 comprises a plurality of first blocking units 501. [0056] The first blocking units 501 herein are independent entities, wherein gaps between adjacent first blocking units 501 form the above-mentioned channels 502.

[0057] According to the present disclosure, the number of the first blocking units 501 is not limited, but it can be set reasonably in the non-display region 02 according to a distance from the boundary of the display region 01 to the frame sealant 30 and sizes of channels 502 between the first blocking units 501.

[0058] In an embodiment of the present disclosure, the first blocking units 501 are arranged in the non-display

region 02, and it can block and shunt the impact force of the photoelectric material during cell-assembling, so as to reduce the impact force produced by the photoelectric material to the frame sealant 30 (which passes through the blocking portion 50 and reaches the frame sealant 30), thereby avoiding damage to the frame sealant 30.

[0059] Alternatively, the first blocking units **501** have a higher density in the four corner regions of the display panel than in other regions.

[0060] In the process of cell-assembling, the four corner regions of the display panel are subjected to most impact forces from the photoelectric material in various directions. Therefore, in order to avoid damage to the frame sealant **30** in the four corner regions of the display panel by the photoelectric material, the first blocking units **501** arranged in the four corner regions. In other regions than the four corner regions, the first blocking units **501** can be uniformly arranged at certain intervals.

[0061] In an embodiment of the present disclosure, the first blocking units 501 have a higher density in the four corner regions of the display panel than in other regions. In the four corner regions of the display panel, higher density of the first blocking units 501 can result in more effective blocking and shunting of the impact force from the photoelectric material, thereby avoiding damage to the frame sealant 30 in the four corner regions of the display panel caused by the photoelectric material.

[0062] Alternatively, as shown in FIGS. 3-7, FIG. 10 and FIGS. 12-13, the first blocking units 501 are blocking walls.

[0063] According to the present disclosure, no limitation is made to the shape and arrangement of the blocking walls 501, as long as the blocking walls 501 can block and shunt the photoelectric material.

[0064] Further, the blocking walls **501** have a shape of a straight line, a polyline or a curve.

[0065] Specifically, as shown in FIGS. 3-4, FIG. 10 and FIG. 12, the blocking walls 501 may have a shape of a straight line. In some embodiments, the boundary of the display region 01 is a rectangle, and the blocking walls 501 at a side of a boundary of the display region 01 may be inclined relative to the boundary as shown in FIGS. 3, 10 and 12. In some embodiments, the blocking walls 501 at a side of a boundary of the display region 01 may be vertical relative to the boundary as shown in FIG. 4.

[0066] If the blocking walls 501 at a side of a boundary of the display region 01 are vertical relative to the boundary, the number of the blocking walls 501 may be set to be larger, as compared to the case where they are inclined. This is because, if the blocking walls 501 are inclined, the photoelectric material may flow obliquely, which can reduce the impact force to the frame sealant 30, and if the blocking walls 501 are vertical, the photoelectric material has a short path of flowing, and thus compared to the case where the blocking walls 501 are inclined, the frame sealant 30 is subjected to more impact force from the photoelectric material. In this case, by providing more blocking walls 501, more shunt can be made to the photoelectric material so as to reduce the impact force to the frame sealant 30.

[0067] Based on this, alternatively, according to an embodiment of the present disclosure, the straight-line shaped blocking walls **501** at a side of a boundary of the display region **01** are inclined relative to the boundary.

[0068] As shown in FIGS. 5-6 and 13, the blocking walls 501 can also have a polyline shape. Or as shown in FIG. 7, the blocking walls 501 can have a curve shape.

[0069] According to the present disclosure, the shape of the polyline or the curve is not limited, and thus it can be any polyline or curve.

[0070] In some embodiments, as shown in FIGS. **8**, **9** and **11**, the first blocking unit **501** can be a spacer array. As shown in FIGS. **8** and **11**, the spacer array **501** may have a shape of a parallelogram. A pair of opposite sides of the parallelogram are parallel to a boundary of the display region **01**. In some embodiments, the spacer array **501** may be arranged in any other form.

[0071] According to the present disclosure, the material of the spacers is not limited in the embodiments of the present disclosure, and it can be a complex mixture. The material of the blocking walls may be the same as the material of the spacers.

[0072] In some embodiments, another pair of opposite sides of the spacer array 501 can be vertical to the boundary of the display region 01. In some other embodiments, the spacer array 501 can also be inclined relative to the boundary of the display region 01, as shown in FIGS. 8, 9 and 11. In an embodiment of the present disclosure, alternatively, the other pair of opposite sides of the spacer array 501 are inclined relative to the boundary of the display region 01. [0073] The spacer array 501 is arranged in a shape of a parallelogram wherein one pair of opposite sides of the parallelogram are parallel to a boundary of the display region 01 and the other pair of opposite sides are inclined relative to the boundary of the display region 01. In this case, in the process of cell-assembling, the photoelectric material flows in a direction (shown by the arrows in FIG. 8) inclined relative to the boundary of the display region 01, and more inclined flowing of the photoelectric material is formed between spacer arrays 501 while less inclined flowing of photoelectric material is formed within the spacer array 501. The inclined flowing photoelectric material has smaller impact on the frame sealant 30. Moreover, after passing through the spacer arrays 501, the photoelectrical material changes to flow along orientation of the frame sealant 30 to form a slow circulation around the frame sealant 30 until coming to stop gradually.

[0074] Alternatively, as shown in FIG. 9, the spacer array 501 has an irregular shape with two straight-line sides and two curved-line sides. The two straight-line sides are parallel to a boundary of the display region 01.

[0075] According to the present disclosure, the bending degree and bending direction of the two curved-line sides are not limited, as long as they can effectively block and shunt the photoelectric material.

[0076] When the spacer array **501** is arranged in a shape having two straight-line sides and two curved-line sides, in the process of cell-assembling, the photoelectric material flows along the orientation of the curved-line sides (as shown by the arrows in FIG. 9), which can enable transition of the flowing direction of the photoelectric material to a direction parallel to the frame sealant **30** at a smoother angle, thereby further reducing impact on the frame sealant **30** and consuming the impact force of the photoelectric material in the circulation.

[0077] Alternatively, along a direction from the boundary of the display region 01 to the frame sealant 30, acute angles formed by tangent lines of the curved-line side of the spacer

array 501 and the boundary of the display region 01 decreases gradually, as shown in FIG. 9.

[0078] Larger degree of bending of the curved-line sides of the spacer array 501 means more reduction of the impact force of the photoelectric material. Moreover, the closer to the boundary of the display region 01, the stronger the impact force of the photoelectric material. As the photoelectric material gradually flows from the boundary of the display region 01 to the frame sealant 30, the impact force of the photoelectric material will gradually decrease. In order to prevent the spacer array 501 from being damaged by the strong impact force of the photoelectric material close to the boundary of the display region 01, a portion of the curved-line side of the spacer array 501 near the boundary of the display region 01 may have a small degree of bending, i.e., an angle formed by a tangent line of the curved-line side of the spacer array 501 and the boundary of the display region 01 is large. Since the impact force of the photoelectric material decreases gradually along a direction from the boundary of the display region 01 to the frame sealant 30, the effect of blocking of the spacer array 501 to the impact force of the photoelectric material can be gradually enhanced. Accordingly, angles formed by tangent lines of the curved-line side of the spacer array 501 and the boundary of the display region 01 can be set to decrease gradually.

[0079] It shall be noted that in the case where the blocking walls or another pair of opposite sides of the spacer array arranged in a parallelogram are inclined to the boundary of the display region, the present disclosure does not limit the angle of inclination, and it can be any angle as long as the photoelectric material can obliquely flow and the impact force to the frame sealant **30** can be reduced.

[0080] If a first blocking unit **501** is a spacer array, alternatively, as shown in FIG. **8**, width L1 of the first blocking unit **501** at a side of a boundary of the display region **01** along an orientation of the boundary and width L2 of a channel (a gap) between the first blocking unit **501** and an adjacent first blocking unit **501** have a relation of $0.7L1 \le L2 \le 1.3L1$.

[0081] The width L1 of the first blocking unit 501 and the width L2 of the channel between the first blocking unit and an adjacent first blocking unit 501 have a relation of 0.7L1≤L2≤1.3L1 herein. As such, in the process of cellassembling, a relative balance can be achieved between the impact of the photoelectric material to the first blocking unit 501 and the shunting effect. The impact of the photoelectric material can be effectively blocked without damaging the first blocking unit 501, and meanwhile, the photoelectric material can be shunted, thereby reducing the impact force from the photoelectric material to the frame sealant 30. When a relation between the width L1 of the first blocking unit 501 and the width L2 of the channel between the first blocking unit 501 and the adjacent first blocking unit 501 meets 0.9L1≤L2≤1.1L1, blocking and shunting of the photoelectric material by the first blocking unit are perfectly balanced.

[0082] When the straight-line shaped blocking walls at a side of a boundary of the display region **01** or another pair of opposite sides of the spacer array arranged as a parallelogram are inclined relative to the boundary, further alternatively, as shown in FIGS. **3** and **8**, acute angle θ between the first blocking units **501** at a side of a boundary of the display region **01** and the boundary may be between 30°-60°.

[0083] When the angle θ is set between 30°-60°, the impact of the photoelectric material on the first blocking units **501** and the shunting effect are relatively balanced, that is, blocking of the photoelectric material by the first blocking units **501** will not cause the first blocking units **501** themselves to be damaged, and the photoelectric material can be shunted well at the same time, thus facilitating blocking the impact force from the photoelectric material to the frame sealant **30**. When the angle θ is set between 45°-50°, blocking units **501** can be perfectly balanced, and thus it is more beneficial to block the impact force from the photoelectric material by the first blocking units **501** can be perfectly balanced, and thus it is more beneficial to block the impact force from the photoelectric material **30**.

[0084] When the straight-line shaped blocking walls at a side of a boundary of the display region **01** or another pair of opposite sides of the spacer array arranged as a parallelogram are inclined relative to the boundary, alternatively, as shown in FIGS. **10** and **11**, the blocking portion **50** further comprises a plurality of second blocking units **503** uniformly arranged, which are disposed at a side of the first blocking units **501** far away from the display region **01**. As shown in FIGS. **10** and **11**, the second blocking units **503** are blocking walls, and the second blocking units **503** at a side of a boundary of the display region **01** are parallel to the boundary.

[0085] According to the present disclosure, lengths of the second blocking units **503** and spacing between adjacent second blocking units **503** are not limited, but they can be set according to the type and arrangement of the first blocking units **501** as long as the photoelectric material can be further blocked.

[0086] In an embodiment of the present disclosure, the second blocking units **503** at a side of a boundary of the display region **01** are arranged parallel to the boundary. Thus after the photoelectric material flows through the first blocking units **501**, the second blocking units **503** further block the photoelectric material flowing to the frame sealant **30**, thereby reducing impact on the frame sealant **30** more effectively.

[0087] When the first blocking units 501 are blocking walls, alternatively, as shown in FIGS. 12 and 13, the blocking portion 50 further comprises third blocking units 504. The third blocking units 504 are spacers and are arranged between the blocking walls 501.

[0088] According to the present disclosure, the number of the third blocking units **504** is not limited as long as they are evenly arranged between the blocking walls.

[0089] It shall be noted that the blocking walls are not limited herein, and they can be any of the above-mentioned blocking walls, and the blocking walls are not limited to the straight-line shaped blocking walls **501** as shown in FIG. **12** or the polyline-shaped blocking walls **501** as shown in FIG. **13**.

[0090] In the embodiments of the present disclosure, the spacers 504 and the blocking walls 501 are arranged in the same region of the non-display region 02. Compared to separately arranging the spacers 504 and the blocking walls 501 in the non-display region 02, this can reduce a space occupied on the non-display region 02, and thus realize side narrowing of the display panel of a photoelectric material.

[0091] The blocking walls in the embodiments of the present disclosure may also be set as other shapes, and any open design that can meet reducing of the flow speed of the

photoelectric material or extending of the flow path of the photoelectric material shall fall into the protection scope of the present disclosure.

[0092] An embodiment of the present disclosure further provides a display device comprising the above-mentioned display panel.

[0093] The display device can specifically be any product or component having a display function, such as a liquid crystal display, an organic electroluminescent diode display, a cell phone and a tablet computer.

[0094] The above described are only specific embodiments of the present disclosure, and the protection scope of the present disclosure is not limited so. Any variation or substitution that is easily conceivable by those skilled in the art within the technical scope disclosed by the present disclosure shall fall into the protection scope of the present disclosure. Thus the protection scope of the present disclosure is defined by the protection scope of the appended claims.

LIST OF REFERENCE NUMERALS

- [0095] 01 display region
- [0096] 02 non-display region
- [0097] 10 first substrate
- [0098] 20 second substrate
- [0099] 30 frame sealant
- [0100] 40 liquid crystal layer
- [0101] 50 blocking portion
- [0102] 501 first blocking unit
- [0103] 502 channel
- [0104] 503 second blocking unit
- [0105] 504 third blocking unit

1. A display panel comprising: a first substrate, a second substrate, and a frame sealant and a photoelectric material layer between them; wherein the photoelectric material is in a liquid and flowing status, the photoelectric material layer is located in and covers a display region of the display panel, and the frame sealant is located in a non-display region of the display panel;

wherein the display panel further comprises a blocking portion provided in the non-display region and at a side of the frame sealant near the display region for blocking the photoelectric material.

2. The display panel according to claim 1, wherein the blocking portion comprises a channel for shunting the photoelectric material.

3. The display panel according to claim **2**, wherein the blocking portion comprises a plurality of independent first blocking units.

4. The display panel according to claim **3**, wherein the first blocking units have a higher density in four corner regions of the display panel than in other regions.

5. The display panel according to claim 3, wherein the first blocking units are blocking walls.

6. The display panel according to claim **5**, wherein the blocking walls have a shape of a straight line, a polyline or a curve.

7. The display panel according to claim 3, wherein the first blocking units are spacer arrays.

8. The display panel according to claim **7**, wherein the spacer arrays have a shape of a parallelogram; and wherein a pair of opposite sides of the parallelogram are parallel to a boundary of the display region of the display panel.

9. The display panel according to claim **7**, wherein the spacer arrays have an irregular shape with two straight-line sides and two curved-line sides; and wherein the two straight-line sides are parallel to a boundary of the display region of the display panel.

10. The display panel according to claim **9**, wherein in a direction from the boundary of the display region to the frame sealant, angles between tangent lines of the curved-line sides of the spacer array and the boundary of the display region decrease gradually.

11. The display panel according to claim 7, wherein width L1 of a first blocking unit at a side of a boundary of the display region along an orientation of the boundary and width L2 of a channel between the first blocking unit and an adjacent first blocking unit have a relation of $0.7L1 \le L2 \le 1$. 3L1.

12. The display panel according to claim 7, wherein width L1 of a first blocking unit at a side of a boundary of the display region along an orientation of the boundary and width L2 of a channel between the first blocking unit and an adjacent first blocking unit have a relation of $0.9L \le L2 \le 1$. 1L1.

13. The display panel according to claim **5**, wherein straight-line shaped blocking walls at a side of a boundary of the display region are inclined relative to the boundary.

14. The display panel according to claim 13, wherein an acute angle between the straight-line shaped blocking walls

at the side of the boundary of the display region and the boundary is between 30° - 60° .

15. The display panel according to claim **8**, wherein another pair of opposite sides of the spacer array arranged as a parallelogram at a side of a boundary of the display region are inclined relative to the boundary.

16. The display panel according to claim **15**, wherein the other pair of the opposite sides of the spacer array arranged as a parallelogram are vertical relative to the boundary.

17. The display panel according to claim 15, wherein an acute angle between the other pair of the opposite sides of the spacer array and the boundary is between 30° - 60° .

18. The display panel according to claim **3**, wherein the blocking portion further comprises a plurality of second blocking units uniformly arranged, which are provided at a side of the first blocking units far away from the display region; and

the second blocking units are blocking walls, and the second blocking units at the side of a boundary of the display region are parallel to the boundary.

19. The display panel according to claim **5**, wherein the blocking portion further comprises third blocking units; and

the third blocking units are spacers that are provided between the blocking walls.

20. A display device comprising the display panel according to claim **11**.

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