



US 20190333858A1

(19) **United States**

(12) **Patent Application Publication**

KIM et al.

(10) **Pub. No.: US 2019/0333858 A1**

(43) **Pub. Date: Oct. 31, 2019**

(54) **DIELECTRIC LAYER, SEMICONDUCTOR MEMORY DEVICE INCLUDING THE DIELECTRIC LAYER, AND METHODS OF MANUFACTURING THE DIELECTRIC LAYER AND THE SEMICONDUCTOR MEMORY DEVICE**

(21) Appl. No.: 16/225,432

(22) Filed: Dec. 19, 2018

(30) **Foreign Application Priority Data**

Apr. 25, 2018 (KR) 10-2018-0047915

(71) Applicants: **KOREA INSTITUTE OF SCIENCE AND TECHNOLOGY**, Seoul (KR); **INSTITUTE FOR BASIC SCIENCE**, Daejeon (KR); **ULSAN NATIONAL INSTITUTE OF SCIENCE AND TECHNOLOGY**, Ulsan (KR)

Publication Classification

(72) Inventors: **Seong Keun KIM**, Seoul (KR); **Woo Chul LEE**, Seoul (KR); **Sang Tae KIM**, Seoul (KR); **Hyun Cheol SONG**, Seoul (KR); **Seung Hyub BAEK**, Seoul (KR); **Ji Won CHOI**, Seoul (KR); **Jin Sang KIM**, Seoul (KR); **Chong Yun KANG**, Seoul (KR); **Christopher W. BIELAWSKI**, Ulsan (KR); **Jung Hwan YUM**, Ulsan (KR); **Eric S. LARSEN**, Ulsan (KR)

(51) **Int. Cl.**
H01L 23/532 (2006.01)
H01L 49/02 (2006.01)
H01L 27/108 (2006.01)
(52) **U.S. Cl.**
CPC *H01L 23/5329* (2013.01); *H01L 27/10844* (2013.01); *H01L 28/60* (2013.01)

(57) **ABSTRACT**

(73) Assignees: **KOREA INSTITUTE OF SCIENCE AND TECHNOLOGY**, Seoul (KR); **INSTITUTE FOR BASIC SCIENCE**, Daejeon (KR); **ULSAN NATIONAL INSTITUTE OF SCIENCE AND TECHNOLOGY**, Ulsan (KR)

Provided is a dielectric layer that has a rock salt structure in a room temperature stable phase. The dielectric layer is made of a compound having a chemical formula of $Be_xM_{1-x}O$, where M includes one of alkaline earth metals and x has a value greater than 0 and less than 0.5.

1

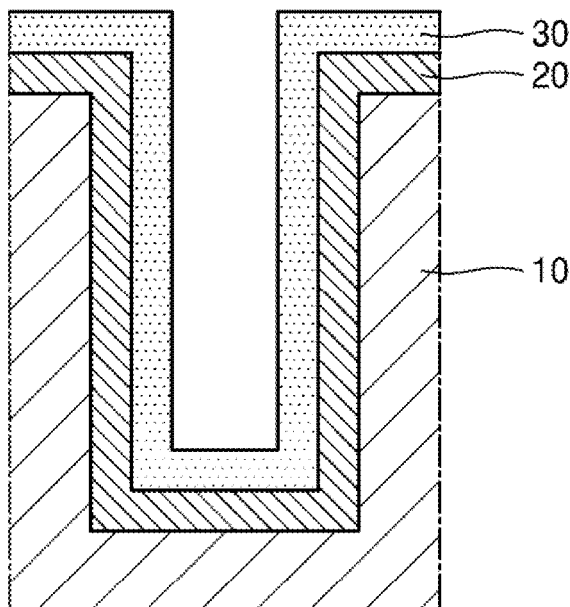


FIG. 1

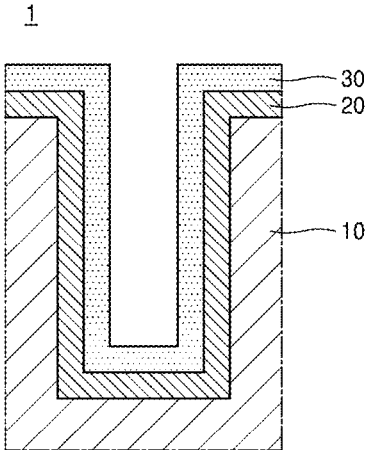


FIG. 2

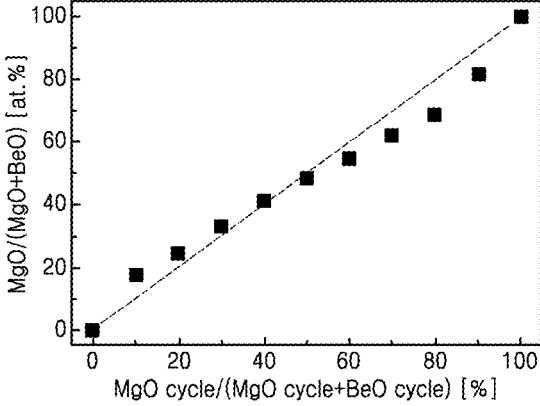


FIG. 3

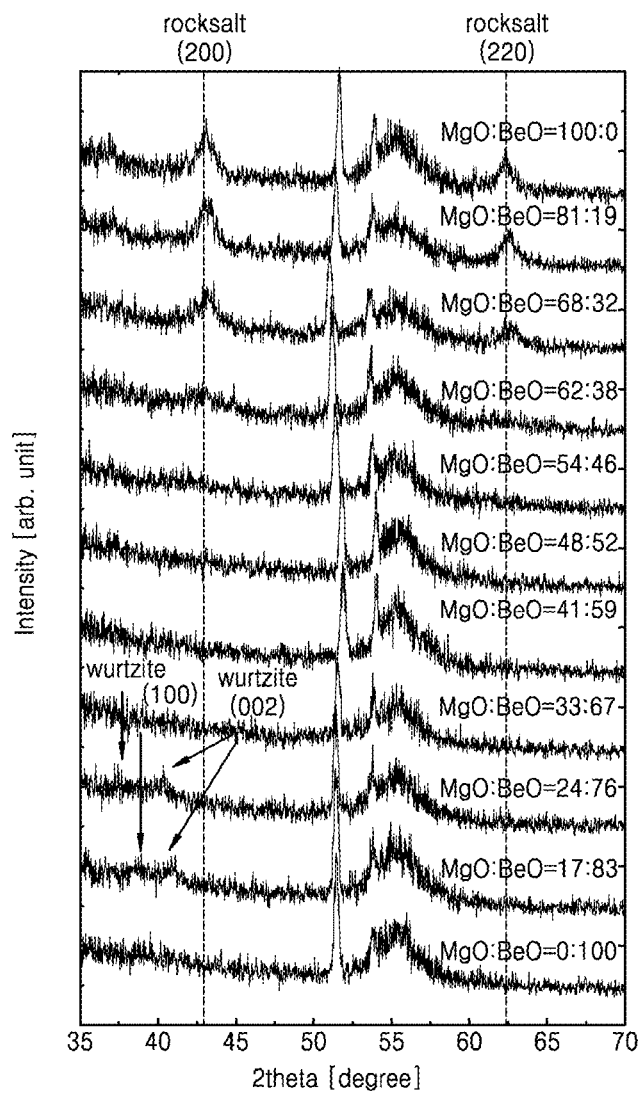


FIG. 4

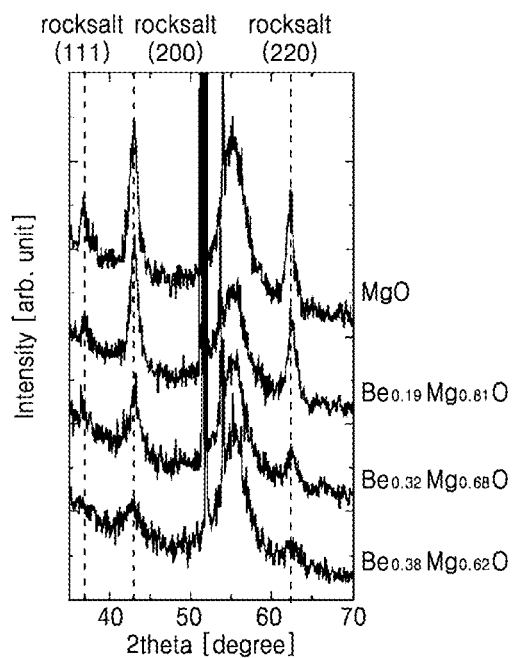


FIG. 5

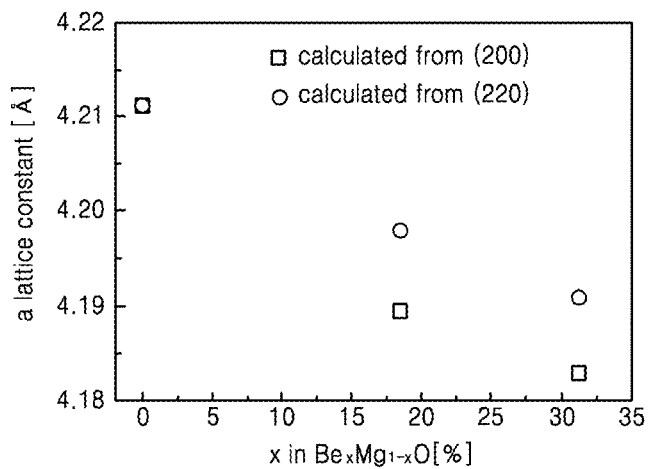


FIG. 6

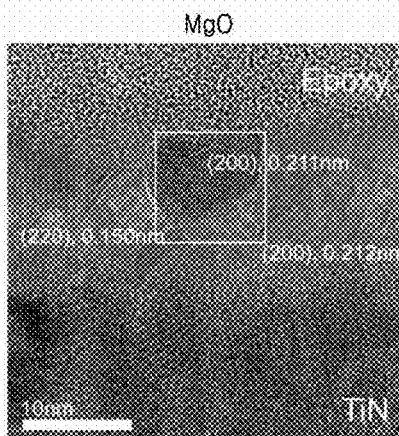


FIG. 7

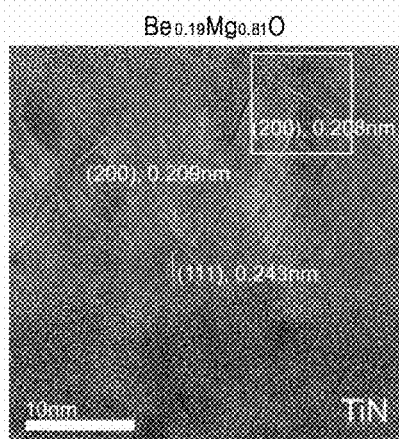


FIG. 8

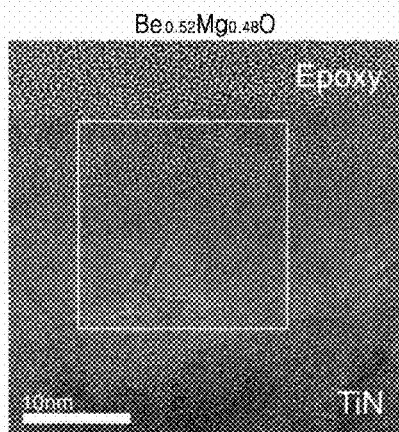


FIG. 9

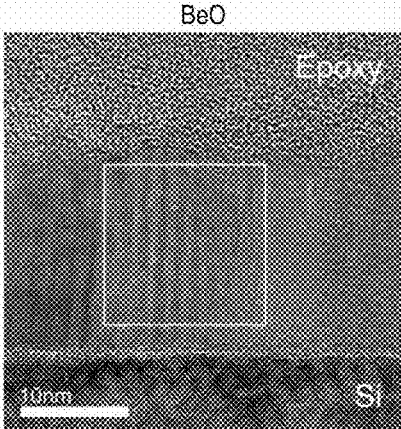


FIG. 10

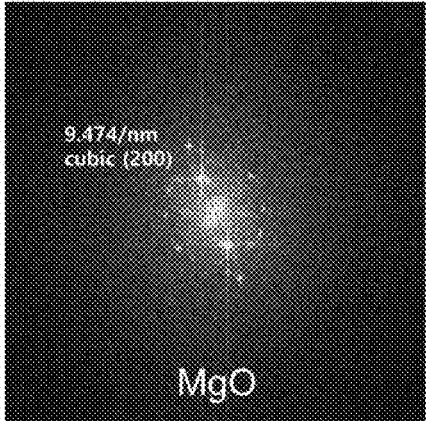


FIG. 11

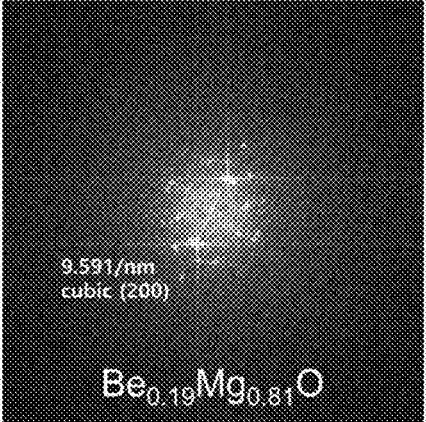


FIG. 12

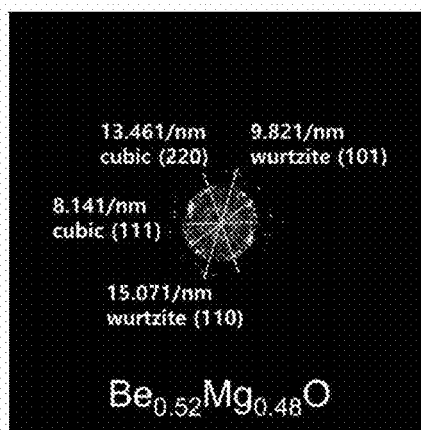


FIG. 13

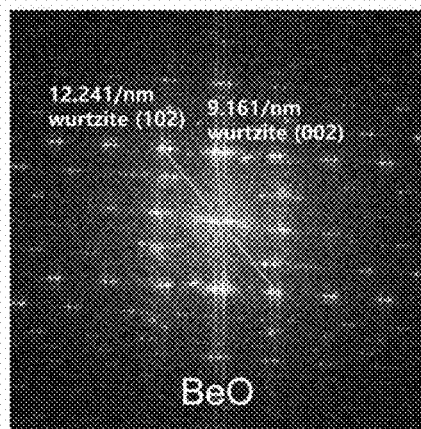


FIG. 14

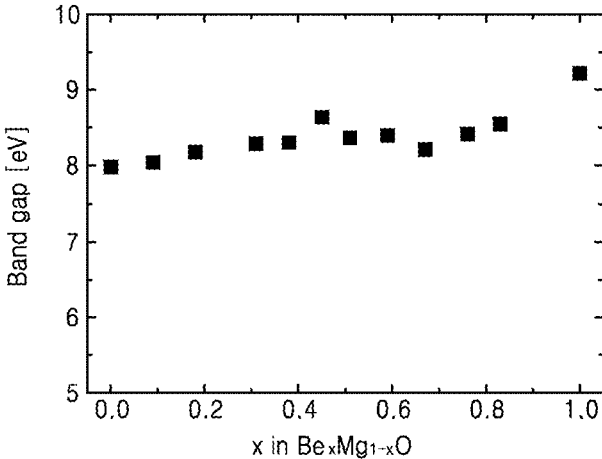
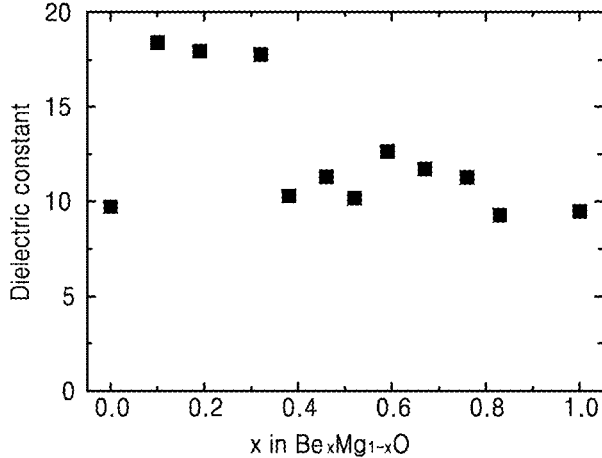


FIG. 15



**DIELECTRIC LAYER, SEMICONDUCTOR
MEMORY DEVICE INCLUDING THE
DIELECTRIC LAYER, AND METHODS OF
MANUFACTURING THE DIELECTRIC
LAYER AND THE SEMICONDUCTOR
MEMORY DEVICE**

CROSS-REFERENCE TO RELATED PATENT
APPLICATION

[0001] This application claims the benefit of Korean Patent Application No. 10-2018-0047915, filed on Apr. 25, 2018, in the Korean Intellectual Property Office, the disclosure of which is incorporated herein in its entirety by reference.

BACKGROUND

1. Field

[0002] The present invention relates to a dielectric layer, a semiconductor memory device including the dielectric layer, and methods of manufacturing the dielectric layer and the semiconductor memory device and, more particularly, to a dielectric layer having a high dielectric constant and a large band gap, a semiconductor memory device including the dielectric layer, and methods of manufacturing the dielectric layer and the semiconductor memory device.

2. Description of the Related Art

[0003] Currently, integration densities of semiconductor devices exponentially increase along with rapid development of semiconductor industries. Ultra large-scale integration (ULSI) dynamic random-access memory (DRAM) devices with a design rule of a 20-nm level or less are being developed. Development of ULSI DRAM devices with a design rule of a 10-nm level less than the 20-nm level depends on a capacitor having a high capacitance and a low leakage current. In a DRAM device, a capacitor that records data requires at least a certain capacitance regardless of a device size. However, the capacitor is reduced in size to increase an integration density of the device, and the capacitance value proportional to an area is unavoidably reduced due to the reduced capacitor size. As such, to solve this problem, use of a dielectric having a high dielectric constant is required. In addition to a high dielectric constant, the dielectric of the capacitor that stores charges needs to ensure a low leakage current to prevent loss of charges.

[0004] Therefore, a dielectric layer having a high dielectric constant to increase an integration density of a DRAM device has been actively developed. For example, silicon oxide (SiO₂) or aluminum oxide (Al₂O₃) having a dielectric constant equal to or lower than 10 is used in early days, and zirconium oxide (ZrO₂) having a dielectric constant of about 40 is currently used. To achieve a higher capacitance, a layer material such as titanium oxide (TiO₂) (dielectric constant of about 80) or strontium titanate (SrTiO₃) (dielectric constant > 100) having a rutile structure is being developed.

[0005] A critical factor for determining a leakage current of a dielectric is a band gap of the dielectric layer. A material having a large band gap may generate a high-energy barrier at an interface between an electrode and the dielectric and thus may ensure a low leakage current. However, since a dielectric constant is generally inversely proportional to a band gap, a material having a high dielectric constant has a

small band gap. For example, SiO₂ having a dielectric constant of 3.9 has a large band gap of about 9 eV and TiO₂ or SrTiO₃ having a dielectric constant equal to or higher than 80 has a small band gap of about 3 eV. Therefore, when a material having a high dielectric constant is used, an increase in a layer thickness is unavoidable to reduce a leakage current.

[0006] As a design rule of 20 nm or less is applied to DRAM devices, a limitation of a layer thickness causes a problem. A rapid reduction in the size of the DRAM devices to 20 nm or less leads to a limitation of a space for a dielectric layer and upper and lower electrodes. Particularly, in a three-dimensional (3D) capacitor having a stack structure, a dielectric layer is limited to a very small thickness of about 5 nm. Such a limitation means that existing methods using a material having a higher dielectric constant are no longer valid and that a material having a large band gap and a low leakage current at a small thickness is required. As such, a high-k material requiring a thickness equal to or greater than 10 nm, e.g., SrTiO₃, is no longer usable and a new material capable of ensuring a low leakage current at a thickness of about 5 nm is required.

RELATED ART DOCUMENT

Patent Document

[0007] (Patent Document 1) KR19990080412A (1999-11-05)

SUMMARY

[0008] The present invention provides a dielectric layer capable of ensuring low leakage current characteristics of a capacitor and of increasing a dielectric constant, a semiconductor memory device including the dielectric layer, and methods of manufacturing the dielectric layer and the semiconductor memory device. However, the scope of the present invention is not limited thereto.

[0009] According to an aspect of the present invention, there is provided a dielectric layer having a rocksalt structure in a room temperature stable phase and made of a compound having a chemical formula of Be_xM_{1-x}O, where M includes one of alkaline earth metals and x has a value greater than 0 and less than 0.5.

[0010] In the chemical formula, M may include alkaline earth metal, oxide of which has a rocksalt structure in a room temperature stable phase, e.g., one of magnesium (Mg), calcium (Ca), strontium (Sr), and barium (Ba).

[0011] The dielectric layer may include a compound of beryllium oxide (BeO) and alkaline earth metal oxide forming a rocksalt solid solution without phase separation therebetween.

[0012] In the chemical formula, x has a value greater than 0 and equal to or less than 0.38, and more specifically, a value equal to or greater than 0.1 and equal to or less than 0.32.

[0013] The dielectric layer may be used as a capacitor dielectric layer of a semiconductor memory device.

[0014] According to another aspect of the present invention, there is provided a method of manufacturing a dielectric layer, the method including depositing a stack structure of beryllium oxide (BeO) and alkaline earth metal oxide by performing an atomic layer deposition (ALD) process, and obtaining a dielectric layer by performing heat treatment on

the stack structure, wherein the dielectric layer has a rocksalt structure in a room temperature stable phase and is made of a compound having a chemical formula of $\text{Be}_x\text{M}_{1-x}\text{O}$, where M includes one of alkaline earth metals and x has a value greater than 0 and less than 0.5.

[0015] The ALD process may be performed in a temperature range from room temperature to 350° C., and the heat treatment may be performed in a temperature range from 300° C. to 600° C.

[0016] In the chemical formula, M may include alkaline earth metal, oxide of which has a rocksalt structure in a room temperature stable phase, e.g., one of magnesium (Mg), calcium (Ca), strontium (Sr), and barium (Ba).

[0017] According to another aspect of the present invention, there is provided a semiconductor memory device including a capacitor including a lower electrode, a dielectric layer on the lower electrode, and an upper electrode on the dielectric layer, wherein the dielectric layer has a rocksalt structure in a room temperature stable phase and is made of a compound having a chemical formula of $\text{Be}_x\text{M}_{1-x}\text{O}$, where M includes one of alkaline earth metals and x has a value greater than 0 and less than 0.5.

[0018] In the chemical formula, M may include alkaline earth metal, oxide of which has a rocksalt structure in a room temperature stable phase, e.g., one of magnesium (Mg), calcium (Ca), strontium (Sr), and barium (Ba).

[0019] The dielectric layer may include a compound of beryllium oxide (BeO) and alkaline earth metal oxide forming a rocksalt solid solution without phase separation therebetween.

[0020] In the chemical formula, x has a value greater than 0 and equal to or less than 0.38, and more specifically, a value equal to or greater than 0.1 and equal to or less than 0.32.

[0021] The lower electrode may include an electrode made of one of titanium nitride (TiN), tantalum nitride (TaN), ruthenium (Ru), platinum (Pt), gold (Au), silver (Ag), aluminum (Al), rhodium (Rh), molybdenum (Mo), palladium (Pd), cobalt (Co), copper (Cu), iridium (Ir), tin oxide (SnO_2), ruthenium oxide (RuO_2), iridium oxide (IrO_2), molybdenum oxide (MoO_2), and strontium ruthenate (SrRuO_3).

[0022] According to another aspect of the present invention, there is provided a method of manufacturing a semiconductor memory device, the method including generating a lower electrode, depositing a stack structure of beryllium oxide (BeO) and alkaline earth metal oxide on the lower electrode by performing an atomic layer deposition (ALD) process, generating an upper electrode on the stack structure, and obtaining a dielectric layer by performing heat treatment on the stack structure, wherein the dielectric layer has a rocksalt structure in a room temperature stable phase and is made of a compound having a chemical formula of $\text{Be}_x\text{M}_{1-x}\text{O}$, where M includes one of alkaline earth metals and x has a value greater than 0 and less than 0.5.

[0023] The ALD process may be performed in a temperature range from room temperature to 350° C., and the heat treatment may be performed in a temperature range from 300° C. to 600° C.

[0024] In the chemical formula, M may include alkaline earth metal, oxide of which has a rocksalt structure in a room temperature stable phase, e.g., one of magnesium (Mg), calcium (Ca), strontium (Sr), and barium (Ba).

BRIEF DESCRIPTION OF THE DRAWINGS

[0025] The above and other features and advantages of the present invention will become more apparent by describing in detail embodiments thereof with reference to the attached drawings in which:

[0026] FIG. 1 is a cross-sectional view of a capacitor including a dielectric layer, according to an embodiment of the present invention;

[0027] FIG. 2 is a graph showing composition ratios of a $\text{Be}_x\text{Mg}_{1-x}\text{O}$ layer based on a magnesium oxide (MgO)-beryllium oxide (BeO) cycle ratio in an atomic layer deposition (ALD) process of a method of manufacturing a dielectric layer, according to test examples of the present invention;

[0028] FIG. 3 is a graph showing x-ray diffraction patterns based on variations in a composition ratio of a $\text{Be}_x\text{Mg}_{1-x}\text{O}$ layer used as a dielectric layer, according to test examples of the present invention;

[0029] FIG. 4 is a graph showing x-ray diffraction patterns based on variations in a composition ratio (x) of BeO in a $\text{Be}_x\text{Mg}_{1-x}\text{O}$ layer used as a dielectric layer, from 0 at. % to 38 at. %, according to test examples of the present invention;

[0030] FIG. 5 is a graph showing variations in a lattice constant of a rocksalt structure based on a composition ratio of a $\text{Be}_x\text{Mg}_{1-x}\text{O}$ layer used as a dielectric layer, according to test examples of the present invention;

[0031] FIGS. 6 to 9 are high-resolution transmission electron microscope (HRTEM) images of MgO, $\text{Be}_{0.19}\text{Mg}_{0.81}\text{O}$, $\text{Be}_{0.52}\text{Mg}_{0.48}\text{O}$, and BeO layers generated by performing an ALD process of a method of manufacturing a dielectric layer, according to test examples of the present invention;

[0032] FIGS. 10 to 13 are selected area diffraction pattern (SADP) images of rectangular regions of the HRTEM images of the MgO, $\text{Be}_{0.19}\text{Mg}_{0.81}\text{O}$, $\text{Be}_{0.52}\text{Mg}_{0.48}\text{O}$, and BeO layers of FIGS. 6 to 9;

[0033] FIG. 14 is a graph showing variations in a band gap based on a composition ratio of a $\text{Be}_x\text{Mg}_{1-x}\text{O}$ layer generated by performing an ALD process of a method of manufacturing a dielectric layer, according to test examples of the present invention; and

[0034] FIG. 15 is a graph showing variations in a dielectric constant based on a composition ratio of a $\text{Be}_x\text{Mg}_{1-x}\text{O}$ layer generated by performing an ALD process of a method of manufacturing a dielectric layer, according to test examples of the present invention.

DETAILED DESCRIPTION

[0035] Hereinafter, the present invention will be described in detail by explaining embodiments of the invention with reference to the attached drawings. The invention may, however, be embodied in many different forms and should not be construed as being limited to the embodiments set forth herein; rather, these embodiments are provided so that this disclosure will be thorough and complete, and will fully convey the concept of the invention to one of ordinary skill in the art. In the drawings, the sizes of at least some elements are exaggerated or reduced for convenience of explanation, and like reference numerals denote like elements.

[0036] As used herein, the term “room temperature” refers to the temperature of natural indoor air which is not artificially heated or cooled, and may include, for example, a temperature range from 0° C. to 50° C.

[0037] In a rocksalt structure described below, cations and anions may have the same coordination number of 6 and a cation-anion radius ratio may have a range from about 0.414 to about 0.732. In this structure, a unit lattice includes anions aligned in a face-centered cubic (FCC) structure and cations located at the center of the cube and the centers of twelve edges of the cube. It is known that salt (NaCl) is a representative example of the rocksalt structure.

[0038] Rocksalt beryllium oxide (BeO) is known as having a high dielectric constant (about 270) and having a large band gap (>10 eV), and thus is regarded as a dielectric layer material of a next-generation dynamic random-access memory (DRAM) capacitor device. Particularly, since a physical thickness of a dielectric layer is severely limited in a 10-nm-level memory device, only a BeO layer is expected to simultaneously ensure a high capacitance and a low leakage current at a small layer thickness. However, unlike that alkaline earth metal oxide such as magnesium oxide (MgO), calcium oxide (CaO), or strontium oxide (SrO) has a rocksalt structure in a stable phase, BeO has a hexagonal wurtzite structure in a room temperature stable phase. In addition, wurtzite BeO has a low dielectric constant (about 8) unlike rocksalt BeO. Therefore, to use a BeO layer for a DRAM capacitor, the BeO layer needs to be controlled to have a rocksalt structure in a stable phase at room temperature.

[0039] In a process of generating a BeO-based layer, not only the BeO-based layer needs to be controlled to have a rocksalt structure but also a rocksalt-structure high-k dielectric layer generation process needs to be friendly to a DRAM process. Particularly, an aspect ratio of a three-dimensional (3D) capacitor of a 10-nm-level DRAM device is predicted to exceed 100, and a process capable of generating a uniform-thickness layer in the 3D capacitor is essential. In addition, since the process of generating the BeO-based layer and controlling the structure thereof is limited to a temperature of about 500° C. to about 600° C. to prevent degradation of, for example, a transistor at a front-end, the layer should be generated and the structure thereof should be controlled at a process temperature equal to or lower than 600° C.

[0040] To satisfy the above requirements, according to the present invention, a layer of a compound of BeO and another oxide is generated for stabilization of a rocksalt structure in an atomic layer deposition (ALD) process temperature range. In this case, the other oxide may include alkaline earth metal oxide having a rocksalt structure in a stable phase, e.g., MgO, CaO, SrO, or barium oxide (BaO), but is not limited thereto. The compound of BeO and the other oxide is used as a dielectric of a DRAM capacitor, and an ALD process having excellent step coverage characteristics is performed to generate a uniform layer on a 3D capacitor of a DRAM device.

[0041] Specifically, a dielectric layer according to an embodiment of the present invention has a rocksalt structure in a room temperature stable phase and is made of a compound having a chemical formula of $\text{Be}_x\text{M}_{1-x}\text{O}$, where M includes one of alkaline earth metals and x has a value greater than 0 and less than 0.5. In the chemical formula, M may include alkaline earth metal, oxide of which has a rocksalt structure in a room temperature stable phase, e.g., one of magnesium (Mg), calcium (Ca), strontium (Sr), and barium (Ba). The dielectric layer may include a compound of BeO and the alkaline earth metal oxide forming a rocksalt

solid solution without phase separation therebetween. In the chemical formula of the dielectric layer, x may have a value greater than 0 and equal to or less than 0.38, and more specifically, a value equal to or greater than 0.1 and equal to or less than 0.32. The dielectric layer may be used as a capacitor dielectric layer of a semiconductor memory device.

[0042] A method of manufacturing a dielectric layer, according to an embodiment of the present invention, includes depositing a stack structure of BeO and alkaline earth metal oxide by performing an ALD process, and obtaining a dielectric layer by performing heat treatment on the stack structure, wherein the dielectric layer has a rocksalt structure in a room temperature stable phase and is made of a compound having a chemical formula of $\text{Be}_x\text{M}_{1-x}\text{O}$, where M includes one of alkaline earth metals and x has a value greater than 0 and less than 0.5. The ALD process may be performed in a temperature range from room temperature to 350° C., and the heat treatment may be performed in a temperature range from 300° C. to 600° C. In the chemical formula, M may include alkaline earth metal, oxide of which has a rocksalt structure in a room temperature stable phase, e.g., one of Mg, Ca, Sr, and Ba.

[0043] Referring to FIG. 1, a semiconductor memory device according to another embodiment of the present invention includes a capacitor **1** including a lower electrode **10**, a dielectric layer **20** on the lower electrode **10**, and an upper electrode **30** on the dielectric layer **20**. The dielectric layer **20** has a rocksalt structure in a room temperature stable phase and is made of a compound having a chemical formula of $\text{Be}_x\text{M}_{1-x}\text{O}$, where M includes one of alkaline earth metals and x has a value greater than 0 and less than 0.5. In the chemical formula, M may include alkaline earth metal, oxide of which has a rocksalt structure in a room temperature stable phase, e.g., one of Mg, Ca, Sr, and Ba. The dielectric layer **20** may include a compound of BeO and the alkaline earth metal oxide forming a rocksalt solid solution without phase separation therebetween. In the chemical formula of the semiconductor memory device, x may have a value greater than 0 and equal to or less than 0.38, and more specifically, a value equal to or greater than 0.1 and equal to or less than 0.32. The lower electrode **10** may include an electrode made of one of titanium nitride (TiN), tantalum nitride (TaN), ruthenium (Ru), platinum (Pt), gold (Au), silver (Ag), aluminum (Al), rhodium (Rh), molybdenum (Mo), palladium (Pd), cobalt (Co), copper (Cu), iridium (Ir), tin oxide (SnO_2), ruthenium oxide (RuO_2), iridium oxide (IrO_2), molybdenum oxide (MoO_2), and strontium ruthenate (SrRuO_3).

[0044] A method of manufacturing a semiconductor memory device, according to another embodiment of the present invention, includes generating a lower electrode, depositing a stack structure of BeO and alkaline earth metal oxide on the lower electrode by performing an ALD process, generating an upper electrode on the stack structure, and obtaining a dielectric layer by performing heat treatment on the stack structure, wherein the dielectric layer has a rocksalt structure in a room temperature stable phase and is made of a compound having a chemical formula of $\text{Be}_x\text{M}_{1-x}\text{O}$, where M includes one of alkaline earth metals and x has a value greater than 0 and less than 0.5. The ALD process may be performed in a temperature range from room temperature to 350° C., and the heat treatment may be performed in a temperature range from 300° C. to 600° C. In the chemical

formula, M may include alkaline earth metal, oxide of which has a rocksalt structure in a room temperature stable phase, e.g., one of Mg, Ca, Sr, and Ba.

[0045] A dielectric layer, a semiconductor memory device including the dielectric layer, and methods of manufacturing the dielectric layer and the semiconductor memory device, according to embodiments of the present invention, will now be described in detail.

[0046] The present invention provides a complex oxide dielectric layer including rocksalt BeO usable as a capacitor dielectric layer material of a next-generation memory semiconductor device, and a method of manufacturing the same. The present invention also provides a memory semiconductor device including a capacitor including a lower electrode, a complex oxide dielectric layer on the lower electrode, and an upper electrode on the dielectric layer, and a method of manufacturing the same.

[0047] Particularly, in the present invention, the dielectric layer includes a compound of BeO and alkaline earth metal oxide having a rocksalt structure in a stable phase, e.g., MgO, CaO, SrO, or BaO, for stabilization of a rocksalt structure.

[0048] For convenience of explanation, in the following description, the alkaline earth metal oxide is assumed as MgO and a $\text{Be}_x\text{Mg}_{1-x}\text{O}$ ($0 \leq x \leq 1$) layer is generated as a dielectric layer to measure characteristics thereof. A lower electrode is generated using TiN that is an electrode material of a commercialized DRAM capacitor, and the $\text{Be}_x\text{Mg}_{1-x}\text{O}$ layer is generated by performing an ALD process.

[0049] In an embodiment of the present invention, $\text{Be}(\text{CH}_3)_2$ is used as a reaction source material of Be and $\text{Mg}(\text{EtCp})_2$ is used as a reaction source material of Mg. H_2O is used as an oxygen source material. A composition ratio of the $\text{Be}_x\text{Mg}_{1-x}\text{O}$ layer is adjusted by controlling a MgO—BeO cycle ratio in the ALD process.

[0050] In the present invention, the reaction source material of Be may include one of $\text{Be}(\text{CH}_3)_2$, $\text{Be}(\text{C}_2\text{H}_5)_2$, di-tert-butyl beryllium, and $\text{Be}(\text{acac})_2$ and the reaction source material of Mg may include one of $\text{Mg}(\text{EtCp})_2$, $\text{Mg}(\text{Cp})_2$, and $\text{Mg}(\text{thd})_2$. However, the reaction source materials are not limited thereto. The oxygen source material may include one or more selected from among water, oxygen gas, air, mixture gas of nitrogen and oxygen, methanol, ethanol, isopropanol, ozone, hydrogen peroxide, and oxygen plasma.

[0051] In an embodiment of the present invention, a stack structure of BeO and alkaline earth metal oxide may be deposited on a base layer by performing an ALD process, and then a dielectric layer may be obtained by performing heat treatment on the stack structure. Herein, BeO may be ALD-deposited by performing at least one cycle sequentially including providing a reaction source material of Be (e.g., $\text{Be}(\text{CH}_3)_2$) onto the base layer and adsorbing at least a part of the reaction source material of Be on the base layer, purging the remaining non-adsorbed reaction source material of Be, providing an oxygen source material (e.g., H_2O) onto the base layer having the reaction source material of Be adsorbed thereon and generating a unit deposition layer of BeO on the base layer due to reaction between the oxygen source material and the reaction source material of Be, and purging the remaining non-reacted oxygen source material.

[0052] The alkaline earth metal oxide may be ALD-deposited by performing at least one cycle sequentially including providing a reaction source material of alkaline earth metal (e.g., $\text{Mg}(\text{EtCp})_2$) onto the base layer and

adsorbing at least a part of the reaction source material of the alkaline earth metal on the base layer, purging the remaining non-adsorbed reaction source material of the alkaline earth metal, providing an oxygen source material (e.g., H_2O) onto the base layer having the reaction source material of the alkaline earth metal adsorbed thereon and generating a unit deposition layer of the alkaline earth metal oxide on the base layer due to reaction between the oxygen source material and the reaction source material of the alkaline earth metal, and purging the remaining non-reacted oxygen source material.

[0053] In an embodiment of the present invention, a $\text{Be}_x\text{Mg}_{1-x}\text{O}$ layer may be generated in a deposition temperature range from room temperature to 350°C . but is not limited thereto. Furthermore, in an embodiment of the present invention, a TiN substrate is used as a lower electrode, and an electrode-dielectric-electrode capacitor is produced by generating an upper electrode on the $\text{Be}_x\text{Mg}_{1-x}\text{O}$ layer. After the upper electrode is generated, heat treatment is performed at 400°C . for 30 minutes. The heat treatment is not limited to the temperature of 400°C . and may be performed in a temperature range from 300°C . to 600°C .

[0054] In the present invention, the substrate may include one of TiN, TaN, Ru, Pt, Au, Ag, Al, Rh, Mo, Pd, Co, Cu, Ir, SnO_2 , RuO_2 , IrO_2 , MoO_2 , and SrRuO_3 but is not limited thereto.

[0055] In an embodiment of the present invention, the $\text{Be}_x\text{Mg}_{1-x}\text{O}$ layer may have a thickness less than 10 nm. More specifically, for applicability to a DRAM capacitor, a $\text{Be}_x\text{Mg}_{1-x}\text{O}$ layer having a thickness less than 6 nm may be used.

[0056] Composition ratios, crystal structures, lattice constants, band gaps, dielectric constants, etc. of various dielectric layers according to test examples of the present invention will now be described in detail.

[0057] FIG. 2 and Table 1 are a graph and a table showing variations in a composition ratio of a $\text{Be}_x\text{Mg}_{1-x}\text{O}$ layer based on a MgO—BeO cycle ratio in an ALD process of a method of manufacturing a dielectric layer, according to test examples of the present invention.

TABLE 1

MgO cycles/(MgO cycles + BeO cycles) [%]	MgO/(MgO + BeO)[at. %]
0	0
10	17.58009
20	24.48273
30	33.15265
40	41.39726
50	48.48047
60	54.76649
70	62.08071
80	68.69637
90	81.55212
100	100

[0058] The MgO—BeO cycle ratio in the ALD process is calculated as $[\text{MgO ALD cycles}]/[\text{BeO ALD cycles} + \text{MgO ALD cycles}]$ [%]. The composition ratio is calculated by measuring area ratios and relative sensitivity factors (RSFs) of elements based on x-ray photoelectron spectroscopy (XPS). It is shown that the composition ratio of the $\text{Be}_x\text{Mg}_{1-x}\text{O}$ layer in an entire range of the MgO—BeO cycle ratio from 0% to 100% is well controlled based on a pulse cycle ratio.

[0059] FIG. 3 is a graph showing x-ray diffraction patterns based on composition ratios of a $\text{Be}_x\text{Mg}_{1-x}\text{O}$ layer used as a dielectric layer, according to test examples of the present invention. As described above in relation to FIG. 2, the composition ratio of the $\text{Be}_x\text{Mg}_{1-x}\text{O}$ layer may be changed by controlling MgO and BeO cycles in an ALD process. In FIG. 3, rocksalt (200) and (220) peaks are dominantly observed in a composition ratio range of MgO from about 54 at. % to 100 at. % in the $\text{Be}_x\text{Mg}_{1-x}\text{O}$ layer. This shows that the $\text{Be}_x\text{Mg}_{1-x}\text{O}$ layer has a rocksalt structure in the composition ratio range. On the other hand, wurtzite peaks are observed in a high composition ratio range of BeO from 76 at. % to 83 at. %, and this shows that the layer is crystallized to a wurtzite structure when the composition ratio of BeO is increased.

[0060] Referring to the results of FIG. 3, it is shown that a dielectric layer according to an embodiment of the present invention is made of a compound having a chemical formula of $\text{Be}_x\text{Mg}_{1-x}\text{O}$ and has a rocksalt structure in a room temperature stable phase when x has a value greater than 0 and less than 0.5.

[0061] FIG. 4 is a graph showing x-ray diffraction patterns based on variations in a composition ratio of a $\text{Be}_x\text{Mg}_{1-x}\text{O}$ layer in a range of a composition ratio (x) of BeO from 0 at. % to 38 at. % where rocksalt peaks are dominantly observed in FIG. 3. To more clearly observe structural variations of a rocksalt structure having a high dielectric constant, x-ray diffraction patterns in the composition ratio range are specifically shown. Rocksalt MgO (200) and (220) peaks move rightward from 42.948° and 62.360° as BeO is added. It may be understood that such peak variations occur because a lattice constant of rocksalt BeO, e.g., 0.3630 nm, is lower than a lattice constant of rocksalt MgO, e.g., 0.4212 nm, and thus an interplanar spacing is reduced as BeO is added.

[0062] FIG. 5 and Table 2 are a graph and a table showing variations in a lattice constant based on variations in a composition ratio of a $\text{Be}_x\text{Mg}_{1-x}\text{O}$ layer used as a dielectric layer and having a composition ratio (x) of BeO from 0 at. % to 38 at. % in FIG. 4, according to test examples of the present invention.

TABLE 2

x in $\text{Be}_x\text{Mg}_{1-x}\text{O}$ [%]	a lattice constant [Å] (200)	a lattice constant [Å] (220)
0	4.2112	4.2112
18.44	4.1895	4.198
31.3	4.183	4.191

[0063] In $\text{Be}_x\text{Mg}_{1-x}\text{O}$, pure MgO has a lattice constant of 0.4212 nm and the lattice constant is reduced as BeO is added. For example, $\text{Be}_{0.18}\text{Mg}_{0.82}\text{O}$ has a lattice constant of 0.41895 nm to 0.4198 nm, $\text{Be}_{0.31}\text{Mg}_{0.69}\text{O}$ has a lattice constant of 0.4183 nm to 0.4191 nm, and $\text{Be}_{0.38}\text{Mg}_{0.62}\text{O}$ has a lattice constant of 0.4182 nm. Since the lattice constant reduction effect may not be achieved when phase separation into BeO and MgO occurs in the $\text{Be}_x\text{Mg}_{1-x}\text{O}$ layer, the variations in the lattice constant based on the variations in the composition ratio show that the $\text{Be}_x\text{Mg}_{1-x}\text{O}$ layer forms a solid solution and has a cubic rocksalt structure in a range of the BeO composition ratio (x) from 0 at. % to 38 at. %.

[0064] Referring to the results of FIGS. 4 and 5, it is shown that a dielectric layer according to an embodiment of the present invention is made of a compound having a

chemical formula of $\text{Be}_x\text{Mg}_{1-x}\text{O}$ and that the $\text{Be}_x\text{Mg}_{1-x}\text{O}$ layer forms a solid solution and has a cubic rocksalt structure without phase separation between BeO and alkaline earth metal oxide when x has a value greater than 0 and equal to or less than 0.38.

[0065] FIGS. 6 to 9 are high-resolution transmission electron microscope (HRTEM) images of MgO, $\text{Be}_{0.19}\text{Mg}_{0.81}\text{O}$, $\text{Be}_{0.52}\text{Mg}_{0.48}\text{O}$, and BeO layers generated by performing an ALD process of a method of manufacturing a dielectric layer, according to test examples of the present invention. Referring to FIG. 6, it is shown that the MgO layer generated on a TiN substrate has a rocksalt structure. An average interplanar spacing of a rocksalt MgO (200) plane is measured to be 0.211 nm to 0.212 nm. Referring to FIG. 7, it is shown that the $\text{Be}_{0.19}\text{Mg}_{0.81}\text{O}$ layer generated on a TiN substrate also has a rocksalt structure and that an average interplanar spacing of a (200) plane is 0.208 nm to 0.209 nm which is less than that of MgO. An interplanar spacing of the $\text{Be}_{0.19}\text{Mg}_{0.81}\text{O}$ layer is reduced because an interplanar spacing of rocksalt BeO is less than an interplanar spacing of rocksalt MgO, and this means that BeO and MgO are chemically bonded to form a rocksalt solid solution. Referring to FIG. 8, crystallization to a rocksalt structure is not clearly shown in the $\text{Be}_{0.52}\text{Mg}_{0.48}\text{O}$ layer. Referring to FIG. 9, it is shown that the pure BeO layer has a (002) average interplanar spacing of 0.2183 nm corresponding to a wurtzite structure other than a rocksalt structure. This means that the pure BeO layer is crystallized to a wurtzite structure.

[0066] FIGS. 10 to 13 are selected area diffraction pattern (SADP) images of rectangular regions of the HRTEM images of FIGS. 6 to 9. Points corresponding to rocksalt structures and wurtzite structures based on layer composition ratios are observed on the SADP images. Referring to FIGS. 10 and 11, the SADP images of the MgO and $\text{Be}_{0.19}\text{Mg}_{0.81}\text{O}$ layers show rocksalt structures. On the other hand, referring to FIG. 12, the SADP image of the $\text{Be}_{0.52}\text{Mg}_{0.48}\text{O}$ shows a mixture of points corresponding to rocksalt MgO and points corresponding to wurtzite BeO. This means that phase separation into rocksalt MgO and wurtzite BeO occurs in the $\text{Be}_{0.52}\text{Mg}_{0.48}\text{O}$ layer. Referring to FIG. 13, the SADP image of the pure BeO layer shows points corresponding to a single phase of wurtzite.

[0067] Referring to the results of FIGS. 3, and 6 to 13, it is shown that a dielectric layer according to an embodiment of the present invention is made of a compound having a chemical formula of $\text{Be}_x\text{Mg}_{1-x}\text{O}$ and has a rocksalt structure without a wurtzite structure in a room temperature stable phase when x has a value greater than 0 and less than 0.5.

[0068] FIG. 14 and Table 3 are a graph and a table showing variations in a band gap based on a composition ratio of a $\text{Be}_x\text{Mg}_{1-x}\text{O}$ layer used as a dielectric layer, according to test examples of the present invention.

TABLE 3

x in $\text{Be}_x\text{Mg}_{1-x}\text{O}$	Band gap (eV)
0	7.978
0.1	8.041
0.19	8.179
0.32	8.292
0.38	8.302
0.46	8.638
0.52	8.363
0.59	8.3919
0.67	8.2124

TABLE 3-continued

x in $\text{Be}_x\text{Mg}_{1-x}\text{O}$	Band gap (eV)
0.76	8.4147
0.83	8.548
1	9.2124

[0069] Very large band gaps (>8 eV) are shown in an entire range of the composition ratio and the band gap increases in proportion to a composition ratio of BeO. It is known that MgO has a band gap of about 7.8 eV and BeO has a band gap equal to or larger than 10 eV. The band gap of $\text{Be}_x\text{Mg}_{1-x}\text{O}$ is remarkably larger than those of next-generation dielectric materials such as titanium oxide (TiO_2) (3 eV) and strontium titanate (SrTiO_3) (3 eV), and is much larger than those of currently commonly used materials such as zirconium oxide (ZrO_2) (5.8 eV) and hafnium oxide (HfO_2) (5.3 eV to 5.7 eV). In addition, the band gap of $\text{Be}_x\text{Mg}_{1-x}\text{O}$ is larger than those of representative large-band-gap materials such as silicon oxide (SiO_2) (9 eV) and amorphous aluminum oxide (Al_2O_3) (7.5 eV). As such, it is shown that $\text{Be}_x\text{Mg}_{1-x}\text{O}$ is a material capable of easily suppressing a leakage current and reducing a layer thickness.

[0070] FIG. 15 and Table 4 are a graph and a table showing variations in a dielectric constant based on a composition ratio of a $\text{Be}_x\text{Mg}_{1-x}\text{O}$ layer, according to test examples of the present invention.

TABLE 4

x in $\text{Be}_x\text{Mg}_{1-x}\text{O}$	Dielectric Constant
0	9.723
0.1	18.40925
0.19	17.96076
0.32	17.7717
0.38	10.29567
0.46	11.32174
0.52	10.17506
0.59	12.64091
0.67	11.70892
0.76	11.27718
0.83	9.28745
1	9.4849

[0071] It is shown that the dielectric constant in a composition ratio range of $x=0.1$ to 0.32 in $\text{Be}_x\text{Mg}_{1-x}\text{O}$ is about two times that of the other composition ratio range. Single layers of BeO and MgO have a dielectric constant equal to or lower than 10 and the increase in the dielectric constant may not be expected by simply mixing the two materials and may be achieved as a result of a change of a crystal structure of the $\text{Be}_x\text{Mg}_{1-x}\text{O}$ layer to a rocksalt structure in the composition ratio range. When a composition ratio of BeO is further increased (BeO>60 at. %), since phase separation into rocksalt MgO and wurtzite BeO occurs as shown in FIGS. 6 to 9, and 10 to 13, the effect of increasing the dielectric constant by controlling BeO to a rocksalt structure in the layer disappears.

[0072] Referring to the results of FIG. 15, it is shown that a dielectric layer according to an embodiment of the present invention is made of a compound having a chemical formula of $\text{Be}_x\text{Mg}_{1-x}\text{O}$ and has a very large band gap (>8 eV) and a high dielectric constant (>17) when x has a value equal to or greater than 0.1 and equal to or less than 0.32.

[0073] As described above, according to various embodiments of the present invention, a dielectric layer having a high dielectric constant and a large band gap, a semiconductor memory device including the dielectric layer, and methods of manufacturing the dielectric layer and the semiconductor memory device may be developed and, ultimately, a 10-nm-level DRAM device may be produced. However, the scope of the present invention is not limited to the above effects.

[0074] While the present invention has been particularly shown and described with reference to embodiments thereof, it will be understood by one of ordinary skill in the art that various changes in form and details may be made therein without departing from the spirit and scope of the present invention as defined by the following claims.

What is claimed is:

1. A dielectric layer having a rocksalt structure in a room temperature stable phase and made of a compound having a chemical formula of $\text{Be}_x\text{M}_{1-x}\text{O}$, where M comprises one of alkaline earth metals and x has a value greater than 0 and less than 0.5.

2. The dielectric layer of claim 1, wherein M comprises one of magnesium (Mg), calcium (Ca), strontium (Sr), and barium (Ba).

3. The dielectric layer of claim 1, wherein M comprises alkaline earth metal, oxide of which has a rocksalt structure in a room temperature stable phase.

4. The dielectric layer of claim 1, wherein the dielectric layer comprises a compound of beryllium oxide (BeO) and alkaline earth metal oxide forming a rocksalt solid solution without phase separation therebetween.

5. The dielectric layer of claim 1, wherein x has a value greater than 0 and equal to or less than 0.38.

6. The dielectric layer of claim 5, wherein x has a value equal to or greater than 0.1 and equal to or less than 0.32.

7. The dielectric layer of claim 1, wherein the dielectric layer is used as a capacitor dielectric layer of a semiconductor memory device.

8. A semiconductor memory device comprising:

a capacitor comprising:

a lower electrode;

a dielectric layer on the lower electrode; and

an upper electrode on the dielectric layer,

wherein the dielectric layer has a rocksalt structure in a room temperature stable phase and is made of a compound having a chemical formula of $\text{Be}_x\text{M}_{1-x}\text{O}$, where M comprises one of alkaline earth metals and x has a value greater than 0 and less than 0.5.

9. The semiconductor memory device of claim 8, wherein M comprises one of magnesium (Mg), calcium (Ca), strontium (Sr), and barium (Ba).

10. The semiconductor memory device of claim 8, wherein M comprises alkaline earth metal, oxide of which has a rocksalt structure in a room temperature stable phase.

11. The semiconductor memory device of claim 8, wherein the dielectric layer comprises a compound of beryllium oxide (BeO) and alkaline earth metal oxide forming a rocksalt solid solution without phase separation therebetween.

12. The semiconductor memory device of claim 8, wherein x has a value greater than 0 and equal to or less than 0.38.

13. The semiconductor memory device of claim **12**, wherein x has a value equal to or greater than 0.1 and equal to or less than 0.32.

14. The semiconductor memory device of claim **8**, wherein the lower electrode comprises an electrode made of one of titanium nitride (TiN), tantalum nitride (TaN), ruthenium (Ru), platinum (Pt), gold (Au), silver (Ag), aluminum (Al), rhodium (Rh), molybdenum (Mo), palladium (Pd), cobalt (Co), copper (Cu), iridium (Ir), tin oxide (SnO₂), ruthenium oxide (RuO₂), iridium oxide (IrO₂), molybdenum oxide (MoO₂), and strontium ruthenate (SrRuO₃).

* * * * *