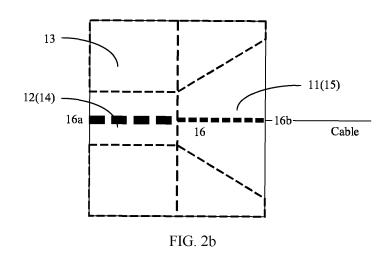
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# (54) SURFACE WAVE EXCITATION DEVICE AND PRINTED CIRCUIT BOARD

(57) A surface wave excitation device and a printed circuit board are provided. The surface wave excitation device includes: a multilayer PCB structure and a transmission line (16), where the multilayer PCB structure includes at least five layers of PCBs (11, 12, 13, 14, and 15). The transmission line (16) is disposed on a wire layer PCB (13), and PCBs (11 and 12) and PCBs (14 and 15) of a same quantity of layers are respectively disposed above and below the wire layer PCB (13). A copper wire is disposed on each layer of PCB (11, 12, 14, and 15), and the copper wire forms a closed region. Closed regions on the PCBs (11 and 12) and the PCBs (14 and 15) that are respectively disposed above and below the

wire layer PCB (13) and that have a same distance from the wire layer PCB (13) are in a same shape, and a closed region on a PCB (11, 12, 14, or 15) farther away from the wire layer PCB (13) occupies a larger area. There are two closed regions (13a and 13b) on the wire layer PCB (13), one of which is disposed on one side of the transmission line (16), and the other is disposed on the other side of the transmission line (16), and shapes of the two closed regions (13a and 13b) are mutually symmetrical with the transmission line (16) as a symmetry axis. Integration of the surface wave excitation device and the PCB is implemented, so that surface wave is easy to propagate.



### Description

#### **TECHNICAL FIELD**

**[0001]** This application relates to communications technologies, and in particular, to a surface wave excitation device and a printed circuit board.

## BACKGROUND

[0002] In the related art, communication is implemented by transmitting an electromagnetic wave through a power line. No additional optical fiber needs to be deployed, and only an existing high-altitude power line needs to be used. In this way, communication can be implemented even in remote rural areas as long as there is a power line. To use a power line for transmission and to have low-loss and ultra-bandwidth performance, a special electromagnetic wave needs to be used. The electromagnetic wave is a surface wave generated through conversion from a transverse electric magnetic (transverse electric magnetic, TEM for short) mode to a transverse magnetic (transverse magnetic, TM for short) mode. The surface wave is a wave in which an electromagnetic field is transmitted in circles outside a conductor cable. The transmission mode of the surface wave is the TM mode. The conductor cable only guides the electromagnetic field transmission, and no current is transmitted inside the conductor cable. Compared with conventional wireless transmission, the surface wave has good transmission directivity and is not easy to spread to cause radiation loss. Compared with a coaxial or metal cavity waveguide, the surface wave has little conductor loss when transmitted in free space. It can be seen that the surface wave has the characteristics of large bandwidth and low loss in the transmission process.

**[0003]** Generally, a surface wave excitation device uses a cone to implement conversion from the TEM mode to the TM mode. However, a cone that meets a communication index is relatively large, and cannot directly interconnect with a printed circuit board (Printed Circuit Board, PCB for short) to propagate the surface wave.

#### SUMMARY

**[0004]** This application provides a surface wave excitation device and a printed circuit board, to implement integration of the surface wave excitation device and the PCB and to facilitate surface wave propagation.

**[0005]** According to a first aspect, this application provides a surface wave excitation device, including: a multilayer PCB structure and a transmission line, where the multi-layer PCB structure includes at least five layers of PCBs, the transmission line is disposed on a wire layer PCB of the at least five layers of PCBs, PCBs of a same quantity of layers are respectively disposed above and below the wire layer PCB, a copper wire is disposed on each layer of PCB, the copper wire is formed by spraying copper on the PCB board, and the copper wire on each layer of PCB forms a closed region. Closed regions on PCBs that are respectively disposed above and below the wire layer PCB and that have a same distance from the wire layer PCB are in a same shape, and a closed region on a PCB farther away from the wire layer PCB occupies a larger area. There are two closed regions on the wire layer PCB, one is disposed on one side of the transmission line, and the other is disposed on the other

<sup>10</sup> side of the transmission line, and shapes of the two closed regions are mutually symmetrical with the transmission line as a symmetry axis. The copper wire disposed on each layer of PCB is used to convert a signal propagated in a TEM mode to a surface wave propagated

<sup>15</sup> in a TM mode, and the transmission line is used to guide the surface wave propagation.

[0006] The surface wave excitation device in this application is used to convert the signal propagated in the TEM mode to the surface wave propagated in the TM
 <sup>20</sup> mode, and a characteristic of at least five layers of PCBs with sprayed copper is used to implement integration of the surface wave excitation device and the PCB, which is easy to integrate and easy for the surface wave excitation device to directly interconnect with the PCB to prop <sup>25</sup> agate the surface wave.

- [0007] In a possible implementation, the signal is transmitted from a first end of the transmission line to a second end of the transmission line; and in a direction from the first end of the transmission line to the second end of the 30 transmission line, a distance between the two closed regions on the wire layer PCB is increasing. In the direction from the first end of the transmission line to the second end of the transmission line, the two closed regions on the wire layer PCB are jointly enclosed on edges opposite 35 to each other to form a semi-closed region, an opening of the semi-closed region faces the second end of the transmission line, and the semi-closed region gradually expands on two sides in the opening direction. Shapes of closed regions disposed on PCBs above and below
- 40 the wire layer PCB are rectangles; a long side of the rectangle is parallel to the transmission line, and one end of the long side of the rectangle is flush with the first end of the transmission line. A centerline of a wide side of the rectangle is in the same plane with and parallel to the
- transmission line in a vertical direction. Lengths of long sides of the rectangles on the PCBs that are respectively disposed above and below the wire layer PCB and that have the same distance from the wire layer PCB are the same; and a long side of a rectangle on a PCB farther
  away from the wire layer PCB is longer.

[0008] The closed regions on the foregoing layers of PCBs jointly form the shape of a square box and a hollow structure inside, which achieves an effect of gradually increasing impedance in a direction in which a signal is
 <sup>55</sup> transmitted on the transmission line, and the signal propagated in a TEM mode may be converted to a surface wave propagated in a TM mode. The surface wave propagation is guided by the transmission line. It can be seen

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that the surface wave excitation device in this application uses a feature of at least five layers of PCBs with sprayed copper, to easily implement integration of the surface wave excitation device and the PCB, and the surface wave excitation device is easy to directly interconnect with the PCB to propagate the surface wave.

**[0009]** In a possible implementation, a shape of a cross section of the semi-closed region on the wire layer PCB includes a trapezoid, a triangle, or an arc.

**[0010]** In a possible implementation, on the wire layer PCB, the signal is transmitted from the first end of the transmission line to the second end of the transmission line; and the transmission line tapers in the direction from the first end of the transmission line to the second end of the transmission line.

**[0011]** In a possible implementation, the transmission line is further connected with a cable, and the cable is configured to propagate a surface wave introduced through the transmission line.

**[0012]** The surface wave is propagated in circles outside the cable. The cable herein only guides the surface wave transmission, and no current is transmitted inside the cable, which meets the communication requirements of high bandwidth and low loss.

**[0013]** According to a second aspect, this application provides a PCB. A surface wave excitation device in the first aspect is disposed on the PCB, to easily implement integration of the surface wave excitation device and the PCB, and the surface wave excitation device is easy to directly interconnect with the PCB to propagate a surface wave

## **BRIEF DESCRIPTION OF DRAWINGS**

**[0014]** To describe the technical solutions in this application or in the prior art more clearly, the following briefly describes the accompanying drawings for describing the embodiments or the prior art. It is clearly that the accompanying drawings in the following description show some embodiments of this application, and a person of ordinary skill in the art may still derive other drawings from these accompanying drawings without creative efforts.

FIG. 1 is a schematic diagram of a propagation form of a surface wave;

FIG. 2a is a schematic side view structural diagram of Embodiment 1 of a surface wave excitation device according to this application;

FIG. 2b is a schematic top perspective structural diagram of Embodiment 1 of a surface wave excitation device according to this application;

FIG. 2c is a schematic structural diagram of a wire layer PCB of Embodiment 1 of a surface wave excitation device according to this application;

FIG. 3a is a schematic side view structural diagram of Embodiment 2 of a surface wave excitation device according to this application;

FIG. 3b is a schematic top perspective structural diagram of Embodiment 2 of a surface wave excitation device according to this application;

FIG. 3c is a schematic structural diagram of a wire layer PCB of Embodiment 2 of a surface wave excitation device according to this application; and FIG. 4 is a simulation diagram of electric field distribution of surface wave propagation from a surface wave excitation device to a copper wire.

### **DESCRIPTION OF EMBODIMENTS**

**[0015]** To make the objectives, technical solutions, and advantages of this application clearer, the following clearly and completely describes the technical solutions in this application with reference to the accompanying drawings in this application. It is clearly that the described embodiments are merely a part rather than all of the embodiments of this application. All other embodiments obtained

<sup>20</sup> by a person of ordinary skill in the art based on the embodiments of this application without creative efforts shall fall within the protection scope of this application.

[0016] In the related art, a surface wave is propagated through a power line. No additional optical fiber needs to be deployed and only an existing high-altitude power line needs to be used. In this way, communication can be implemented even in remote rural areas as long as there is a power line, thereby reducing costs incurred in deploying optical fiber backhaul, increasing wireless coverage regions, and providing convenient scenarios for Internet of Things applications. FIG. 1 is a schematic diagram of a propagation form of a surface wave. As shown

in FIG. 1, a surface wave may be generated through conversion from a TEM mode to a TM mode. The surface
 <sup>35</sup> wave is a wave in which an electromagnetic field is transmitted in circles outside a conductor cable. The transmission mode of the surface wave is the TM mode. The conductor cable herein only guides the electromagnetic field transmission, and no current is transmitted inside

40 the conductor cable. Compared with conventional wireless transmission, the surface wave has good transmission directivity and is not easy to spread to cause radiation loss. Compared with a coaxial or metal cavity waveguide, the surface wave has little conductor loss

<sup>45</sup> when transmitted in free space. It can be seen that the surface wave has the characteristics of large bandwidth and low loss in the transmission process. In the prior art, a surface wave excitation device uses a cone to implement conversion from the TEM mode to the TM mode.

50 However, a cone that meets a communication index is relatively large, and cannot directly interconnect with a PCB to propagate the surface wave.

[0017] To resolve the foregoing problem, this application provides a surface wave excitation device, including:
 <sup>55</sup> a multi-layer PCB structure and a transmission line, where the multi-layer PCB structure includes at least five layers of PCBs, the transmission line is disposed on a wire layer PCB of the at least five layers of PCBs, PCBs

of a same quantity of layers are respectively disposed above and below the wire layer PCB, a copper wire is disposed on each layer of PCB, the copper wire is formed by spraying copper on the PCB board, and the copper wire on each layer of PCB forms a closed region. Closed regions on PCBs that are respectively disposed above and below the wire layer PCB and that have a same distance from the wire layer PCB are in a same shape, and a closed region on a PCB farther away from the wire layer PCB occupies a larger area. There are two closed regions on the wire layer PCB, one of which is disposed on one side of the transmission line, and the other is disposed on the other side of the transmission line, and shapes of the two closed regions are mutually symmetrical with the transmission line as a symmetry axis. The copper wire disposed on each layer of PCB is used to convert a signal propagated in a TEM mode to a surface wave propagated in a TM mode, and the transmission line is used to guide the surface wave propagation. It may be considered that the closed regions formed by the copper wires disposed on the PCB layers jointly form the shape of a square box and a hollow structure inside (It should be noted that the hollow described herein is not true hollow, but is that the closed regions of the layers form a hollow structure. Actually, there is a PCB medium in the hollow part). The hollow part takes on a shape of a gradually increasing opening in a direction in which a signal is transmitted on the transmission line, and a copper medium is used between a side wall of the hollow part and an outer surface of the square box. The feature of the electromagnetic field distribution of the surface wave is mainly concentrated in a region inside a medium forming a waveguide and near to a surface of the medium, and the electromagnetic field decays exponentially with the increase of the distance from the surface outside the medium. The closed regions on the foregoing layers of PCBs jointly form the shape of a square box and a hollow structure inside, which achieves an effect of gradually increasing impedance in a direction in which a signal is transmitted on the transmission line, and the signal propagated in a TEM mode may be converted to a surface wave propagated in a TM mode. The surface wave propagation is guided by the transmission line. It can be seen that the surface wave excitation device in this application uses a feature of at least five layers of PCBs with sprayed copper, to easily implement integration of the surface wave excitation device and the PCB, and the surface wave excitation device is easy to directly interconnect with the PCB to propagate the surface wave.

**[0018]** The following describes the technical solutions of this application in detail by using several specific embodiments.

**[0019]** FIG. 2a is a schematic side view structural diagram of Embodiment 1 of a surface wave excitation device according to this application. FIG. 2b is a schematic top perspective structural diagram of Embodiment 1 of a surface wave excitation device according to this application. FIG. 2c is a schematic structural diagram of a wire layer PCB of Embodiment 1 of a surface wave excitation device according to this application. In combination with FIG. 2a, FIG. 2b, and FIG. 2c (a copper wire structure on a PCB board is shown in the accompanying drawings, and the PCB board is not shown), the device in this embodiment may include: five layers of PCBs (PCB 11, PCB 12, PCB 13, PCB 14, and PCB 15 sequentially shown from top to bottom), and a transmission line 16. The transmission line 16 is disposed on PCB 13 located in the middle a generative in dimension and a factor of PCB 11.

<sup>10</sup> middle, a copper wire is disposed on each of PCB 11, PCB 12, PCB 13, PCB 14, and PCB 15, and the copper wire forms a closed region on each layer of PCB. [0020] On PCB 13, two closed regions (closed regions)

13a and 13b) are formed by the copper wire. The closed
regions 13a and 13b are respectively disposed on two sides of the transmission line 16, and the closed regions
13a and 13b are mutually symmetrical with the transmission line 16 as a symmetry axis. A signal is transmitted from a first end 16a of the transmission line 16 to a second

<sup>20</sup> end 16b, and a distance between the closed regions 13a and 13b is increasing in the signal transmission direction. The closed regions 13a and 13b on PCB 13 are jointly enclosed on edges opposite to each other to form a semiclosed region 13c, an opening of the semi-closed region

<sup>25</sup> 13c faces the second end 16b of the transmission line, and the semi-closed region 13c gradually expands on two sides in the opening direction. In FIG. 2c, a shape of a cross section of the semi-closed region 13c on PCB 13 is a triangle. Optionally, a shape of a cross section of the

30 semi-closed region 13c on PCB 13 may alternatively be a trapezoid or an arc, as long as the cross section is in the signal transmission direction and two sides of the semi-closed region 13c gradually expands. This is not specifically limited in this embodiment of this application.

<sup>35</sup> **[0021]** On PCB 11, PCB 12, PCB 14, and PCB 15, shapes of the closed regions formed by the copper wires are all rectangles, a long side of the rectangle is parallel to the transmission line 16, and one end of the long side of the rectangle is flush with the first end 16a of the trans-

40 mission line 16. A centerline of a wide side of the rectangle is in the same plane with and parallel to the transmission line 16 in a vertical direction. Long sides of the rectangles on PCB 11 and PCB 15 have the same length, long sides of the rectangles on PCB 12 and PCB 14 have

<sup>45</sup> the same length, and the lengths of the long sides of the rectangles on PCB 12 and PCB 14 are less than the lengths of the long sides of the rectangles on PCB 11 and PCB 15.

[0022] In the transmission direction from the first end
<sup>50</sup> 16a of the transmission line 16 to the second end 16b, the transmission line 16 tapers. Such a shape can fit with the closed region formed by the copper wire on the PCB, to better improve the accuracy of generated impedance.
[0023] The transmission mode of the surface wave is
<sup>55</sup> a TM mode, and the mode is obtained through conversion after impedance is gradually increased in the TEM mode. In this embodiment of this application, a multi-layer PCB structure is used, and the transmission line 16 is disposed

on PCB 13 in the middle. To ensure symmetry of field distribution in a propagation process of the surface wave, the transmission line 16 is used as a central axis, copper is sprayed on PCB 13 to form two symmetrical closed regions, and copper is sprayed on the upper and lower PCBs (PCB 11, PCB 12, PCB 14, and PCB 15) of PCB 13 to form a rectangular closed region. Along side of the rectangle increases layer by layer in the transmission direction from the first end 16a of the transmission line 16 to the second end 16b and in the direction away from PCB 13. As the length of the long side of the rectangle increases layer by layer, the impedance generated by combining the rectangle on each PCB with the two closed regions on PCB 13 is increasing. The distance between the two closed regions on PCB 13 gradually increases, and the distance matches with layer order transformation of the impedance generated by the rectangles on PCB 11, PCB 12, PCB 14, and PCB 15 to achieve an effect of increasing output impedance. In FIG. 4a, the rectangles on PCB 12 and PCB 14 and parts closer to each other in the two closed regions on PCB 13 are combined to generate an impedance of 50  $\Omega$ , and the rectangles on PCB 11 and PCB 15 and parts farther from each other in the two closed regions on PCB 13 are combined to generate an impedance of 70  $\Omega$ . It can be seen that the impedance of the surface wave excitation device used in this embodiment of this application increases layer by layer, reflection decreases layer by layer, and performance correspondingly improves.

[0024] FIG. 3a is a schematic side view structural diagram of Embodiment 2 of a surface wave excitation device according to this application. FIG. 3b is a schematic top perspective structural diagram of Embodiment 2 of a surface wave excitation device according to this application. FIG. 3c is a schematic structural diagram of a wire layer PCB of Embodiment 2 of a surface wave excitation device according to this application. In combination with FIG. 3a, FIG. 3b, and FIG. 3c (a copper wire structure on a PCB board is shown in the accompanying drawings, and the PCB board is not shown), the device in this embodiment may include seven layers of PCBs (PCB 21, PCB 22, PCB 23, PCB 24, PCB 25, PCB 26, and PCB 27 sequentially from top to bottom) and a transmission line 28. The transmission line 28 is disposed on PCB 24 located in the middle, and a copper wire structure is disposed on each of PCB 21, PCB 22, PCB 23, PCB 24, the PCB 25, PCB 26, and PCB 27, and the copper wire forms a closed region on each layer of PCB.

**[0025]** On PCB 24, two closed regions (closed regions 24a and 24b) are formed by the copper wire. The closed regions 24a and 24b are respectively disposed on two sides of the transmission line 28, and the closed regions 24a and 24b are mutually symmetrical with the transmission line 28 as a symmetry axis. A signal is transmitted from a first end 28a of the transmission line 28 to a second end 28b, and a distance between the closed regions 24a and 24b increases in the signal transmission direction. The closed regions 24a and 24b on PCB 24 are jointly

enclosed on edges opposite to each other to form a semiclosed region 24c, an opening of the semi-closed region 24c faces the second end 28b of the transmission line, and the semi-closed region 24c gradually expands on two sides in the opening direction. In FIG. 3c, a shape of a cross section of the semi-closed region 24c on PCB 24 is a triangle. Optionally, a shape of a cross section of the semi-closed region 24c on PCB 24 may alternatively be a trapezoid or an arc, as long as the cross section is in

the signal transmission direction and two sides of the semi-closed region 24c gradually expands. This is not specifically limited in this embodiment of this application.
 [0026] On PCB 21, PCB 22, PCB 23, PCB 25, PCB 26, and PCB 27, shapes of the closed region formed by

<sup>15</sup> the copper wires are rectangles, a long side of the rectangle is parallel to the transmission line 28, and one end of the long side of the rectangle is flush with the first end 28a of the transmission line 28. A centerline of a wide side of the rectangle is in the same plane with and parallel

to the transmission line 28 in a vertical direction. Long sides of the rectangles on PCB 21 and PCB 27 have the same length, long sides of the rectangles on PCB 22 and PCB 26 have the same length, and long sides of the rectangles on PCB 23 and PCB 25 have the same length.

The lengths of the long sides of the rectangles on PCB 23 and PCB 25 are less than the lengths of the long sides of the rectangles on PCB 22 and PCB 26, and the lengths of the long sides of the rectangles on PCB 22 and PCB 26 are less than the lengths of the long sides of the rectangles on PCB 21 and PCB 27.

[0027] A difference from Embodiment 1 shown in FIG. 2a to FIG. 2c lies in that the multilayer PCB structure in this embodiment of this application includes seven layers of PCBs, and therefore impedance transformation of one 35 more order can be implemented. As shown in FIG. 3a, the rectangles on PCB 23 and PCB 25 and parts closest to each other in the two closed regions on PCB 24 are combined to generate an impedance of 50  $\Omega$ , and the rectangles on the PCB 22 and the PCB 26 and parts 40 closer to each other in the two closed regions on PCB 24 are combined to generate an impedance of 70  $\Omega$ , and the rectangles on PCB 21 and PCB 27 and parts farthest away from each other in the two closed regions on PCB 24 are combined to generate an impedance of 90  $\Omega$ . It

<sup>45</sup> can be seen that the impedance of the surface wave excitation device used in this embodiment of this application increases layer by layer, reflection decreases layer by layer, and performance correspondingly improves. Compared with the two-layer impedance increase in Embod<sup>50</sup> iment 1, this embodiment of this application implements three-layer impedance increase, and has better surface wave propagation performance, and less energy loss of the surface wave than the two-layer impedance increase.
[0028] It should be noted that, the spatially symmetric increase conner wire structure used by the surface wave

<sup>5</sup> increase copper wire structure used by the surface wave excitation device in this application includes at least five layers of PCBs. On a basis that a process level allows and costs are controllable, a larger quantity of PCB layers

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indicates a larger impedance transformation order that can be implemented, and better surface wave transmission performance can be achieved. Therefore, the quantity of PCB layers is not specifically limited.

**[0029]** In the foregoing embodiment, both the transmission line 16 and the transmission line 28 are connected with a cable to propagate a surface wave introduced through the transmission line. The surface wave is propagated on the cable in the form shown in FIG. 1. The cable herein only guides the surface wave transmission, and no current is transmitted inside the cable, which meets the communication requirements of large bandwidth and low loss. FIG. 4 is a simulation diagram of electric field distribution of surface wave propagation from a surface wave excitation device to a copper wire. As shown in FIG. 4, according to the technical solutions of this application, the surface wave is well transitioned from the surface wave excitation device to the cable for propagation.

**[0030]** The surface wave excitation device shown in FIG. 2a to FIG. 3c uses a feature of at least five layers of PCBs with sprayed copper, to implement integration of the surface wave excitation device and the PCB. The integration is easy and the surface wave excitation device is easy to directly interconnect with the PCB to propagate the surface wave. Based on this, an embodiment of this application provides a PCB. The PCB may be used in a signal transmission device. The foregoing surface wave excitation device is disposed on the PCB to implement surface wave transmission, which provides a technical effect same as that in the foregoing embodiment. Details are not described herein again.

**[0031]** Finally, it should be noted that the foregoing embodiments are merely intended for describing the technical solutions of this application other than limiting this application. Although this application is described in detail with reference to the foregoing embodiments, persons of ordinary skill in the art should understand that they may still make modifications to the technical solutions described in the foregoing embodiments or make equivalent replacements to some or all technical features thereof, without departing from the scope of the technical solutions of the embodiments of this application.

#### Claims

 A surface wave excitation device, comprising: a multi-layer printed circuit board PCB structure and a transmission line, wherein the multi-layer PCB structure comprises at least five layers of PCBs; the transmission line is disposed on a wire layer PCB of the at least five layers of PCBs, and PCBs of a same quantity of layers are respectively disposed above and below the wire layer PCB; a copper wire is disposed on each layer of PCB of the at least five layers of PCB, and the copper wire on each layer of PCB forms a closed region; closed regions on PCBs that are respectively disposed above and below the wire layer PCB and that have a same distance from the wire layer PCB are in a same shape, and a closed region on a PCB farther away from the wire layer PCB occupies a larger area; and there are two closed regions on the wire layer PCB, one of which is disposed on one side of the transmission line, and the other is disposed on the other side of the transmission line, and shapes of the two closed regions are mutually symmetrical with the transmission line as a symmetry axis; and the copper wire disposed on each layer of PCB is used to convert a signal propagated in a transverse electric magnetic TEM mode to a surface wave propagated in a transverse magnetic TM mode, and the transmission line is used to guide the surface wave propagation.

- 2. The apparatus according to claim 1, wherein on the wire layer PCB, the signal is transmitted from a first end of the transmission line to a second end of the transmission line; and in a direction from the first end of the transmission line to the second end of the transmission line, a distance between the two closed regions on the wire layer PCB is increasing.
  - 3. The apparatus according to claim 2, wherein in the direction from the first end of the transmission line to the second end of the transmission line, the two closed regions on the wire layer PCB are jointly enclosed on edges opposite to each other to form a semi-closed region, an opening of the semi-closed region faces the second end of the transmission line, and the semi-closed region gradually expands on two sides in the opening direction.
- 4. The apparatus according to any one of claims 1 to 3, wherein on the wire layer PCB, the signal is transmitted from the first end of the transmission line to the second end of the transmission line; and shapes of the closed regions disposed on the PCB above and below the wire layer PCB are rectangles, a long side of the rectangle is parallel to the transmission line, one end of the long side of the rectangle is flush with the first end of the transmission line, and a centerline of a wide side of the rectangle is in the same plane with and parallel to the transmission line in a vertical direction.
- 5. The apparatus according to claim 4, wherein lengths of long sides of the rectangles on the PCBs that are respectively disposed above and below the wire layer PCB and that have the same distance from the wire layer PCB are the same, and a long side of a rectangle on a PCB farther away from the wire layer PCB is longer.

- 6. The apparatus according to claim 3, wherein a shape of a cross section of the semi-closed region on the wire layer PCB comprises a trapezoid, a triangle, or an arc.
- 7. The apparatus according to any one of claims 1 to 6, wherein on the wire layer PCB, the signal is transmitted from the first end of the transmission line to the second end of the transmission line; and the transmission line tapers in the direction from the 10 first end of the transmission line to the second end of the transmission line.
- The apparatus according to any one of claims 1 to 7, wherein the transmission line is further connected <sup>15</sup> with a cable, and the cable is used to propagate a surface wave introduced through the transmission line.
- **9.** A printed circuit board PCB, wherein the surface <sup>20</sup> wave excitation device according to any one of claims 1 to 8 is disposed on the PCB.

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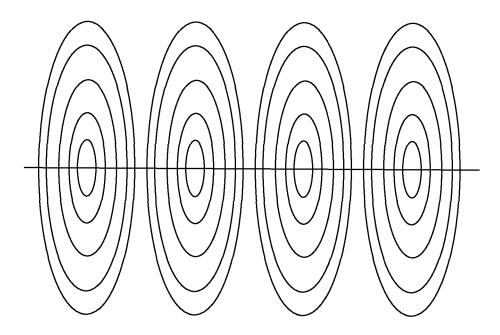
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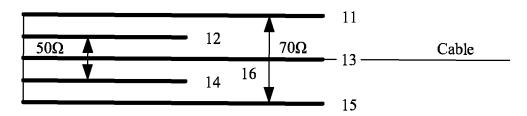
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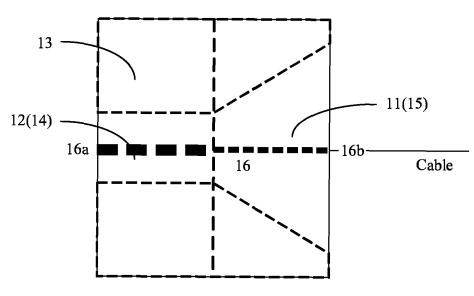
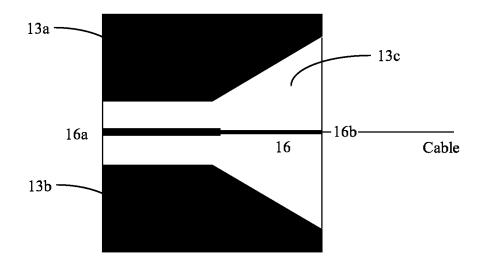
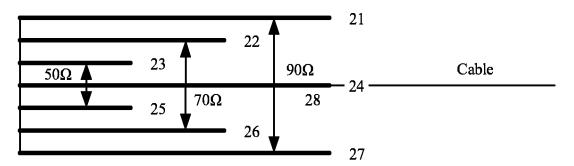


FIG. 2b









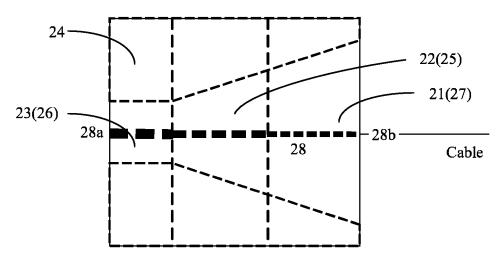


FIG. 3b

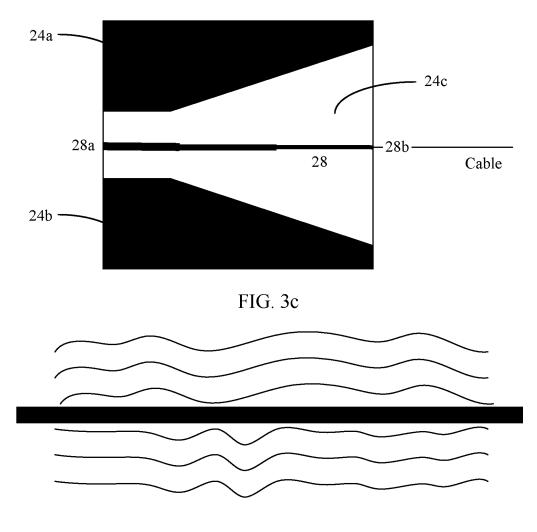


FIG. 4

# EP 3 823 091 A1

INTERNATIONAL SEARCH REPORT Interna	ational application No. PCT/CN2018/105950						
	FC1/CIN2010/103330						
A. CLASSIFICATION OF SUBJECT MATTER H01P 3/10(2006.01)i							
According to International Patent Classification (IPC) or to both national classification and IPC B. FIELDS SEARCHED							
<ul> <li>FIELDS SEARCHED</li> <li>Minimum documentation searched (classification system followed by classification symbols)</li> </ul>							
H01P,; H05K							
Documentation searched other than minimum documentation to the extent that such documents	are included in the fields searched						
Electronic data base consulted during the international search (name of data base and, where pra	acticable, search terms used)						
WPI, EPODOC, CNKI, CNPAT: 电路板, 线路板, 锥形, 锥体, 波导, 电磁波, 电磁表 式, 转换, 转变, 横磁, 横电磁, 激励, 通信, PCB, PWB, printed circuit board, board, e mode, surface, surface wave, electromagnetic suface wave, surface electromagnetic wave, electromagnet+ transverse, transit+, convert+, launch	lectromagnetic wave?, communicat+,						
C. DOCUMENTS CONSIDERED TO BE RELEVANT							
Category* Citation of document, with indication, where appropriate, of the relevant pa	Assages Relevant to claim No.						
A WO 2018129156 A1 (INTEL CORP.) 12 July 2018 (2018-07-12) description, paragraphs [0016]-[0022], claims 1-9 and 17-20, and figures 1-5	1-9						
A US 8497749 B2 (CORRIDOR SYSTEMS, INC.) 30 July 2013 (2013-07-30) entire document	1-9						
A US 2852753 A (INT. STANDARD ELECTRIC CORP.) 16 September 1958 (195 entire document	1-9						
A CN 104124211 A (INFINEON TECHNOLOGIES AG) 29 October 2014 (2014-1 entire document	.0-29) 1-9						
Further documents are listed in the continuation of Box C.	x.						
<ul> <li>* Special categories of cited documents:</li> <li>"A" document defining the general state of the art which is not considered to be of particular relevance</li> </ul>	after the international filing date or priorit ith the application but cited to understand th lying the invention						
<ul> <li>* Special categories of cited documents:</li> <li>*A" document defining the general state of the art which is not considered to be of particular relevance</li> <li>*E" earlier application or patent but published on or after the international filing date</li> <li>*T" later document published date and not in conflict w principle or theory under considered novel or can when the document is taken the document is taken the document is taken to be of particular in the document is taken the document is taken the document in the document is taken the document in the document in the document is taken the document in the document in the document is taken the document in the document in the document is taken the document in the document in the document is taken the document in the document in the document in the document is taken the document in the document in the document in the document is taken the document in the document is taken the document in the docu</li></ul>	after the international filing date or priorit ith the application but cited to understand th lying the invention relevance; the claimed invention cannot b ot be considered to involve an inventive ste can alone						
<ul> <li>* Special categories of cited documents:</li> <li>*A" document defining the general state of the art which is not considered to be of particular relevance</li> <li>*E" earlier application or patent but published on or after the international filing date</li> <li>*U" document which may throw doubts on priority claim(s) or which is special reason (as specified)</li> </ul>	after the international filing date or priorit in the application but cited to understand th lying the invention relevance; the claimed invention cannot b to be considered to involve an inventive ste era alone relevance; the claimed invention cannot b an inventive step when the document i ore other such documents, such combinatio						
<ul> <li>* Special categories of cited documents:</li> <li>*A" document defining the general state of the art which is not considered to be of particular relevance</li> <li>*E" earlier application or patent but published on or after the international filling date</li> <li>*U" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other</li> </ul>	after the international filing date or priorit in the application but cited to understand th lying the invention relevance; the claimed invention cannot b ot be considered to involve an inventive ste can alone relevance; the claimed invention cannot b an inventive step when the document i ore other such documents, such combinatio n skilled in the art						
<ul> <li>* Special categories of cited documents:</li> <li>*A" document defining the general state of the art which is not considered to be of particular relevance</li> <li>*E" earlier application or patent but published on or after the international filing date</li> <li>*U" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)</li> <li>*O" document referring to an oral disclosure, use, exhibition or other means</li> </ul>	after the international filing date or priorit ith the application but cited to understand th lying the invention relevance; the claimed invention cannot b to be considered to involve an inventive ste can alone relevance; the claimed invention cannot b an inventive step when the document i ore other such documents, such combinatio a skilled in the art same patent family						
<ul> <li>* Special categories of cited documents:</li> <li>*A" document defining the general state of the art which is not considered to be of particular relevance</li> <li>*E" earlier application or patent but published on or after the international filing date</li> <li>*U" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other means</li> <li>*O" document referring to an oral disclosure, use, exhibition or other means</li> <li>*O" document published prior to the international filing date but later than the priority date claimed</li> <li>*Date of the actual completion of the international search</li> </ul>	after the international filing date or priorit ith the application but cited to understand th lying the invention relevance; the claimed invention cannot b to be considered to involve an inventive ste can alone relevance; the claimed invention cannot b an inventive step when the document i ore other such documents, such combinatio a skilled in the art same patent family						
<ul> <li>* Special categories of cited documents:</li> <li>*A" document defining the general state of the art which is not considered to be of particular relevance</li> <li>*E" earlier application or patent but published on or after the international filing date</li> <li>*U" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other means</li> <li>*O" document referring to an oral disclosure, use, exhibition or other means</li> <li>*P" document published prior to the international filing date but later than the priority date claimed</li> <li>*Date of the actual completion of the international search</li> </ul>	after the international filing date or priorit in the application but cited to understand th lying the invention relevance; the claimed invention cannot b ot be considered to involve an inventive ste cen alone relevance; the claimed invention cannot b an inventive step when the document i ore other such documents, such combinatio a skilled in the art same patent family						
*       Special categories of cited documents:       "T"       Inter document published to in conflict we principle or theory under to be of particular relevance         "A"       document defining the general state of the art which is not considered to be of particular relevance       "T"       Inter document published on or after the international filing date         "E"       earlier application or patent but published on or after the international filing date       "T"       document of particular considered novel or cannow when the document is tak or priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)       "Y"       document of particular considered novel or cannow when the document is tak or means         "O"       document referring to an oral disclosure, use, exhibition or other means       "W"       document member of the international filing date but later than         "P"       document published prior to the international filing date but later than       "E"       Banuary 2019       18         Name and mailing address of the ISA/CN       National Intellectual Property Administration, PRC (ISA/CN)       Authorized officer	after the international filing date or priorit in the application but cited to understand th lying the invention relevance; the claimed invention cannot b to be considered to involve an inventive ste cen alone relevance; the claimed invention cannot b an inventive step when the document i ore other such documents, such combinatio a skilled in the art same patent family						
*       Special categories of cited documents:       "T"       Inter document published on or after the international filing date         "A"       document defining the general state of the art which is not considered to be of particular relevance       "T"       Inter document of particular relevance         "E"       earlier application or patent but published on or after the international filing date       "T"       document of particular relevance         "L"       document which may throw doubts on priority claim(s) or which is special reason (as specified)       "Y"       document of particular to myber or bub is a particular filing date of another citation or other means         "O"       document referring to an oral disclosure, use, exhibition or other means       "W"       document member of the international filing date but later than the priority date claimed         Date of the actual completion of the international search       Date of mailing of the international search       Date of mailing of the international search         Name and mailing address of the ISA/CN       Authorized officer	after the international filing date or priorit in the application but cited to understand th lying the invention relevance; the claimed invention cannot b to be considered to involve an inventive ste cen alone relevance; the claimed invention cannot b an inventive step when the document i ore other such documents, such combinatio a skilled in the art same patent family						

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			AL SEARCH REPOR patent family members	Г	Ir		application No. 7 <b>7/CN2018/105950</b>
	ent document in search report		Publication date (day/month/year)	Pa	tent family meml	ber(s)	Publication date (day/month/year)
WO	2018129156	A1	12 July 2018	US	2018191061	L A1	05 July 2018
US	8497749	B2	30 July 2013	US	2008211727	7 A1	04 September 2008
				US	2009284435		19 November 2009
				US	7567154		28 July 2009
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			*	FR	66330		29 June 1956
				DE	968646	5 C	13 March 1958
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				US	9356332		31 May 2016
				US	2014320231		30 October 2014
				CN	104124211		10 May 2017
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