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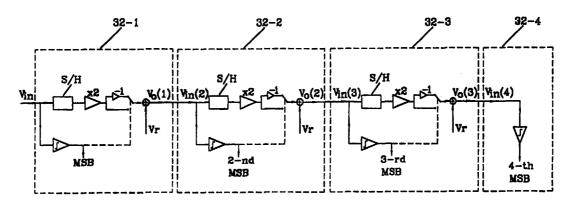
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(54) Title: PIPELINE ANALOG-TO-DIGITAL CONVERSION



(57) Abstract

Pipeline A/D-conversion of an analog input signal is performed according to a new and inventive algorithm which generates a Gray coded digital output signal. A pipeline A/D-converter comprises a number of cascaded stages through which the analog input signal is propagated. Each stage generally generates an output bit of the digital output signal, and furthermore processes the pipeline signal. According to the inventive Gray coding algorithm, the output bit generated in a stage determines whether or not the pipeline signal of that stage is inverted. In a pipeling A/D-converter based on the Gray coding algorithm according to the invention, the accumulation of offset errors will generally be very low. Furthermore, the fact that the signal inversion is digitally controlled enables high precision implementations which further improve the performance of the inventive pipeline A/D-converter. In another embodiment of the invention, the Gray coding algorithm is modified to form a second algorithm which makes low device count implementations possible.

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INTERNATIONAL SEARCH REPORT

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A. CLASS	IFICATION OF SUBJECT MATTER		
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IPC6: H			
Documentati	on searched other than minimum documentation to the e	extent that such documents are included in	the fields searched
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C. DOCU	MENTS CONSIDERED TO BE RELEVANT		
Category*	Citation of document, with indication, where appr	ropriate, of the relevant passages	Relevant to claim No.
Х	US 4599602 A (AKIRA MATZUZAWA ET (08.07.86), column 1, line 1 figures 5,9a,11		1-2,5-7, 10-11
x	 US 5550492 A (FRANK MURDEN), 27.7 (27.08.96), column 1, line 1 figures 1,2		1-2,5-7, 10-11
x	 US 4931797 A (MINORU KAGAWA ET A (05.06.90), column 1, line 1 figures 1,9		1-2,5-7, 10-11
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