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(72) Inventors:
• **Tsai, Yung-Yu**
350 Miao-Li County (TW)
• **Hsieh, Ming-Cheng**
350 Miao-Li County (TW)

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(74) Representative: **2K Patentanwälte Blasberg
Kewitz & Reichel
Partnerschaft
Schumannstrasse 27
60325 Frankfurt am Main (DE)**

(71) Applicant: **Chimei InnoLux Corporation
Chu-Nan 350
T'ai pei (TW)**

(54) **Adaptive timing controller and driving method thereof**

(57) An adaptive timing controller (320), including a receiver (322), a frame buffer (324), a data format check circuit (326) and a data converting unit (328), is provided. The receiver (322) is used for receiving image data of an image frame. The frame buffer (324) is used for tempo-

rarily storing the image data. The data format check circuit (326) is used for detecting display types of the image data and a 3D display panel. The data converting unit (328) is used for rearranging the image data into rearranged image data corresponding to the display type of the 3D display panel.

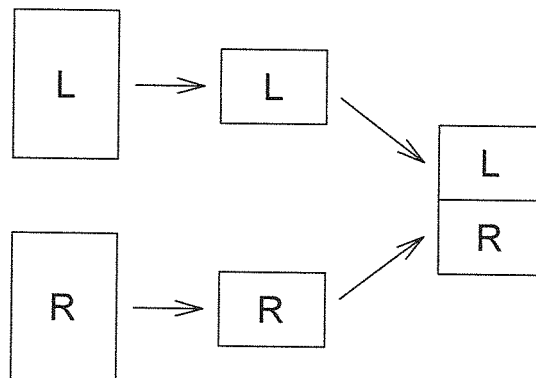


FIG. 6



EUROPEAN SEARCH REPORT

Application Number
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The present search report has been drawn up for all claims			TECHNICAL FIELDS SEARCHED (IPC) G09G H04N
Place of search The Hague		Date of completion of the search 25 October 2013	Examiner Bouffier, Alexandre
CATEGORY OF CITED DOCUMENTS X : particularly relevant if taken alone Y : particularly relevant if combined with another document of the same category A : technological background O : non-written disclosure P : intermediate document		T : theory or principle underlying the invention E : earlier patent document, but published on, or after the filing date D : document cited in the application L : document cited for other reasons & : member of the same patent family, corresponding document	

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