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(54) Title: WAFER TEMPERATURE MEASUREMENT TOOL

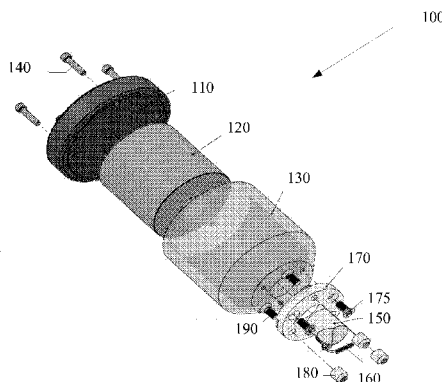
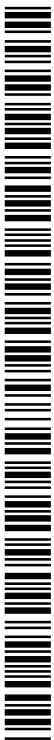


Figure 1

(57) Abstract: A wafer temperature measurement tool for measuring the surface temperature of a semiconductor wafer. The tool can be used to measure temperature on different parts of the wafer to provide a high resolution temperature distribution map. The tool includes an internal calibrated weight that is slidably disposed within a tool body. A temperature sensor is attached to the bottom of the weight. Ceramic stands are attached to the bottom of the tool body. Gravity pulls down on the weight such that the temperature sensor contacts the wafer when the ceramic stands of the tool body are placed on the wafer.



## WAFER TEMPERATURE MEASUREMENT TOOL

### BACKGROUND

[0001] The present invention relates generally to mechanism for measuring the temperature of a hot chuck surface.

5 [0002] Semiconductor reliability tests, which are known as wafer level reliability (WLR) tests, are typically carried out at ambient temperatures as high as 350° C. The duration of such WLR tests can range from several minutes to several weeks. When a WLR test is performed on many dice across a wafer, either sequentially or simultaneously, it is important to maintain the entire wafer at a uniform temperature.

10 [0003] In order to achieve and maintain a uniform temperature across the entire wafer, the temperature of the wafer needs to be measured. A cost-efficient, reliable wafer temperature measurement tool is therefore desirable.

### SUMMARY

15 [0004] According to an embodiment, wafer temperature measurement tool is provided. The tool includes a tool body, a calibrated weight, a foot, and a temperature sensor. The calibrated weight is slidably disposed within the tool body. The foot is attached to the bottom end of the calibrated weight and protrudes from the bottom end of the tool body. The temperature sensor extends from the bottom surface of the foot.

20 [0005] According to another embodiment, a method is provided for measuring wafer surface temperature. A temperature measurement tool is placed on the wafer. The tool has a calibrated weight slidably disposed within a tool body and a ceramic foot attached to a bottom end of the calibrated weight. The temperature sensor extends from a bottom surface of the ceramic foot. Gravity is then allowed to pull down the ceramic foot such  
25 that the temperature sensor thermally contacts the wafer. A temperature reading is acquired from the temperature sensor.

[0006] According to yet another embodiment, a wafer temperature measurement tool is provided. The wafer temperature measurement tool includes a cylindrical tool body, a calibrated weight, ceramic stands, and a temperature sensor. The calibrated weight is  
30 slidably disposed within the tool body. The ceramic stands extend from a bottom end of the tool body. The temperature sensor is attached to a ceramic foot on a bottom end of

the calibrated weight, and the ceramic foot and temperature sensor are external to the tool body.

### **BRIEF DESCRIPTION OF THE DRAWINGS**

5 [0007] The invention, together with further objects and advantages thereof, may best be understood by reference to the following description taken in conjunction with the accompanying drawings in which:

[0008] Fig. 1 is an exploded perspective view of a wafer surface temperature measurement tool in accordance with an embodiment;

10 [0009] Fig. 2A is a side cross-sectional view of an embodiment of the wafer surface temperature measurement tool showing the internal calibrated weight within the tool body;

[0010] Fig. 2B is a side view of the tool shown in Fig. 2A;

[0011] FIG. 3A is a perspective view of an embodiment of the internal calibrated weight with the ceramic foot attached;

15 [0012] Fig. 3B is a perspective view of the internal calibrated weight of Fig. 3A without the ceramic foot attached.

[0013] FIG. 4A is a bottom view of an embodiment of the wafer surface temperature measurement tool;

20 [0014] Fig. 4B is a perspective view of an embodiment of the assembled wafer surface temperature measurement tool; and

[0015] FIG. 5 is a flow chart of a method of temperature calibration performed by using the wafer surface temperature measurement tool.

25 [0016] It is to be understood that, in the drawings, like reference numerals designate like structural elements. Also, it is understood that the depictions in the figures are diagrammatic and not to scale.

### DETAILED DESCRIPTION OF EMBODIMENTS

[0017] The present invention relates generally to measuring the temperature across the surface of a semiconductor wafer. As noted above, WLR tests require a wafer to be maintained at a uniform temperature. The embodiments herein describe a wafer surface temperature measurement tool and method for measuring the temperature across a wafer. The tool provides a repeatable, reliable, accurate, and practical temperature calibration method, which is a key element in achieving uniform wafer temperature throughout a WLR test.

10 [0018] The wafer surface temperature measurement tool can be placed on the surface of a heated wafer to accurately acquire the surface temperature of the wafer. The temperature sensor of the tool is relatively small in size, so its contact area with the wafer is minimal. The small contact area allows repeated placement on the wafer to provide high areal resolution, capable of yielding a detailed wafer temperature distribution map.

15 [0019] WLR tests at elevated temperatures (e.g., 25 °C - 300 °C ) are typically performed on a specially designed metal chuck, usually known as a “hot chuck.” The hot chuck is heated by a dedicated control system, which controls the temperature at the top surface of the chuck. The backside of the wafer to be tested is placed over the top surface of the chuck, using small vacuum holes in the chuck to help achieve good physical and thermal contact between the wafer and the chuck. However, the controlled temperature at the top surface of the chuck differs from the actual temperature of the tested devices, which are located at the top surface of the wafer.

20 [0020] In principle, every die (site) across the tested wafer could have a dedicated temperature sensor. If the readings of a number of such temperature sensors covering the entire area of the wafer, are used in conjunction with the dedicated control system, the temperature across the tested devices at the top surface of the wafer could be measured and controlled, rather than at the top surface of the chuck. This approach using multiple temperature sensors across the entire area of the wafer, while theoretically viable, is impractical, because each and every tested wafer would have to be integrated both physically and electronically with a system, including the chuck, multiple temperature sensors, and a dedicated control system.

30 [0021] According to an embodiment described herein, a calibration step takes place prior to the actual wafer level reliability test. During the calibration step, the

temperatures across the top surface of the wafer are obtained as a respective array of offset numbers. After calibration, the set chuck temperature is adjusted accordingly, such that the resulting temperature of each tested device is closer to the required value, while the remaining temperature deviation is known and can be taken into account.

5 [0022] There are two common methods of obtaining the surface temperatures across a wafer: (1) using a commercially made silicon calibration wafer; and (2) using a small high-accuracy temperature sensor. When a commercially made silicon calibration wafer is used, multiple high-accuracy temperature sensors, which are embedded in the wafer, are connected via wires to a meter in order to provide a map of the respective readings  
10 across the wafer. This method yields accurate and reliable results. However, the calibration wafer is very expensive and wafers that differ from the calibration wafer (e.g. substrate-doping, thickness) are likely to have a different temperature offset between the hot-chuck surface and their surfaces.

[0023] When a small, high-accuracy temperature sensor is used, it is applied  
15 manually to the wafer by a low thermal conductance tool (e.g., a cotton swab) and moved across the wafer to provide a map of the temperature across the wafer. However, this method is inherently inconsistent because the normal force applied manually to the sensor varies during measurements and among sites across the wafer. The measured temperature is fairly sensitive to this variability, possibly by more than 1.0 °C.

20 [0024] The embodiments described herein alleviate the variability of the method using the manually applied temperature sensor described above, by using a constant, calibrated force each time. The resulting low-cost tool is both simple and easy to-use; allowing accurate, consistent, and reliable temperature measurements across a wafer. As a result, it provides an effective temperature calibration prior to most wafer level  
25 reliability tests at elevated temperatures.

[0025] Fig. 1 is an exploded perspective view of an embodiment of a wafer surface temperature measurement tool 100. According to the embodiment illustrated in Fig. 1, the tool 100 is substantially cylindrical, with a slightly wider cap 110 and a cylindrical tool body 130 with a tapered portion. The cap 110 serves as a handle for the user and is  
30 attached to the top of the tool body 130, as shown in Fig. 1. As shown in Fig. 1, the cap 110 is attached to the top of the tool body 130 by three screws 140 and corresponding threaded holes through the cap 110 and the top surface of the tool body 130. According to

an embodiment, the cap 110 can be formed of plastic. In other embodiments, the cap can be formed of other materials, such as, for example, fiberglass, cork, wood, or rubber.

[0026] The tool body 130 can be formed of stainless steel. As shown in Fig. 1, the tool body 130 has an inner cylindrical cavity. A stainless steel calibrated weight 120 is  
5 slidably disposed within the cavity. As shown in Fig. 1, the calibrated weight 120 fits relatively loosely within the cavity such that the weight 120 can slide within the cavity by the force of gravity. In other embodiments, the weight can be formed of any suitably  
10 dense material and the tool body can be formed of any suitably rigid material. Fig. 2A is a side cross-sectional view along the main axis of the tool 100 showing the internal calibrated weight within the tool body 130, and Fig. 2B is a side view of the tool 100. In this embodiment, the internal calibrated weight 120 is formed from a single, integral piece of stainless steel that is machined to form three cylindrical sections along the same primary axis. In a particular embodiment, the weight of the calibrated weight 120 is about 1.25lbs.

[0027] The weight 120 includes a top cylindrical section 120A (which is the largest and heaviest section), a smaller cylindrical mid-section 120B, and a bottom section 120C. As shown in Fig. 2A, the bottom section 120C is a threaded stud for fitting into a threaded orifice through the top surface of a ceramic foot 150 (see Figs. 1, 3). The larger  
20 top section 120A of the internal calibrated weight 120 is made slightly shorter than the corresponding cavity in the tool body 130, so that the entire internal calibrated weight 120 can slide within the cavity. The weight 120 is pulled down by gravity when the tool 100 is held normally (i.e., right side up with the top plastic cap 110 up) or the opposite way when the tool 100 is held upside down. The ceramic foot 150 is attached to the bottom surface of the smaller mid-section 120B of the internal calibrated weight 120 by the  
25 threaded stud 120C, which is screwed into the threaded orifice in the ceramic foot 150. The ceramic foot can be angled, as shown in Fig. 3A to allow routing of the electric wires 165 such that the wires 165 can suspend freely without being hampered by any part of the tool. Furthermore, the angling of the foot 150 allows for a smaller footprint.

[0028] In the illustrated embodiment, a small, thin, and rectangular resistance  
30 temperature device (RTD) or temperature sensor 160 is provided at the bottom of the tool 100 to measure the temperature of the wafer. The temperature sensor 160 is connected via electrical wires to a computer for displaying and/or recording the sensed temperature(s) of the wafer. As shown in Fig. 2A, the temperature sensor 160 is held by

the electric wires 165, which are loosely passed through a small opening in the ceramic plate 170. In the embodiment illustrated in Figs. 2A and 2B, the electric wires 165 extend out of a small opening in the side of the ceramic plate 170 to a computer. The ceramic plate 170 also has a large central hole through which the ceramic foot 150 is screwed to the threaded stud 120C of the internal calibrated weight 120. Fig. 3A is a perspective view of the internal calibrated weight 120 with the ceramic foot 150 attached. Fig. 3B shows the internal calibrated weight 120 without the ceramic foot 150 attached. In this embodiment, the connection of the ceramic foot 150 to the threaded stud 120C of the calibrated weight 120 as well as the screws 175 hold the ceramic plate 170 in place against the tool body 130. The screws 175 are used to attach the plate 170 to the tool body 130. In other embodiments, the temperature sensor can have a different shape. In other embodiments the sensor can be a thermocouple or thermistor.

[0029] When the tool 100 is held upside down, the temperature sensor 160 is suspended a few millimeters above the surface of the ceramic foot 150. When the tool 100 is held normally (oriented right side up), the calibrated weight 120 slides downward in the tool body 130 and the attached ceramic foot 150 also slides down enough to push the temperature sensor 160 against the wafer surface while pulling the temperature sensor's electric wires a bit. It will be appreciated that the opening in the ceramic plate 170 through which the electric wires 165 are passed acts as an effective strain relief.

[0030] Fig. 4A shows a bottom view and Fig. 4B shows a tilted perspective view of the assembled tool 100. In order to assure stable positioning of the internal calibrated weight 120 on the wafer, three ceramic stands 180 are provided on the ceramic plate 170 in the illustrated embodiment, as shown in Figs. 4A and 4B. The low thermal conductivity of the ceramic material and small contact area of the stands 180 formed with the underlying wafer minimize any effect on the actual temperature to be measured by the temperature sensor 160.

[0031] In the illustrated embodiment, three threaded studs 190 extend through holes in the ceramic plate 170, fitting into threaded orifices in the ceramic stands 180 (Fig. 1). In this embodiment, three screws 175 extend through another set of holes in the ceramic foot 150, and attach the ceramic plate 170 to the bottom surface of the tool body 130, using fitting threaded orifices, respectively. In other embodiments, the stands 180 (as well as the foot 150 and plate 170) can be formed of non-conductive materials, such as, for example, mica, glass, quartz, or stone.

[0032] Fig. 5 is a flow chart of a method 500 of temperature calibration performed by using the wafer surface temperature measurement tool described herein. In 510, the temperature measurement tool is placed on a selected area on the surface of a wafer with ceramic stands contacting the wafer. The tool has a ceramic foot at its bottom with a temperature sensor extending from the bottom surface of the ceramic foot. The ceramic foot is attached to the bottom end of a calibrated weight that is slidably disposed within the body of the tool.

[0033] In 520, gravity is allowed to pull down the ceramic foot such that the temperature sensor forms a good thermal contact with the wafer. As gravity pulls down the ceramic foot when the tool is placed on the wafer, the temperature sensor is subjected to a force equal to the weight of the calibrated stainless steel weight internal to the tool body and applied from above by the ceramic foot, while the respective reaction force (same strength but in the opposite direction) pushes its bottom surface up. As a result, the temperature sensor is always maintained by the same forces, using an optimal weight of the internal calibrated stainless steel weight. Thus, when the tool is moved from location to location on the wafer, the same force is applied to the temperature sensor each time because gravity is pulling on the calibrated weight and therefore subjecting the temperature sensor to the weight of the calibrated stainless steel weight rather than a manual force, which can be different each time.

[0034] In 530, the temperature sensor reading is allowed to stabilize. Once the temperature reading has stabilized, in 540, the temperature sensor reading is acquired. The acquisition of the temperature reading can be performed by a device, such as a computer. Such a device can be electrically connected to the temperature sensor. In an embodiment, they are electrically connected by electrical wires. As described above, the electrical wires can pass through a portion of the tool, such as the ceramic plate. In other embodiments, the temperature sensor and the computer can be connected wirelessly.

[0035] After the temperature reading is acquired, the tool can be moved to another position on the wafer in 550 to acquire the temperature reading at another location on the wafer. Steps 510-550 can be repeated to acquire a temperature map of a wafer. Using the tool described herein, method 500 can generate an accurate, consistent, and reliable areal temperature map of the wafer.

[0036] Although only a few embodiments of the invention have been described in detail, it should be appreciated that the invention may be implemented in many other



forms without departing from the scope of the invention. It should be apparent that the described wafer temperature measurement tool can be used in a wide variety of applications. In view of all of the foregoing, it should be apparent that the present embodiments are illustrative and not restrictive and the invention is not limited to the  
5 details given herein, but may be modified within the scope and equivalents of the appended claims.

### CLAIMS

What is claimed is:

1. A wafer temperature measurement tool, comprising:  
5 a tool body;  
a calibrated weight slidably disposed within the tool body;  
a foot attached to a bottom end of the calibrated weight and protruding from a bottom end of the tool body; and  
a temperature sensor extending from a bottom surface of the foot.
- 10 2. The wafer temperature measurement tool of claim 1, wherein the calibrated weight is formed of stainless steel.
3. The wafer temperature measurement tool of claim 1 or 2, wherein the foot protrudes from the tool body and through a plate.
4. The wafer temperature measurement tool of claim 3, wherein a plurality of stands  
15 are attached to the plate.
5. The wafer temperature measurement tool of claim 4, wherein the foot, plate, and stands are ceramic.
6. The wafer temperature measurement tool of any of the preceding claims, wherein the tool body is cylindrical.
- 20 7. The wafer temperature measurement tool of claim 1, wherein the temperature sensor is operably connected to a computer.
8. A method of measuring wafer surface temperature, comprising:  
placing a temperature measurement tool on the wafer, wherein the tool has a calibrated weight slidably disposed within a tool body and a ceramic foot attached to a  
25 bottom end of the calibrated weight, and wherein a temperature sensor extends from a bottom surface of the ceramic foot;  
allowing gravity to pull down the ceramic foot such that the temperature sensor thermally contacts the wafer; and  
acquiring a temperature reading from the temperature sensor.
- 30 9. The method of claim 8, further comprising moving the temperature measurement tool to a different location on the wafer after acquiring the temperature reading.
10. The method of claim 8, wherein acquiring the temperature reading is performed by a computer.

11. The method of claim 10, wherein the temperature sensor and the computer are operably connected.
12. The method of any of claims 8-11, wherein the temperature reading is allowed to stabilize before acquiring the temperature reading.
- 5 13. The method of any of claims 8-11, wherein only stands on the bottom of the temperature measurement tool contact the wafer when the temperature measurement tool is placed on the wafer.
14. A wafer temperature measurement tool, comprising:  
a cylindrical tool body;  
10 a calibrated weight slidably disposed within the tool body;  
ceramic stands extending from a bottom end of the tool body; and  
a temperature sensor attached to a ceramic foot on a bottom end of the calibrated weight, wherein the ceramic foot and temperature sensor are external to the tool body.
- 15 15. The tool of claim 14, wherein the tool is configured such that when the tool is placed on a wafer, the ceramic stands contact the wafer and gravity pulls down the calibrated weight within the tool body such that the temperature sensor contacts the wafer.
16. The tool of claim 14 or 15, wherein the calibrated weight is formed of stainless steel.
- 20 17. The tool of any of claims 14-16, wherein the temperature sensor is operably connected to a computer.
18. The tool of claim 17, wherein the temperature sensor is wirelessly connected to the computer.
19. The tool of claim 17, wherein electrical wires connect the temperature sensor to  
25 the computer.

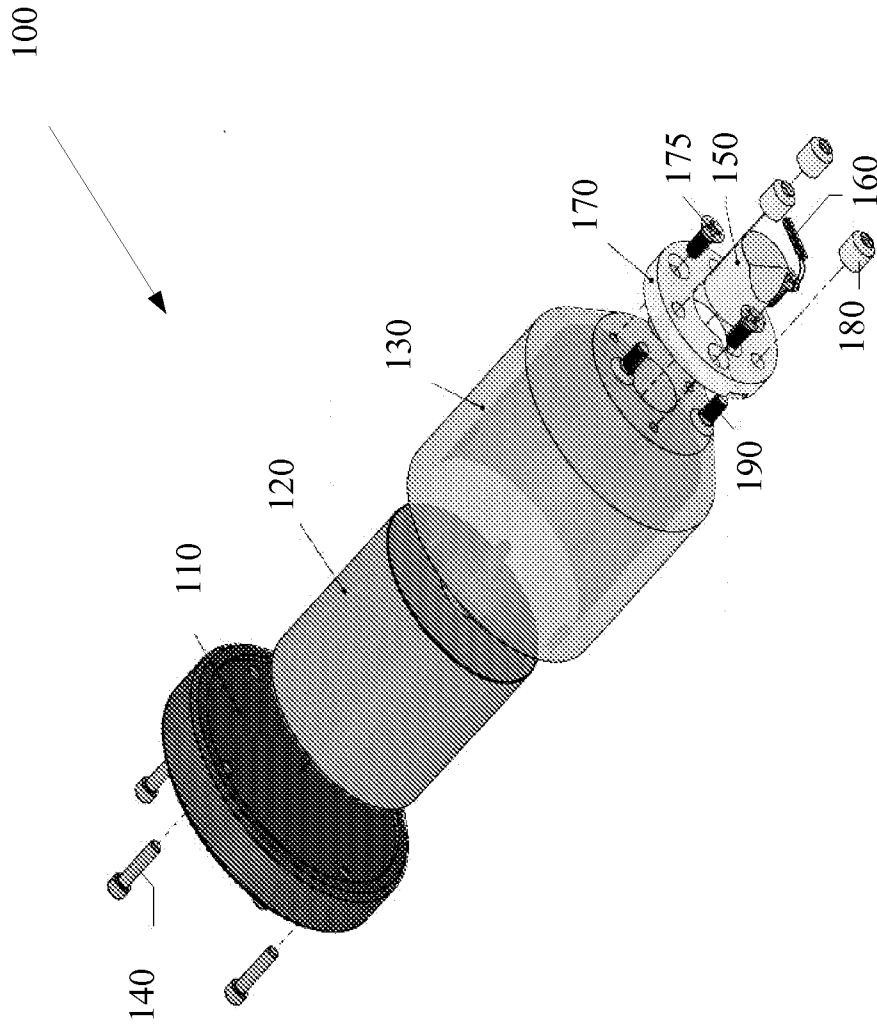


Figure 1

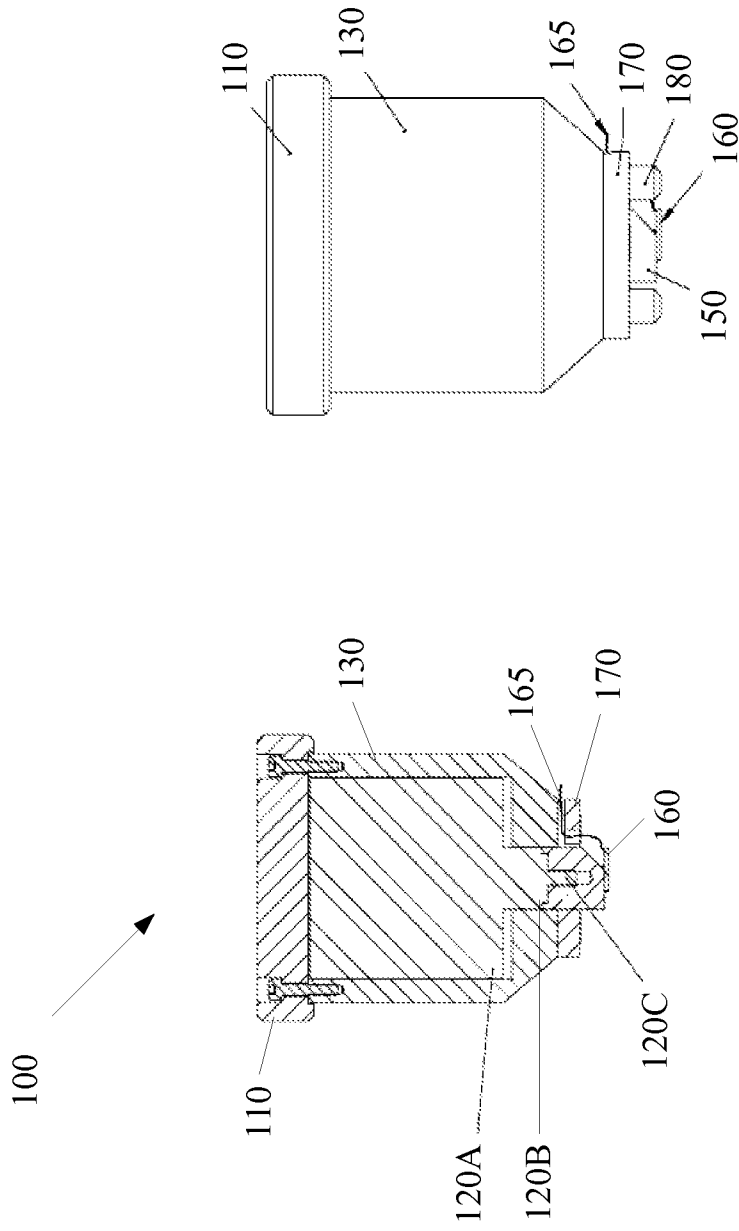


Figure 2B

Figure 2A

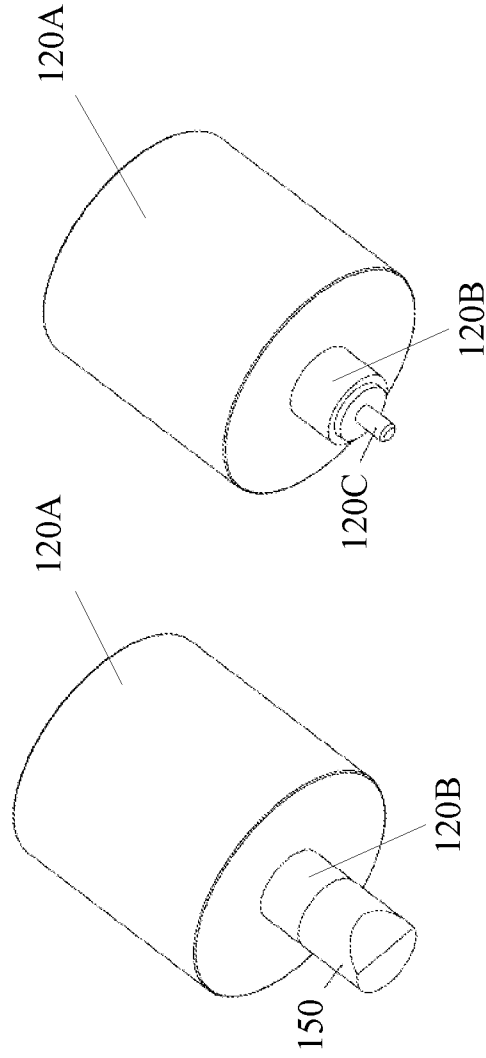


Figure 3B

Figure 3A

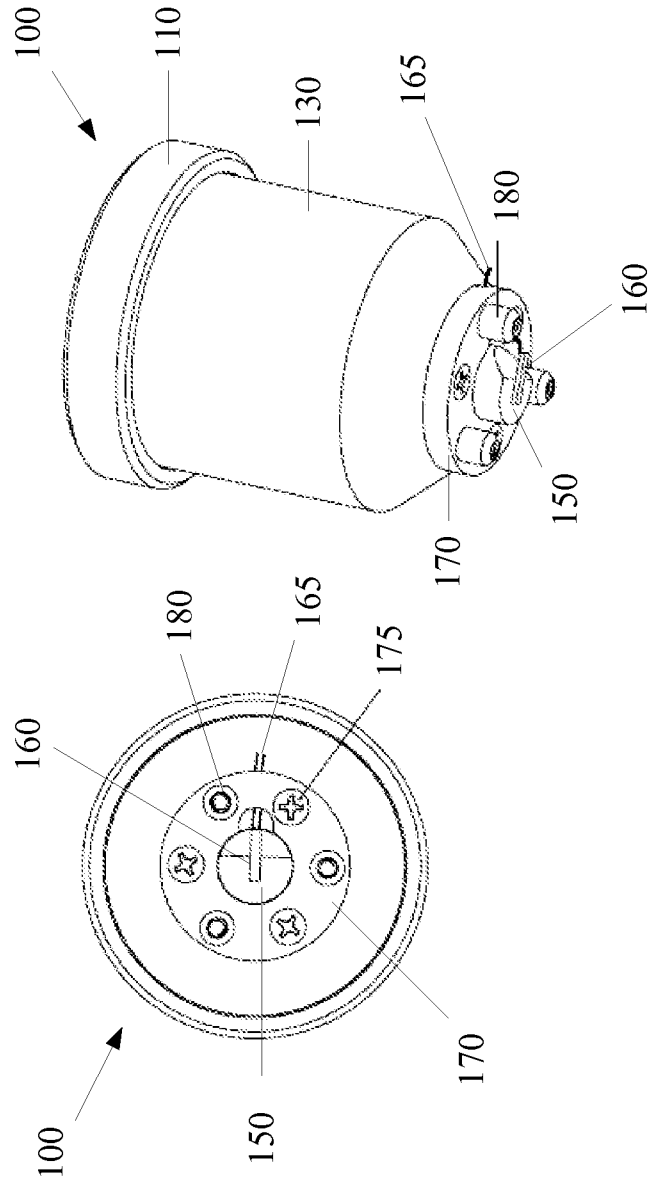


Figure 4B

Figure 4A

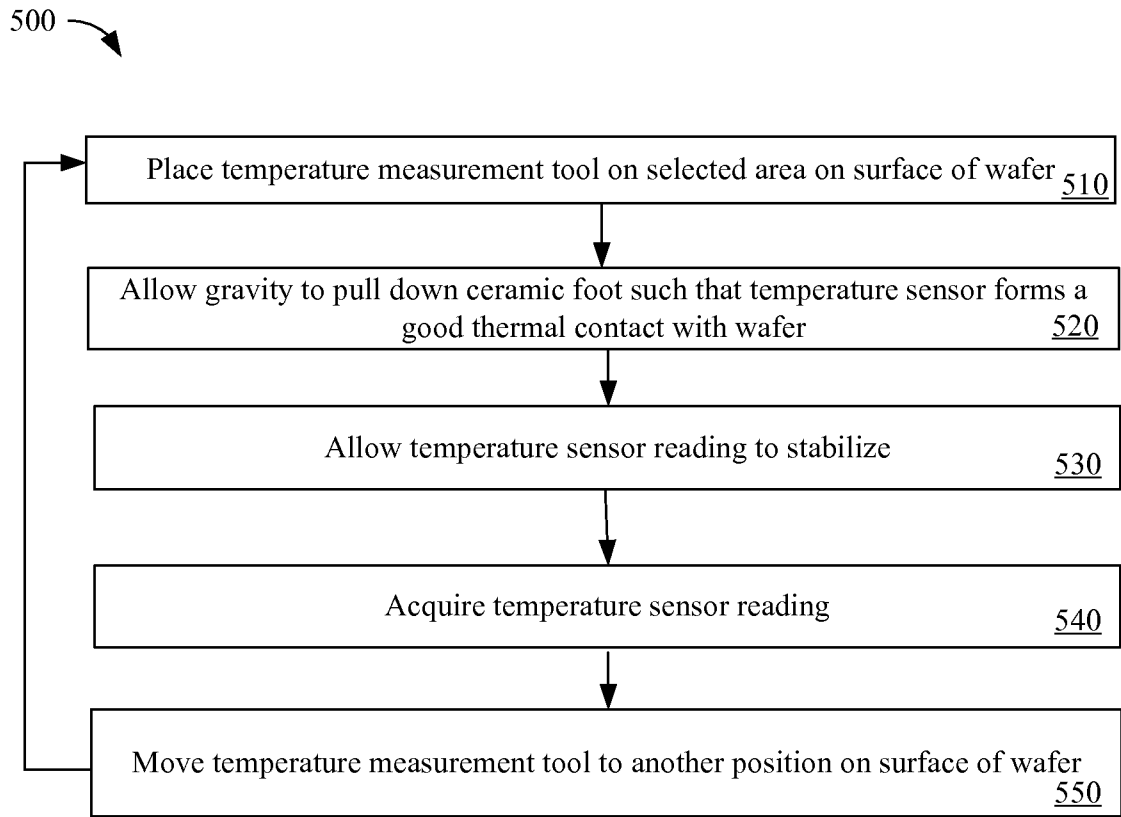


Figure 5



INTERNATIONAL SEARCH REPORT

International application No  
PCT/US2014/018070

A. CLASSIFICATION OF SUBJECT MATTER  
INV. G01K1/14  
ADD.  
  
According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED  
Minimum documentation searched (classification system followed by classification symbols)  
G01K H01L

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)  
EPO-Internal, WPI Data

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	WO 97/03342 A1 (CVC PRODUCTS INC [US]) 30 January 1997 (1997-01-30) the whole document	1-19
A	US 5 446 824 A (MOSLEHI MEHRDAD M [US]) 29 August 1995 (1995-08-29) figures 6a-6b	1-19
A	JP 2004 022803 A (TAIHEIYO CEMENT CORP) 22 January 2004 (2004-01-22) the whole document	1-19
A	US 6 572 265 B1 (GOTTHOLD JOHN P [US] ET AL) 3 June 2003 (2003-06-03) the whole document	1-19
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Further documents are listed in the continuation of Box C.

See patent family annex.

\* Special categories of cited documents :

<p>"A" document defining the general state of the art which is not considered to be of particular relevance</p> <p>"E" earlier application or patent but published on or after the international filing date</p> <p>"L" document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)</p> <p>"O" document referring to an oral disclosure, use, exhibition or other means</p> <p>"P" document published prior to the international filing date but later than the priority date claimed</p>	<p>"T" later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention</p> <p>"X" document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone</p> <p>"Y" document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art</p> <p>"&amp;" document member of the same patent family</p>
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Date of the actual completion of the international search  18 July 2014	Date of mailing of the international search report  25/07/2014
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Name and mailing address of the ISA/ European Patent Office, P.B. 5818 Patentlaan 2 NL - 2280 HV Rijswijk Tel. (+31-70) 340-2040, Fax: (+31-70) 340-3016	Authorized officer  Rosello Garcia, M
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## INTERNATIONAL SEARCH REPORT

International application No  
PCT/US2014/018070

C(Continuation). DOCUMENTS CONSIDERED TO BE RELEVANT		
Category*	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
A	US 5 436 494 A (MOSLEHI MEHRDAD M [US]) 25 July 1995 (1995-07-25) the whole document	1-19
A	----- JP 2001 085488 A (BRIDGESTONE CORP) 30 March 2001 (2001-03-30) the whole document -----	1-19

# INTERNATIONAL SEARCH REPORT

Information on patent family members

International application No PCT/US2014/018070
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Patent document cited in search report	Publication date	Patent family member(s)	Publication date
WO 9703342	A1	30-01-1997	DE 19681502 T1 11-03-1999
			US 6004029 A 21-12-1999
			US 6106148 A 22-08-2000
			WO 9703342 A1 30-01-1997
-----			
US 5446824	A	29-08-1995	NONE
-----			
JP 2004022803	A	22-01-2004	NONE
-----			
US 6572265	B1	03-06-2003	US 6572265 B1 03-06-2003
			US 2003206573 A1 06-11-2003
-----			
US 5436494	A	25-07-1995	JP H07311095 A 28-11-1995
			US 5436494 A 25-07-1995
-----			
JP 2001085488	A	30-03-2001	NONE
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